

Diego Gonzalez

Title: BATTERY POWER DELIVERY MODULE

Abstract: Source: US2008136367A A system and method for digital management and control of power conversion from battery cells. The system utilizes a power management and conversion module that uses a CPU to maintain a high power conversion efficiency over a wide range of loads and to manage charge and discharge operation of the battery cells. The power management and conversion module includes the CPU, a current sense unit, a charge/discharge unit, a DC-to-DC conversion unit, a battery protection unit, a fuel gauge and an internal DC regulation unit. Through intelligent power conversion and charge/discharge operations, a given battery type is given the ability to emulate other battery types by conversion of the output voltage of the battery and adaptation of the charging scheme to suit the battery.

Classifications:

International (IPC 8-9): F24J2/38 G01D4/00 G01R17/02 G01R19/00 G01R19/165 G01R22/06 G01R27/08 G01R27/26 G01R27/28 G01R31/28 G01R31/40 G01S3/786 G03B23/02 G05B23/02 G05F1/40 G05F1/46 G05F1/613 G05F1/67 G05F1/70 G05F5/00 G06F15/00 G06F19/00 G08C15/00 H01H37/00 H01L31/00 H01L31/02 H01L31/04 H01L31/042 H01M10/44 H01R13/514 H02H3/00 H02H3/087 H02H7/122 H02J1/00 H02J1/10 H02J1/12 H02J13/00 H02J3/00 H02J3/02 H02J3/18 H02J3/38 H02J7/00 H02J7/35 H02M1/00 H02M1/32 H02M1/36 H02M3/04 H02M3/155 H02M3/156 H02M3/158 H02M7/00 H02M7/42 H02M7/44 H02M7/48 H02M7/537 H02S40/34 H05K7/10 (Advanced/Invention); G08C15/00 H01L29/66 H02H7/10 H02J1/12 H02J3/38 H02J7/35 H02M3/158 (Advanced/Non-invention); F24J2/38 G01R19/00 G01R19/165 G01R31/28 G01R31/40 G01S3/78 G05F1/66 G05F5/00 G06F15/00 G08C15/00 H01L31/00 H01L31/04 H01L31/042 H01M10/42 H02H3/087 H02H7/122 H02J1/00 H02J1/10 H02J13/00 H02J3/00 H02J3/38 H02J7/00 H02J7/35 H02M3/04 H02M7/00 H02M7/42 H02M7/44 H05K7/10 (Core/Invention); G08C15/00 H02M3/04 (Core/Non-invention)

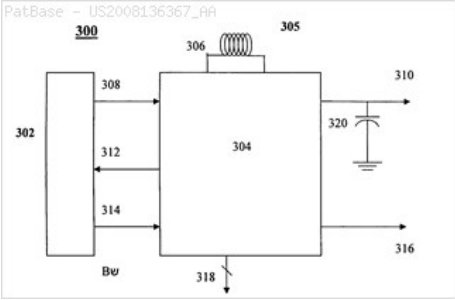
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European: G01D4/00R1 G01R31/26A3 G01S3/786B H01L31/02H2B H02J1/10C H02J1/12 H02J3/38 H02J3/38D1 H02J3/38D1S H02J3/38D1S2 H02J3/38D1W H02J3/46 H02J7/00K H02M3/156 H02M3/158P H02M7/493 H05K7/14F7D S01R31/40 S01R31/40B T02J3/38 T02J3/38D1 T02J3/38D1S2 T02J7/35 T02M1/00P1 T02M1/00P3 T02M3/158B Y02B10/14 Y02B70/32B Y02B70/34B Y02B90/24B2 Y02B90/24D Y02B90/24J Y02E10/50 Y02E10/52 Y02E10/56B Y02E10/58 Y02E60/12 Y04S20/32B Y04S20/34 Y04S20/40 Y04S20/52

US: 136/244 136/259 257/140 257/491 257/499 29/825 30/80 307/112 307/117 307/2 307/43 307/43P 307/46 307/52 307/52P 307/56 307/58 307/63 307/63P 307/64 307/66 307/77 307/77P 307/80 307/80P 307/82 307/82P 307/82S 307/85 307/86 307/87 307/97 320/103 320/103P 320/107 320/112 320/132 320/132S 320/134 320/134S 320/135 320/135S 320/162 323/225 323/265 323/271 323/282 323/282S 323/283 323/285 323/299 323/299S 323/304 323/318 323/318P 323/344 324/537 324/537P 324/654 324/654P 324/705 324/713 324/713P 324/713S 324/761.01 324/761.010S 361/730 361/87 363/46 363/49 363/50 363/55 363/55P 363/56.03 363/56.05 363/71 363/71P 363/78 363/78S 363/95 363/95S 439/540.1 700/291 702/182 702/188 702/188S 702/58 702/58P 702/60 702/61 702/64 702/64P

Family:

Publication number	Publication date	Application number	Application date
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Probable Assignee: SOLAREEDGE TECHNOLOGIES LTD					
Assignee(s): (std): ADEST MEIR ; BINDER YARON ; FISHELOV AMIR ; GALIN YOAV ; GAZIT MEIR ; SELLA GUY ; HANDELSMAN LIOR ; SOLAREEDGE LTD ; SOLAREEDGE TECHNOLOGIES ; GLOVINSKI TZACHI ; GLOVINSKY TZACHI ; RADIMOV NIKOLAY ; SOLAREEDGE TECHNOLGIES LTD ; SOLAREEDGE TECHNOLOGIES LTD ; YOSKOVITCH ILAN ; SOLAREEDGE TECHNOLOGIES INC ; YOSCOVICH ILAN					
Assignee(s): KREOS CAPITAL IV EXPERT FUND LTD					
Inventor(s): (std): ADEST MEIR ; BINDER YARON ; FISHELOV AMIR ; GALIN YOAV ; GAZIT MEIR ; HANDELSMAN LIOR ; RADIMOV NIKOLAY ; SELLA GUY ; YOSCOVICH ILAN ; YOSKOVITCH ILAN ; YOAV GALIN ; MEIR ADEST ; LIOR HANDELSMAN ; GUY SELLA ; AMIR FISHELOV ; YARON BINDER ; MEIR GAZIT ; GLOVINSKY TZACHI ; GLOVINSKI TZACHI					
Agent(s): KIRSCHNER KLAUS DIETER; PUSCHMANN BORCHERT BARDEHLE PATENTANWAELTE PARTNERSCHAFT MBB; JEHAN ROBERT; Jansen Cornelis Marinus; JANSEN CORNELIS MARINUS; SUGHRUE MION PLLC; NIXON PEABODY LLP; EMPK AND SHILOH LLP C O LANDON IP INC; LAW OFFICE OF MICHAEL E KONDOUDIS PC; JOSEPH; LAW OFFICE OF MICHEAL E KONDOUDIS; BANNER AND WITCOFF LTD; EITAN MEHULAL LAW GROUP ET AL; BACH JOSEPH; FARBER ALLAN					
Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PE PG PH PL PT QA RO RS RU RW SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW					

Title: **HIGH VOLTAGE** SENSOR DEVICE AND METHOD THEREFOR

Abstract: Source: US2006163691A In one embodiment, a **high voltage** element is formed overlying a doped semiconductor region that can be depleted during the operation of the **high voltage** element.

Classifications:

International (IPC 8-9): G01R19/00 G01R19/165 G01R31/14 H01L21/02 H01L21/66 H01L21/77 H01L21/82 H01L21/822 H01L21/8222 H01L23/544 H01L27/04 H01L27/088 H01L29/00 H01L29/02 H01L29/72 H03K5/153 (Advanced/Invention); G01R31/14 H01L29/02 (Advanced/Non-invention); G01R19/00 H01L21/02 H01L21/66 H01L29/00 H01L29/66 H03K5/153 (Core/Invention); G01R19/165 H01L27/088 H01L29/00 (Core/Non-invention); H01L (Subclass/Invention)

International (IPC 1-7): G01R19/165

CPC: H02M1/32 H02M1/36 H02M2001/0022 H01L27/0288 H01L23/5228 H01L27/0629 H01L27/0802 H01L28/20 H01L29/0696 H01L29/1045 H01L29/405 H01L29/7835 H03K17/102 H01L2924/0002

European: H01L27/02B4F8 H01L27/06D4V H01L29/10D2B2B H01L29/40P4 H01L29/78F3 H02M1/32 H02M1/36 H03K17/10B T01L27/08B T01L29/06D3B T02M1/00C4V

US: 257/528 257/528P 257/536 257/539 257/539P 257/E21.003 257/E21.004 257/E21.004S 257/E21.531 257/E21.531S 257/E29.054 257/E29.054S 257/E29.173 257/E29.173S 257/E29.268 257/E29.268S 324/522 324/76.11 324/76.110S 438/10 438/17 438/17P 438/329 438/382 438/382P 438/382S 438/5

Family:

Publication number	Publication date	Application number	Application date
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Priority:

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Probable Assignee: SEMICONDUCTOR COMPONENTS IND LLC

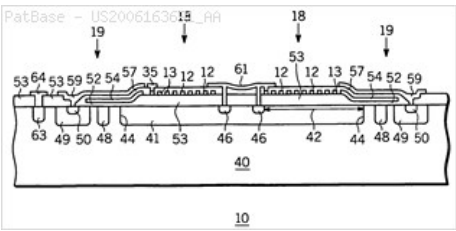
Assignee(s): (std): BURTON RICHARD S ; CHANG GEORGE ; HALL JEFFERSON W ; OIKAWA KAZUNORI ; QUDDUS MOHAMMED T ; SEMICONDUCTOR COMPONENTS IND ; SEMICONDUCTOR COMPONENTS INDUSTRIES L L C ; QUDDUS MOHAMMED TANVIR ; SEMICONDUCTOR COMPONENT IND LL ; SEMICONDUCTOR COMPONENT IND LLC

Assignee(s): SEMICONDUCTOR COMPONENT INDUSTRIES LLC ; SEMICONDUCTOR COMPONENTS INDUSTRIES LLC ; SEMICONDUCTOR COMPONENTS IND LLC ; JPMORGAN CHASE BANK NA ; JPMORGAN CHASE BANK NA COLLATERAL AGENT AS

Inventor(s): (std): BURTON RICHARD S ; CHANG GEORGE ; HALL JEFFERSON W ; HALL JEFFERSON W QUDDUS MOHAMM ; GEORGE CHANG ; GEORGE CHANG G ; JW JEFFERSON W HALL ; KAZUNORI OIKAWA ; MT MOHAMMED TANVIR QUDDUS ; OIKAWA KAZUNORI ; QUDDUS MOHAMMED T ; QUDDUS MOHAMMED TANVIR ; RS RICHARD S BURTON ; TANVIR QUDDUS MOHAMMED ; MOHAMMED TANVIR QUDDUS T ; JEFFERSON W HALL W

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Title: MODULAR HIGH VOLTAGE SENSING UNIT

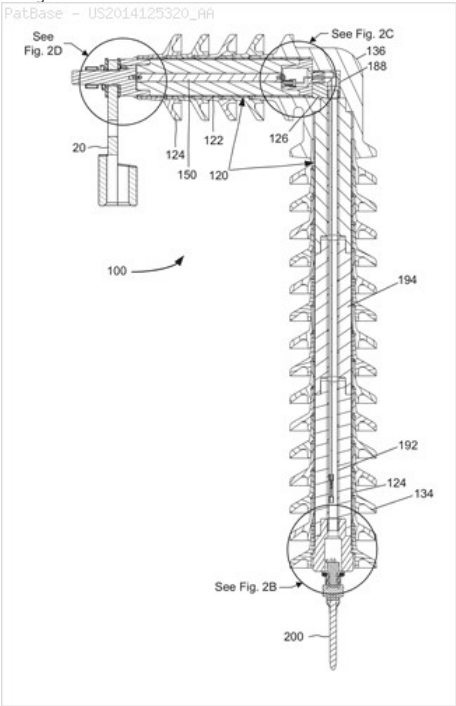
Abstract: Source: US2014125320A A high voltage sensing unit includes a housing having an internal bore. A resistive voltage divider, including a primary resistor and a secondary resistor configured in series, is included within the housing. A connecting assembly is configured to attach the high voltage sensing unit to a terminal of a high voltage switching device. The connecting assembly provides an electrical connection from the high voltage switching device to the primary resistor and physically supports the high voltage sensing unit. An interface cable provides an electrical connection from the secondary resistor to a receptacle on the high voltage switching device, which can receive a voltage signal and pass the voltage signal to a controller using existing control wiring.

Classifications:

International (IPC 8-9): G01R1/20 G01R15/14 G01R19/00 G01R31/36 G05B5/01 H02H1/00 H02H3/02 H02H3/06 (Advanced/Invention); G01R15/04 (Advanced/Non-invention)

CPC: G01R15/04 G01R1/203 G01R15/14

US: 324/126



Family:

Publication number	Publication date	Application number	Application date
BR102013028063 A2	20141029	BR20131028063	20131031
CA2831768 AA	20140502	CA20132831768	20131030
CO7250283 A1	20150430	CO20130258845	20131031
CR20130561 A	20140303	CR20130000561	20131031
EP2728365 A2	20140507	EP20130191303	20131101
MX2013012839 A1	20140521	MX20130012839	20131101
US2014125320 AA	20140508	US20130036752	20130925

Priority:

US20120721549P 20121102 US20130036752 20130925

Probable Assignee: THOMAS AND BETTS INT INC

Assignee(s): (std): THOMAS AND BETTS INT

Assignee(s): THOMAS AND BETTS INT INC ; AYALA WILLIAM ; FONG ROBERT ; GARDNER DANIEL L

Inventor(s): (std): AYALA III WILLIAM ; AYALA WILLIAM ; AYALA WILLIAM III ; FONG ROBERT ; GARDNER DANIEL L ; GARNIER DANIEL L ; WILLIAM AYALA III

Inventor(s): ROBERT FONG ; DANIEL L GARDNER

Agent(s): CARPMAELS AND RANSFORD LLP

Designated states: AL AT BA BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC ME MK MT NL NO PL PT RO RS SE SI SK SM TR

Title: ON CHIP TRANSFORMER ISOLATOR

Abstract: Source: US2005269657A An integrated circuit having voltage isolation capabilities includes a first area of the integrated circuit containing functional circuitry that is located in the substrate of the integrated circuit. A second area of the integrated circuit contains an integrated RF isolation circuitry for voltage isolating the functional circuitry. The RF isolation circuitry is located in the metal layers of the integrated circuit.

Classifications:

International (IPC 8-9): G01R19/00 G08C13/00 H01F19/08 H01L23/48 H01L23/49 H01L23/522 H01L23/66 H01L25/00 H01L31/00 H01P5/02 H02M3/335 H03C3/00 H03H2/00 H03H7/00 H03M9/00 H04B1/16 H04B1/18 H04B1/28 H04B1/38 H04B1/40 H04B1/69 H04B17/00 H04B3/00 H04B3/02 H04B3/28 H04B3/30 H04B3/50 H04J3/04 H04L25/02 H04L7/00 H04L7/04 (Advanced/Invention); H01F17/00 H01F19/08 H01F21/02 H01L23/522 H01L29/207 H01L33/00 H02M1/08 H02M3/28 H04B17/00 (Advanced/Non-invention); G01R19/00 H01F19/00 H01L23/48 H01L23/52 H01L23/58 H01L25/00 H01L31/00 H02M3/24 H03C3/00 H03H2/00 H03M9/00 H04B1/16 H04B1/18 H04B1/28 H04B1/38 H04B1/40 H04B3/00 H04B3/02 H04L25/02 H04L7/00 (Core/Invention); H01F17/00 H01F19/00 H01F21/02 H01L23/52 H01L29/02 H01L33/00 H02M1/08 H02M3/24 (Core/Non-invention)

International (IPC 1-7): H01L31/00 H02M3/335 H04B3/00

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European: G06F13/40E2B G06F13/42P6 H01F19/08 H01L23/48 H01L23/522L H01L23/64C H01L23/66 H01L24/05 H01L24/06 H01L24/48 H01L24/49 H01L25/065N H03K5/135 H03M9/00 H04B3/02 H04B3/30 H04L25/02K1 H04L25/02K1A H04L25/02K3 H04L5/14 T01F17/00A T01L223/66A1 T01L224/04010 T01L224/04042 T01L224/05599 T01L224/45099 T01L224/48091 T01L224/48095 T01L224/48137 T01L224/48237 T01L224/48247 T01L224/48464 T01L224/48470 T01L224/48472 T01L224/49171 T01L224/49175 T01L224/49176 T01L224/85399 T01L225/065510 T01L225/065532 T01L23/495A T01L23/495C2 T01L23/495L T01L23/52L T01L23/66 T01L924/01004 T01L924/01005 T01L924/01014 T01L924/01015 T01L924/01027 T01L924/01079 T01L924/01083 T01L924/10253 T01L924/13055 T01L924/13091 T01L924/14000 T01L924/19041 T01L924/19042 T01L924/19043 T01L924/30105 T01L924/30107 T01L924/30110 T01L924/30250 T04B203/54S14

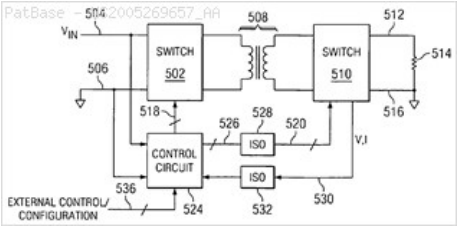
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Family:

Publication number	Publication date	Application number	Application date
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CN1989702 A	20070627	CN200580024186	20050602
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HK1104391 A3	20080111	HK20070109048	20070820
JP2008502215 T2	20080124	JP20070515541T	20050602
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WO05122423 A2	20051222	WO2005US19313	20050602
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Priority:

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Probable Assignee: SILICON LABORATORIES INC

Assignee(s): (std): ALFANO DONALD E ; CALLAHAN PHIL A ; DONG ZHIWEI ; DUPUIS TIMOTHY ; JAVED AHSAN ; LEUNG KA Y ; SILICON LAB INC ; THOMSEN AXEL ; TANG WILLIAM W K ; YAN SHOULI

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Inventor(s): (std): THOMSEN AXEL ; TIMOTHY DUPUIS ; ZHIWEI DONG ; SHOULI YAN ; JAVED AHSAN ; CALLAHAN PHIL A ; AXEL THOMSEN ; YAN SHOULI ; TANG WILLIAM W K ; TANG WILLIAM ; LEUNG KA Y ; ETTER BRETT ; BRESEMANN DAVID ; ALFANO DONALD E ; BRESEMANN DAVID P ; DONG ZHIWEI ; DUPUIS TIMOTHY

Inventor(s): TANG WILLIAM WK ; ALFANO DONALD

Agent(s): HOWISON AND ARNOTT LLP; HOWISON AND ARNOT LLP; TROP PRUNER AND HU PC; TROP PRUNER AND HU P C

Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KM KP KR KZ LC LK LR LS LT LU LV MA MC MD MG MK ML MN MR MW MX MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RU SC SD SE SG SI SK SL SM SN SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

Title: METHOD AND APPARATUS FOR GENERATING MULTI-LEVEL REFERENCE VOLTAGE IN SYSTEMS USING EQUALIZATION OR CROSSTALK CANCELLATION

Abstract: Source: US2002075968A A system and method are shown for generation of at least one reference voltage level in a bus system. A reference voltage generator on a current driver includes at least one reference voltage level, at least one control signal, and an active device. The active device is coupled to the at least one control signal, such as a current control signal, and a selected reference voltage of the at least one reference voltage level. The active device is arranged to shift the at least one reference voltage level based on the at least one current control signal such as an equalization signal, a crosstalk signal, or the combination thereof, employed on the current driver.

Classifications:

International (IPC 8-9): G01R19/00 G06F1/26 G06F12/00 G06F13/16 G06F13/38 G06F13/40 G06F13/42 G06F7/38 G06F7/64 G06G7/18 G11C11/56 G11C7/00 G11C7/10 G11C7/22 H03B1/00 H03D1/24 H03H7/30 H03K19/003 H03K19/173 H03K3/00 H03K7/02 H03M1/00 H03M1/06 H03M7/00 H04B17/00 H04L25/02 H04L25/03 H04L25/08 H04L25/34 H04L25/49 H04L27/01 H04N7/24 (Advanced/Invention); G06F12/00 G06F13/16 H04L25/02 H04L25/03 H04L25/49 H04L7/033 (Advanced/Non-invention); G06F12/00 G06F13/16 G06F13/38 G06F13/40 G11C11/56 G11C7/00 G11C7/10 H03H7/30 H04L25/02 H04L25/03 H04L25/49 (Core/Invention); H04L25/03 (Core/Non-invention)

International (IPC 1-7): G06F1/00 G06F1/26 G06F1/28 G06F1/30 G06F13/00 G06F13/40 G06F7/64 G06G7/18 G11C29/00 H01L27/08 H04L25/02 H04L25/03 H04L25/34 H04L25/49

CPC: H04L25/03019 H04L25/03343 H04L25/03363 H04L25/03477 G11C5/063

G11C7/1048 G11C7/1069 G11C8/06 H04L25/03057 H04L25/4917 H03K5/133 H04L25/0276 G11C7/02 G11C7/1096 G11C7/222 G11C8/10 H03K5/131 H03K17/162 H03K19/00323 H03K2005/00065 H03M5/20 H04L25/028 H04L25/0292 H04L25/03878 G11C7/04 G11C7/1051 G11C7/10 G11C7/1057 G11C7/106 G11C7/1072 G11C7/1078 G11C7/1084 G11C7/1087 G11C7/22 G11C11/56 G11C27/02 G11C2207/108 H04L7/0332 H04L25/0272 H04L25/0282 H04L25/0298 H04L25/08 H04L25/4902 H03K3/356034 H03K19/018528

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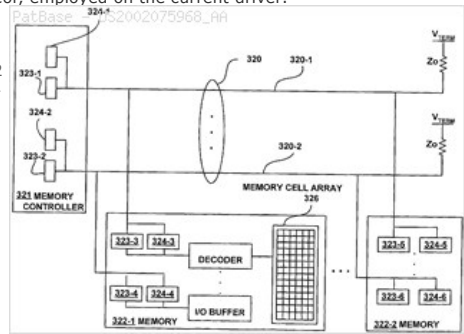
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Family:

Publication number	Publication date	Application number	Application date
AT463911 E	20100415	AT20010966571T	20010905
AU200127688 A5	20010716	AU20010027688D	20010105
AU2001287074 AA	20020322	AU20010287074	20010905
DE20122739 U1	20070823	DE20012022739U	20010105
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EP1410588 B1	20100407	EP20010966571	20010905
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Priority:

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Probable Assignee: RAMBUS INC

Assignee(s): (std): CHAU PARK ; DONNELLY KEVIN S ; RAMBUS INC ; WERNER CARL ; HOROWITZ MARK ; LIAW H J ; SIDIROPOULOS STEFANOS ; KIM JUN ; GARLEPP BRUNO W ; HOROWITZ MARK A ; GARRETT BILLY W JR ; WERNER CARL W ; BEST SCOTT ; CHAU PAK ; CHAU PAK S ; GARRETT JR BILLY W ; ZERBE JARED L ; ZERBE JARED LEVAN ; ZERBE JARED

Assignee(s): CHAU PAK SHING ; LIAW H

Inventor(s): (std): BEST SCOTT ; CHAU PAK ; CHAU PAK S ; CHAU PAK SHING ; CHAU PARK ; DONNELLY KEVIN S ; GARLEPP BRUNO W ; GARRETT BILLY W ; GARRETT BILLY W JR ; GARRETT JR BILLY W ; HOROWITZ MARK ; HOROWITZ MARK A ; KIM JUN ; LIAW H ; LIAW H J ; LIAW HAW JYH ; WERNER CARL ; WERNER CARL W ; ZERBE JARED ; ZERBE JARED L ; ZERBE JARED LEVAN ; SIDIROPOULOS STEFANOS

Inventor(s): STEFANOS SIDIROPOULOS ; MARK A HOROWITZ ; MARK HOROWITZ ; PAK S CHAU ; PARK CHAU ; SCOTT BEST ; JARED L ZERBE ; JARED LEVAN ZERBE ; JR BILLY W ; JUN KIM ; KEVIN S DONNELLY ; H J LIAW ; GARRETT BILLY WJR ; GARRETT ; GARRETT BILLY ; GARLEPP BRUNO ; DONNELLY KEVIN ; BILLY W GARRETT ; BRUNO W GARLEPP ; CARL W WERNER ; CARL WERNER

Agent(s): EISENFUEHR SPEISER AND PARTNER; MCDONNELL BOEHNEN HULBERT AND BERGHOFF LLP; MORGAN LEWIS AND BOCKIUS LLP RAMBUS INC; SILICON EDGE LAW GROUP LLP; ARTHUR J; MORGAN LEWIS AND BOCKIUS LLP

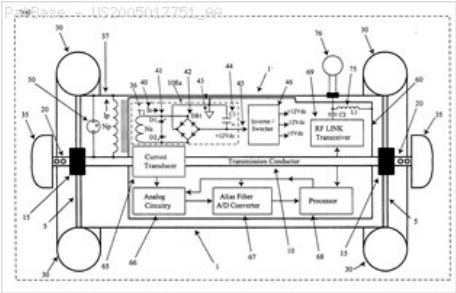
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Title: BODY CAPACITANCE ELECTRIC FIELD POWERED DEVICE FOR HIGH VOLTAGE LINES

Abstract: Source: US2005017751A Devices that couple to high voltage transmission lines obtain power themselves using the body capacitance of an element of the devices. The devices generate a comparatively lower voltage from the current flowing between the high voltage line and the element of the device that generates the body capacitance. The devices can be used to operate sensors that monitor the transmission lines or parameters of the power distribution system, such as current, line temperature, vibration, and the like. The devices can also be used as indicators, such as aircraft warning lights, information signs, etc. In addition, the devices can operate as RF transmission/reception or repeater devices, radar devices, mesh networking nodes, video/audio surveillance, sound emitting devices for scaring animals, drones that traverse the power line, etc. Because the devices operate in response to line voltage rather than current, the devices are reliable even in low current conditions.

Classifications:

International (IPC 8-9): G01R15/14 G01R31/02 G01R31/36 G01R31/40 (Advanced/Invention); G01R15/14 G01R31/02 G01R31/36 G01R31/40 (Core/Invention)
International (IPC 1-7): G01R31/02 G01R31/36
CPC: G01R15/142 H02G7/14 G01R1/22 G01R15/14 H02G7/00
European: G01R15/14B H02G7/00 H02G7/14 S01R15/14
US: 324/754.01 324/764.01 324/764.010P 324/771



Family:

Publication number	Publication date	Application number	Application date
CA2533807 AA	20060125	CA20042533807	20040722
CA2533807 C	20130521	CA20042533807	20040722
CN100516907 C	20090722	CN200480024595	20040722
CN1842713 A	20061004	CN200480024595	20040722
EP1654549 A2	20060510	EP20040778937	20040722
EP1654549 A4	20100602	EP20040778937	20040722
EP1654549 B1	20120606	EP20040778937	20040722
US2005017751 AA	20050127	US20040774088	20040206
US2008246507 AA	20081009	US20040566007	20040722
US7282944 BB	20071016	US20040774088	20040206
US7902854 BB	20110308	US20040566007	20040722
WO05013650 A2	20050210	WO2004US23645	20040722
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Priority:

US20030490318P 20030725 US20040774088 20040206 US20040566007 20040722
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Probable Assignee: POWER MEASUREMENT LTD

Assignee(s): (std): FORTH BRADFORD J ; FORTH J BRADFORD ; GUNN COLIN N ; HANCOCK MARTIN A ; HYATT GEOFFREY T ; LIGHTBODY SIMON H ; POWER MEASUREMENT LTD

Inventor(s): (std): FORTH J BRADFORD ; GUNN COLIN N ; GUNN COLIN N LIGHTBODY SIMON H ; FORTH BRADFORD J ; HANCOCK MARTIN A ; HYATT GEOFFREY T ; LIGHTBODY SIMON H ; GUNN COLIN ; FORTH JOHN BRADFORD

Inventor(s): LIGHTBODY SIMON ; HYATT GEOFFREY ; FORTH J ; GUNN COLIN N LIGHTBODY SIMON H FORTH BRADFORD J HA ; HANCOCK MARTIN

Agent(s): SMART AND BIGGAR; FINNIE PETER JOHN; WORKMAN NYDEGGER F K A WORKMAN NYDEGGER AND SEELEY; WORKMAN NYDEGGER / POWER MEASUREMENT; WORKMAN NYDEGGER POWER MEASUREMENT

Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KP KR KZ LC LI LK LR LS LT LU LV MA MC MD MG MK ML MN MR MW MX MZ NA NE NI NL NO NZ OM PG PH PL PT RO RU SC SD SE SG SI SK SL SN SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

Title: INTEGRATED DC/DC CONVERTER SUBSTRATE CONNECTIONS

Abstract: Source: US2006214253A Embodiments of the present invention provide an integrated circuit (IC) having an integrated DC-DC power converter therein. This IC is operable to support the distribution of combined power and data signals in a network environment such as an Ethernet network according to protocols such as the power over Ethernet (PoE) protocol. The IC includes a DC-DC power converter, a power feed circuit, and a network physical layer (PHY) module, wherein the PHY module may contain fine line structures susceptible to damage when exposed to excessive voltages. To prevent or reduce the likelihood of damage to the PHY module from voltages supplied to the DC-DC power converter, a common substrate ground is shared between the IC components.

Classifications:

International (IPC 8-9): B32B9/00 G01R19/18 G01R27/04 G01R27/32 G05F1/10 G06F1/00 G06F1/18 G06F1/26 G06F11/30 H01L21/02 H01L21/20 H01L21/76 H01L29/00 H01L29/92 H02H3/20 H02H3/22 H02H9/00 H02H9/04 H02H9/06 H02M3/24 H02M3/335 H02M5/45 H03B1/00 H03F3/38 H03F3/45 H04B1/10 H04B1/18 H04B14/06 H04B3/26 H04B3/54 H04J3/10 H04L12/10 H04L12/28 H04L12/56 H04L25/00 H04L25/06 H04M1/00 H04M11/00 H04M3/00 H04M9/00 H05K9/00 (Advanced/Invention); H04J1/16 (Advanced/Non-invention); B32B9/00 G01R19/18 G01R27/00 G01R27/04 G05F1/10 G06F1/00 G06F1/26 G06F11/30 H01L21/02 H01L21/70 H01L29/00 H01L29/66 H02H3/20 H02H9/00 H02H9/04 H02H9/06 H02M3/24 H02M5/00 H03B1/00 H03F3/38 H03F3/45 H04B1/10 H04B1/18 H04B14/02 H04B3/02 H04B3/54 H04J3/02 H04L12/10 H04L12/56 H04L25/00 H04L25/06 H04M1/00 H04M11/00 H04M3/00 H04M9/00 (Core/Invention); H04J1/00 (Core/Non-invention)

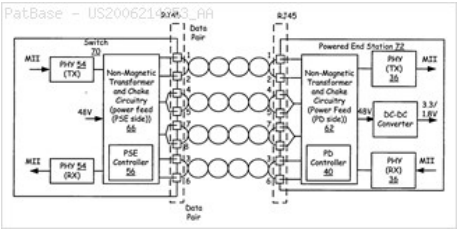
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European: G06F1/18V G06F1/26P H01L23/522C4 H01L23/525 H01L28/60 H02J1/06 H02J13/00E H02M3/335C4 H03F1/26 H03F1/32D H03F3/21C H03F3/393 H03F3/4551B1 H03F3/4551B1A H03F3/4551K H03F3/4553B H03F3/4553K H03K3/356G2D H03K7/08 H03M3/30 H04B14/06 H04L12/10 H04L12/12 H04L12/40A6 H04L12/40M1 H04L12/66 H04L25/02K1 H04L25/02K1A H04L25/02K1A H04L25/02K3 H04L25/02K3C H04L25/08 H04L25/493 H04L49/00 H04L49/40 H04M3/18 T01L924/30110 T03F200/114 T03F200/261 T03F200/271 T03F200/372 T03F200/541 T03F203/21145 T03F203/45082 T03F203/45084 T03F203/45101 T03F203/45212 T04L12/40 T04L49/35A Y02B60/31 Y02B60/33 Y02B60/34

US: 250/551 257/307 257/446 257/499 257/501 257/503 257/503P 257/506 257/508 257/508 257/532 257/532P 257/532S 257/535 257/E21.008 257/E21.008S 257/E21.54 257/E21.574 257/E21.628 257/E29.001 257/E29.001S 257/E29.342 257/E29.342S 257/E29.343 307/1 307/101 307/2 307/201 307/89 307/90 307/91 324/118 324/118P 324/641 327/538 330/10 330/10P 330/253 330/253P 330/258 330/258P 330/291 330/297 361/111 361/119 361/56 361/56P 361/90 363/16 363/16S 363/17 363/17P 363/37 363/97 363/97S 370/201 370/201S 370/216 370/216S 370/252 370/318 370/352 370/395.52 370/395.520P 370/395.53 370/419 370/419P 370/910 375/244 375/257 375/257P 375/258 375/316 375/318 375/319 375/319P 375/350 379/102.04 379/169 379/176 379/186 379/307 379/318 379/322 379/323 379/323P 379/324 379/324P 379/392.01 379/399.01 379/399.010S 379/412 379/413 379/413.02 379/413P 379/416 379/90.01 379/93.36 379/93.360P 438/381 438/381S 438/393 438/393S 439/517 455/574 713/300 713/300P 713/300S 713/310 713/340

Family:

Publication number	Publication date	Application number	Application date
EP1708409 A2	20061004	EP20060251674	20060328
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Priority:

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Probable Assignee: AKROS SILICON INC

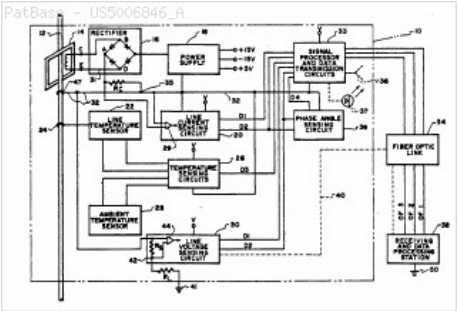
Assignee(s): (std):	AKRAS SILICON INC ; AKROS SILICON INC ; ALTMANN MICHAEL ; CAI JUN ; CRAWLEY PHILIP JOHN ; CREPIN FRANCOIS ; CAMAGNA JOHN ; DHUYVETTER TIMOTHY A ; CRAWLEY PHILLIP JOHN ; CRAWLEY PHILIP J ; CAMAGNA JOHN R ; GATTANI AMIT ; CRAWLEY PHILIP ; CREPIN J FRANCOIS ; GHOSHAL SAJOL
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Inventor(s):	CREPIN J
Agent(s):	KOESTNER BERTANI LLP; ROBERT A MCLAUCHLAN; KEN KOESTNER, KOESTNER BERTANI LLP; KEN KOESTNER KOESTNER BERTANI LLP; MARY JO BERTANI, KOESTNER BERTANI LLP; MARY JO BERTANI KOESTNER BERTANI LLP; KOESTNER KEN J; KEN J; MARY JO; KOESTNER PATENT LAW; KOESTNER KEN J ET AL; BERTANI MARY JO ET AL
Designated states:	AE AG AL AM AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN SV SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

Title: POWER TRANSMISSION LINE MONITORING SYSTEM

Abstract: Source: US5006846A Electric power line phase conductor (line) monitoring system in which a single measuring station can measure all the power parameters of voltage and current and phase angle plus ambient and line temperatures. The measuring station fastens to the line, typically draws power from the line, is small, light, and manually installed without the need for a line power outage. The measured data is transmitted from the measuring station up on the power line phase conductor to a ground receiving and data processing station by either radio or a fiber optic cable or an optical-through-air data link. Each type of data link, fiber optic, radio or optical-through-air, can be built to carry either analog (linear) or digital data. Line true RMS voltage and true RMS current values, line phase angle, along with ambient and line temperature values are digitized and transmitted in digital form through a digital data link. Real time analog voltage and current sinusoidal waveforms are transmitted in analog form through a linear data link with only group delay which for a 50-foot long fiber optic cable would be roughly only 50 nanoseconds. Voltage data is obtained by an elegantly simple voltage divider technique which takes advantage of the highly dielectric character of plastic and glass materials in general and fiber optic cables in particular to form a dual purpose unibody cable assembly containing both the optical fibers of the data link and the large resistance resistive link of the voltage divider. Resistive link materials are described which have high precision resistance tolerance and low temperature coefficients of resistance, and thus improve the accuracy of the voltage sensor by establishing precisely the total resistance of the resistive link and by minimizing changes in the magnitude of that resistance due to changes in ambient temperature.

Classifications:

International (IPC 8-9): G01R15/18 G01R21/133 H02H1/00 H02H1/06 H02J13/00 (Advanced/Invention);
G01R15/14 G01R21/00 H02H1/00 H02J13/00 (Core/Invention)
International (IPC 1-7): H04B9/00
CPC: H02J13/0065 G01R15/183 G01R21/133 Y02E60/74 Y04S10/30 H02H1/0069 H02H1/06 Y02E60/727 Y04S10/26
European: G01R15/18C G01R21/133 H02H1/00E2 H02H1/06 H02J13/00F4B5 Y02E60/72N Y04S10/26
US: 324/126 324/96 340/870.17 340/870.28 340/870.29



Family:

Publication number	Publication date	Application number	Application date
US5006846 A	19910409	US19900464707	19900112
US5181026 A	19930119	US19900632249	19901219

Priority:
US19870119750 19871112 US19900464707 19900112 US19900632249 19901219

Probable Assignee: GRANVILLE GROUP INC
Assignee(s): (std): CHU SAM Y ; COSTA ROBERT E ; GRANVILLE GROUP INC ; GRANVILLE GROUP INC THE ; GRANVILLE J MICHAEL
Assignee(s): GRANVILLE GROUP INC A CORP OF CALIFORNIA
Inventor(s): (std): GRANVILLE J MICHAEL ; COSTA ROBERT E ; CHU SAM Y

Abstract: Source: US4852046A A burner control system includes an input interface 10 for receiving burner status inputs. These are passed to address bus 14 and used to access addresses within EPROM 18. Data at the accessed addresses are output on bus 20 to control the burner via output interface 23 with related relays. Oscillator 16 and counter 17 sequence the selected address areas of the EPROM via bus 15 and a start up sequence, for example, can thus be effected. Incorrect status of the inputs from input block 10, flame detector 12 or the internal system checks in blocks 28 or 29 to bus 14 will cause the EPROM to be addressed at locations which contain data in a form that will cause shutdown or lockout via relay 25. Parity check block 22 also actuates lockout if errors are detected. System status can be provided by display 19.

PatBase - US4852046 A

The block diagram illustrates the control system for a flame detector. Key components and their connections include:

- Inputs and Interlocks:** STAT AIR INTERLOCK, FLAME 10, RELAY CHECK 28, and OSCILLATOR 16 are connected to the ADDRESS COUNTER 17.
- Control and Monitoring:** LOCKOUT RESET INPUT 11, CHECK COUNTER 13, and CHECK ADDRESS BUS 27 are connected to the CHECK COUNTER 13.
- Memory and Data:** EPROM 18 is connected to the DATA BUS 20. DISORD00000 19 is connected to the DATA BUS 20.
- Relays and Outputs:** RELAY CONTACT INTERFATE 27, FAN FLOW MAIN 23, LOAD RELAY 25, LOCKOUT RELAY 26, and LIVE SURVEY 24 are connected to the DATA BUS 20.
- Address Buses:** The system is divided into HIGH ADDRESS BUS and LOW ADDRESS BUS sections.

CPC: G05B19/045 G01R31/3278 G06F11/10
European: G01R31/327C2 G05B19/045 S06F11/10
US: 700/79

US: 700/79

Publication number	Publication date	Application number	Application date
DE3672606 D1	19900816	DE19863672606	19860730
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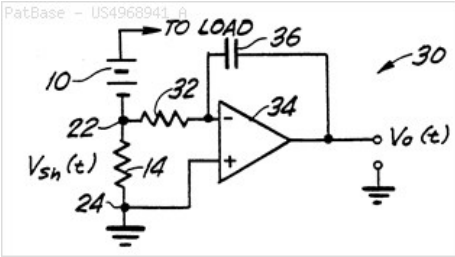
Designated states: BE DE FR IT NI

Title: Apparatus for monitoring the state of charge of a battery

Abstract: Source: US4968941A An apparatus for monitoring the state of charge of a battery having a shunt resistor connected in series with a battery and an integrating circuit connected across the terminals of the shunt resistor is provided. The integrating circuit includes a Memoriode having a large capacitance and the capability to store charge for long periods of time, so that the integration can be performed along the same curve even if power to the integrating circuit is interrupted.

Classifications:

International (IPC 8-9): F02P17/00 G01N27/416 G01R31/36 H02J7/00 H02J7/14 (Advanced/Invention); G01R31/00 (Advanced/Non-invention); G01R31/00 (Core/Non-invention)
International (IPC 1-7): F01N3/00 G01N27/416 G01N27/46 G01R31/36 G05D16/00 H01M10/44 H02J7/00 H02J7/14
CPC: G01R1/203 Y10S320/10 Y10S320/21 H01M10/486 H01M2220/20 H02J7/1423 B60L11/1861 B60L11/1868 G01R31/3606 Y02T10/7005 Y02T10/7044 Y02T10/7066 H02J7/1461 G01R31/006 G01R31/3613 H02J7/1453
European: B60L11/18M28 B60L11/18M32 G01R31/36M2 G01R31/36M2J G01R31/36N G01R31/36N1B H02J7/14D H02J7/14G H02J7/14H S01R1/20B S01R31/00T2 Y02T10/70B Y02T10/70H2 Y02T10/70H6D
US: 320/104 320/125 320/134 320/136 320/137 320/138 320/15 320/150 320/160 320/162 320/19 320/27 320/39 320/48 320/56 320/61 320/DIG.1 320/DIG.10 320/DIG.21 322/25 322/28 324/380 324/426 324/427 324/428 324/431 324/432 324/433 324/434 324/435 332/380 422/109 422/174 60/274 60/276 60/284 702/63



Family:

Publication number	Publication date	Application number	Application date
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US5179340 A	19930112	US19900607237	19901031
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Priority:

US19880218539	19880713	US19900607237	19901031	EP19900121143	19901105
US19940344470	19941123	US19950468210	19950606	US19950468938	19950606
US19940344739	19941123	US19920977921	19921118	US19920919011	19920723
US20040967704	20041015	US20070810155	20070604		

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Designated states: CH DE FR GB IT LI NL SE