

Search history

Search 1: TAC = optic W2 fiber (Results 45186)
Search 2: 1 AND (FT = arra* AND join* AND (lead* OR distribute* OR direct*)) (Results 6884)
Search 3: 2 AND (FT = (box OR container OR drawer OR cabinet)) (Results 2099)
Search 4: 3 AND (CC = CO) (Results 17)
Search 5: TAC=(optic W2 fiber) and PD<20171206 and IC=(G02B7* OR H01R9* OR H04Q1* OR H05K7*) (Results 1562)
Search 6: 5 AND (FT = (box OR container OR drawer OR cabinet)) (Results 435)
Search 7: 6 AND (FT = plate OR sheet) (Results 210)
Search 8: 7 AND (FT = frame OR beam) (Results 144)
Search 9: 8 AND (FT = join* OR connect* OR link*) (Results 141)
Search 10: 9 AND (FT = fuse#) (Results 22)

Title: OPTICAL DEVICE CONTAINING A FIBRE-OPTIC COMPONENT

Abstract: Source: US2003133686A An optical device is described, comprising a fibre-optic component, such as a fibre grating, which is associated with a predetermined transfer function, capable of being placed in a plurality of adjacent windings. Furthermore, the optical device is provided with a rigid housing capable of containing the said fibre-optic component, characterized in that it contains at least one separating element for physically separating the windings of the said plurality so as to prevent mutual contact through superposition and an element for fixing the said fibre-optic component capable of holding the said component in a stable position. The solution makes it possible to avoid undesirable changes of the transfer function that can occur during placement of the fibre-optic component in the housing and/or during the life of the device.

Classifications:

International (IPC 8-9): G01C19/66 G02B6/00 G02B6/34 G02B6/36 G02B6/44 H01S3/02 H01S3/06 H01S3/067

H05K7/00 (Advanced/Invention);

G02B6/00 G02B6/34 H05K7/00 (Advanced/Non-invention);

G02B6/00 G02B6/36 G02B6/44 H01S3/02 H01S3/06

H05K7/00 (Core/Invention);

G02B6/34 (Core/Non-invention)

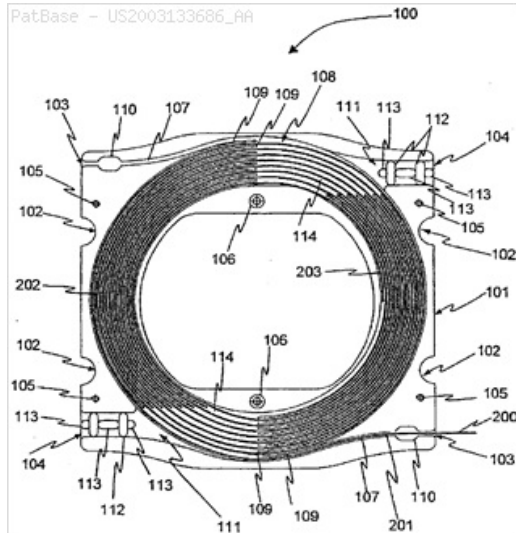
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CPC: G02B6/29394 G02B6/02209 G02B6/2932 G02B6/4453

H01S3/06704

European: G02B6/02G8V G02B6/16A G02B6/44C8A6 H01S3/067B S02B6/293D4F2C S02B6/293W8C

US: 385/135 385/135P 385/37 385/37S

**Family:**

Publication number	Publication date	Application number	Application date
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CA2389162 AA	20020425	CA20002389162	20001129
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DE60017284 D1	20050210	DE20006017284	20001129
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EP1234357 A2	20020828	EP20000977596	20001129
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JP2003515797 T2	20030507	JP20010542426T	20001129
US2003133686 AA	20030717	US20020148573	20020529
WO0141263 A2	20010607	WO2000EP11937	20001129
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Priority:

EP19990123719 19991130 EP20000977596 20001129 WO2000EP11937 20001129
US19990168632P 19991203

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Agent(s): GOWLING LAFLEUR HENDERSON LLP

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Legal status (INPADOC)

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Patent Date recorded Event group Code Event Details

Title: DEVICE AND METHOD FOR MONITORING SIGNAL DIRECTION IN AN OPTICAL COMMUNICATIONS NETWORK

Abstract: Source: US2001048537A A method and device are disclosed for monitoring communication activity within an optical communications network. The device includes a pair of optical components for tapping optical signals transported across a **fiber optic** line in the optical communications network. At least one optical performance monitor (OPM) receives tapped signals from the pair of optical components. An optical switch, controlled by the OPM, selectively provides to the at least one OPM tapped signals corresponding to optical signals traveling on the **fiber optic** line in a first direction. The OPM measures the various signal characteristics of the tapped signals when the optical switch is opened and closed, and determines the direction of travel of optical signals on the **fiber optic** line based upon the measured signal characteristics.

Classifications:

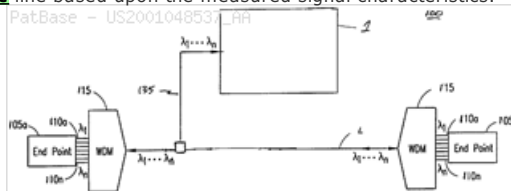
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International (IPC 1-7): G01J3/18 G01J3/28 G02B5/00 G02B5/18 G02B6/00 G02B6/28 G02B6/293 G02B6/34 G02B6/38 G02B6/46 G02B7/00 G02B7/02 H04B10/00 H04B10/08 H04J14/00 H04J14/02

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European: G01J3/02 G01J3/02A G01J3/02B1 G01J3/02N G01J3/14 G01J3/18 G01J3/28B G01J3/36 G02B27/10M G02B27/10S G02B27/10Z G02B27/12L G02B27/12P G02B5/18 G02B6/293D2B G02B6/293D2R G02B6/293W12 G02B6/293W14 G02B6/293W2 G02B6/293W2B2 G02B6/293W2B4 G02B6/38D G02B6/38D15 G02B6/38D16 G02B6/38D2S1 G02B6/42C3W G02B7/00T G02B7/02T H04B10/00 H04B10/077 H04B10/0795 H04B10/07953 H04B10/07955 H04B10/07957 H04B10/08A H04B10/213 H04J14/02 H04J14/02A H04J14/02A1E H04J14/02A2D S01J3/02A S01J3/02N S02B6/293D T04J14/02A T04J14/02A1E T04J14/02A1M T04J14/02A2B T04J14/02A2D T04J14/02M T04J14/02N1

US: 356/328 359/11 359/124 359/127 359/127.7 359/569 359/569P 359/571 359/820 359/820P 385/10 385/134 385/147 385/24 385/24.4 385/24S 385/33 385/36 385/37 385/55 385/55P 398/10 398/10P 398/31 398/31S 398/34 398/34P 398/81 398/81S 398/83 398/87

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Priority:

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WO2001US17831 20010601		

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Agent(s):		MARKS AND CLERK; HUNTON AND WILLIAMS LLP
Designated states:		AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KP KR KZ LC LI LK LR LS LT LU LV MA MC MD MG MK ML MN MR MW MX MZ NE NL NO NZ PL PT RO RU SD SE SG SI SK SL SN SZ TD TG TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

Legal status (INPADOC)

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Patent Date recorded Event group Code Event Details

Title: METHOD FOR SELECTIVELY PROVIDING ACCESS TO VOICE AND DATA NETWORKS BY USE OF INTELLIGENT HARDWARE

Abstract: Source: US2005177640A A method for selectively providing access to voice and data networks by use of intelligent hardware. The present invention provides security measures for controlling access to a network **connection**. An electronic device communicatively coupled to intelligent hardware initiates a request to access a network. The request is received at the intelligent hardware communicatively coupled to the network and configured to allow access to the network according to predetermined criteria. Provided the request satisfies the predetermined criteria, the electronic device is provided access to the network. The predetermined criteria may include placing geographic restrictions (e.g., the room the port is located in), temporal restrictions (e.g., weekend or nighttime restrictions), and user class restrictions (e.g., visitor restrictions or low-level employee restrictions) on specific ports of the intelligent hardware. In one embodiment, a central control site manages the predetermined criteria. In one embodiment, the present invention controls access to a corporate Intranet. In one embodiment, the intelligent device has specific access port serial number. The present invention provides a method of easier management of information systems.

Classifications:

International (IPC 8-9): G01R31/08 G06F1/26 G06F1/32 G06F11/00 G06F13/00 G06F15/16 G06F15/17 G06F15/177 G06F3/01 H04B7/00 H04J1/16 H04J3/14 H04L1/00 H04L12/24 H04L12/26 H04L12/28 H04L12/56 H04L12/64 H04L29/06 H04L29/12 H04M1/66 H04Q11/04 H04Q3/00 (Advanced/Invention);

H04L12/28 H04L29/06 (Advanced/Non-invention);

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H04L12/28 H04L29/06 (Core/Non-invention)

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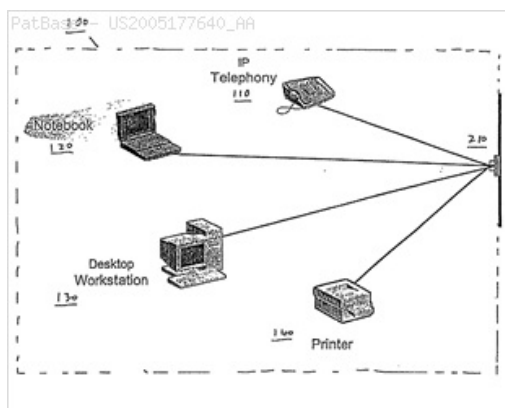
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Family:

Publication number	Publication date	Application number	Application date
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Priority:

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Probable Assignee: HEWLETT PACKARD ENTERPRISE DEVELOPMENT LP

Assignee(s): (std): 3COM CORP ; RUBINSTEIN ALAN ; WANG GARY

Assignee(s): INT BUSINESS MACHINES CORP ; HEWLETT PACKARD DEVELOPMENT CO LP ; HEWLETT PACKARD ENTERPRISE DEVELOPMENT LP ; CHANG RUSSELL ; HEWLETT PACKARD CO ; 3 COM CORP

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Inventor(s): BHAKT PATEL ; YUNG FU CHANG

Agent(s): WAGNER MURABITO AND HAO LLP, THIRD FLOOR; WAGNER MURABITO AND HAO LLP THIRD FLOOR; WAGNER MURABITO AND HAO LLP

Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KP KR KZ LC LI LK LR LS LT LU LV MA MC MD MG MK ML MN MR MW MX MZ NE NL NO NZ OM PH PL PT RO RU SD SE SG SI SK SL SN SZ TD TG TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW

Legal status (INPADOC)

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Patent Date recorded Event group Code Event Details

Title: COMBINATORIAL OPTICAL PROCESSOR

Abstract: Source: US2002158866A A Combinatorial Optical Processor and method for the architecture of optical systems utilizes one or more optical modules, each module including a number of individually addressable optical elements. The optical elements are arranged and addressed in a combinatorial-arithmetic fashion such that a set of optical filter functions are defined, and can be performed, by the optical modules. The number of optical filter functions may be an exponential function of the number of addressable optical elements. Additionally, each of the optical filter functions may be addressed at random and a plurality of such functions may be addressed simultaneously. Apparatus, such as imaging systems, may employ the Combinatorial Optical Processor in order to include without limitation the features of being solid-state, projection of images into free space and random addressability of the filter functions of the system. Combinatorial optical processing principles may be implemented as a method of optical digital to analog conversion. A free-space randomly-addressable interactive three-dimensional display system based on the Combinatorial Optical Processor is also described.

Classifications:

International (ipc 8-9): A61K31/41 A61K31/44 A61K31/444 A61K31/4725 A61P1/00 A61P35/00 B29D11/00 C07D207/00 C07D211/00 C07D401/14 G02B1/06 G02B23/14 G02B26/00 G02B26/02 G02B3/12 G02B3/14 G02B5/32 G02B7/04 G02B7/09 G02F1/00 G02F1/29 G02F3/00 G03B13/02 G03B13/10 G03B13/32 G03B3/00 G03B3/02 G03B3/10 G03H1/02 G06E3/00 G06F3/041 G06T15/00 H04N13/00 H04N5/232 H04N7/18 (Advanced/Invention); G02B1/04 G02B26/00 G02B3/14 (Advanced/Non-invention); A61K31/4427 A61K31/472 A61P1/00 A61P35/00 B29D11/00 C07D401/00 G02B1/00 G02B3/12 G03B13/32 G03B3/00 (Core/Invention); G02B G06T (Subclass/Invention)

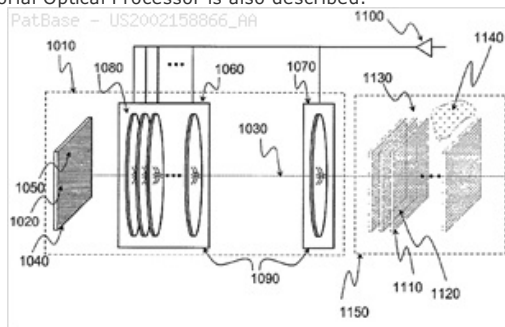
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US: 1/1 264/1.32 264/1.320S 345/173 345/173P 345/419 345/419P 348/144 348/144P 348/341 348/347 348/347S 348/40 348/E5.045 348/E50.45S 348/E7.085 348/E70.85S 349/200 359/107 359/15 359/15P 359/19 359/237 359/237S 359/290 359/290S 359/298 359/298S 359/3 359/380 359/3P 359/432 359/432S 359/665 359/665P 359/665S 359/666 359/666P 359/694 359/694S 396/373 396/378 514/308 514/332 514/333 514/359 546/140 546/186 546/255 546/256 548/518

**Family:**

Publication number	Publication date	Application number	Application date
EP2153252 A1	20100217	EP20080747869	20080508
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Priority:

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US20160359446	20161122				

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Agent(s):	BOYCE CONOR; JOSHUA D ISENBERG; JOSHUA D ISENBERG JDI PATENT; CROWELL AND MORING LLP,INTELLECTUAL PROPERTY GROUP; CROWELL AND MORING LLP INTELLECTUAL PROPERTY GROUP; JDI PATENT; JOSHUA D; CROWELL AND MORING LLP; ISENBERG JOSHUA D
Designated states:	AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PE PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

Legal status (INPADOC)

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Patent Date recorded Event group Code Event Details

Abstract: Source: US2003026081A Optoelectronic components, specifically, ceramic optical sub-assemblies are described. In one aspect, the optoelectronic component includes a ceramic base substrate having a pair of angled (or substantially perpendicular) faces. The electrical traces are formed directly on the ceramic surfaces and extend between the pair of faces. A semiconductor chip assembly is mounted on the first face of the ceramic base substrate and a photonic device is mounted on the second face. Both the semiconductor chip assembly and the photonic device are electrically **connected** to traces on the ceramic base substrate. The semiconductor chip assembly is generally arranged to be electrically **connected** to external devices. The photonic devices are generally arranged to optically communicate with one or more optical fibers. The described structure may be used with a wide variety of photonic devices. It is particularly well adapted for use with vertical cavity surface emitting lasers (or laser arrays) and detectors (or detector arrays). In some embodiments, at least the cathode of the photonic device is soldered directly to a cathode pad on the base substrate. Similarly, in some embodiments, the semiconductor chip assembly is electrically **connected** to the base substrate by direct soldering. Specific base substrate structures are disclosed as well.

International (IPC 8-9): H04B10/00 G02B6/36 G02B6/42 G02B6/43 G02F1/295 H01L21/56 H01L21/60 H01L21/76 H01L23/02 H01L23/12 H01L23/28 H01L23/48 H01L23/495 H01L23/498 H01L23/52 H01L25/10 H01L27/144 H01L29/40 H01L29/84 H01L31/00 H01L31/02 H01L31/0203 H01L31/0232 H01L31/18 H04B10/25 H04B10/40 H05K1/02 H05K7/02 (Advanced/Invention); G02B6/38 H05K1/18 H05K3/40 (Advanced/Non-invention); G02B6/36 G02B6/42 G02B6/43 G02F1/29 H01L23/28 H01L23/48 H01L25/10 H01L27/144 H01L31/02 H01L31/0203 H01L31/18 H05K1/02 (Core/Invention); H05K1/18 H05K3/40 (Core/Non-invention)

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Family:

Publication number	Publication date	Application number	Application date
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Priority:

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Probable Assignee: NATIONAL SEMICONDUCTOR CORP

Assignee(s): (std): NAT SEMICONDUCTOR CORP ; NAT SEMICONDUCTOR CORP A DELAW ; NATIONAL SEMICONDUCTOR CORP

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Inventor(s): (std): AUZEREAU LIONEL ; BARRATT CHRISTOPHER ; GEE STEPHEN A ; GEE STEPHEN ANDREW ; LIVINGSTON EUAN P ; MAZOTTI WILLIAM P ; MOHAN JITENDRA ; PHAM KEN AND ; ROBERTS BRUCE C ; ROBERTS BRUCE CARLTON AND ; NELSON MICHAEL R ; MURRAY CADE ; MAZOTTI WILLIAM PAUL ; LIU JIA ; KOH YONGSEON ; DEANE PETER ; DEAN PETER ; CLARKE ROGER WILLIAM ; CLARKE ROGER W ; BRIANT JOHN P ; TSAY CHEN HUI ; TOWNSEND JANET E ; SMITH CHRISTOPHER J ; ROBERTS BRUCE CARLTON ; NGUYEN LUU THANH ; NGUYEN LUU T ; PENDSE NEERAJ ANIL ; ROBERTS BRUCE ; PHAM KEN ; NGUYEN LUU ; NGUYEN HAU THANH ; MAZOTTI WILLIAM ; LIU JIA AND ; CLARKE ROGER

Inventor(s): NGUYEN LUU T AND ; SMITH CHRISTOPHER ; TOWNSEND JANET ; TSAY, CHEN-HUI ; NELSON MICHAEL ; BRIANT JOHN ; GEE STEPHEN

Agent(s): BEYER WEAVER AND THOMAS LLP; BEYER WEAVER LLP; BETER WEAVER LLP; BEYER LAW GROUP LLP

Legal status (INPADOC)

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Patent Date recorded Event group Code Event Details

Title: Light ramp 2000 **fiber optic** local access architecture

Abstract: Source: US6542267B A passive fiber local access architecture and signaling plan is disclosed which enables the local exchange network to meet the need, as defined by the International Telecommunications Union(ITU) in References 7 and 8, for an economic means of providing a multitude of services to subscribers over a single medium. Integrating disparate signals compatibly and symmetrically over one medium dictates that all digital transmission and signaling at speeds in excess of 100 Mbps be used on the **link**. The additional bandwidth needed to support this expanded capacity demand points to glass fiber as the most suitable candidate for this role. A failure tolerant, diversely routed redundant ring cabling architecture provides a dedicated fiber or fiber pair to each premises from the local exchange carrier POP, eliminating the need for intermediate nodes. line power, signal splitters, amplifiers or metallic loops in the last mile. 12x12 cross **connect** units are deployed in the outside cable plant to meet user demand anywhere along the route, serving 500 or more homes from a single POP. The passive fiber subscriber line is activated and controlled by a set of out of band digital supervision signaling protocols that are compatible with Signaling System 7. These protocols replace existing DC loop closure supervision. Firmware is provided at both POP and subscriber ends of the **link** to administer and monitor **link** operations.

Classifications:

International (IPC 8-9): H04L12/28 H04Q11/00 (Advanced/Invention);

G02B6/28 G02B6/35 (Advanced/Non-invention);

H04L12/28 H04Q11/00 (Core/Invention);

G02B6/28 G02B6/35 (Core/Non-invention)

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CPC: G02B6/28 H04L12/2854 H04Q11/0062 H04Q11/0067

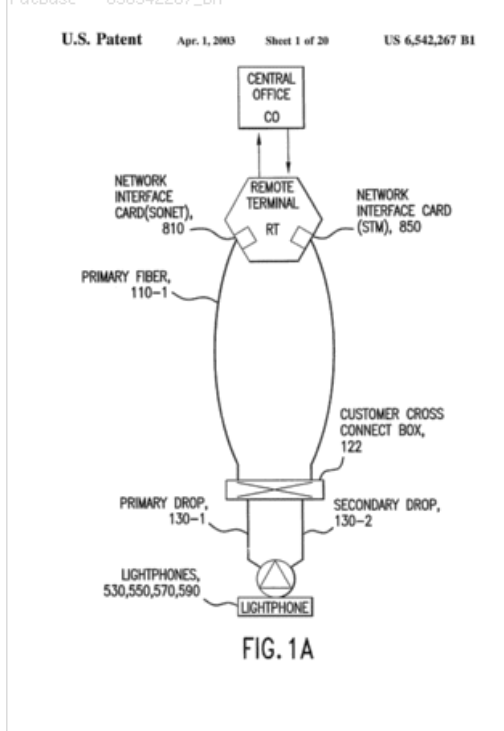
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European: H04L12/28P H04Q11/00P4 S02B6/28 T04Q11/00P4C

T04Q11/00P4E T04Q11/00P4S

US: 359/118 359/119 359/125 359/137 398/59

PatBase - US6542267_BA



Family:

Publication number	Publication date	Application number	Application date
US6542267 BA	20030401	US19970979425	19971124

Priority:

US19970979425 19971124

Probable Assignee:

NICHOLSON PAUL JAMES

Assignee(s): (std):

NICHOLSON PAUL JAMES

Inventor(s): (std):

NICHOLSON PAUL JAMES

Legal status (INPADOC)

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Patent Date recorded Event group Code Event Details

Title: SYSTEMS AND METHODS FOR MANAGING OPTICAL FIBERS AND COMPONENTS WITHIN AN ENCLOSURE IN AN OPTICAL COMMUNICATIONS NETWORKS

Abstract: Source: US2005105873A The preferred embodiments of the present invention include an optical splitter module having **connectorized** pigtails that are stored on the bulkhead faceplate of the module. The module includes an optical splitter output harness, for example, a ribbon cable assembly attached to the bulkhead with rugged strain relief mechanism. The ribbon harness is converted to individual pigtails with **connectors** which are stored on adapter receptacles on the faceplate. Adapter receptacles used may optionally be half receptacles when storage is the only desired function or may be full receptacles when access to the pigtail ferrule tip is required. Access to the ferrule tip may be required for attaching **fiber optic** terminators to eliminate undesirable reflections caused by unterminated **connectors**. The module provides an administrative location for splitter outputs prior to being **connected** individually into service. The module also provides an administrative storage location for splitter outputs taken out of service as a temporary staging area before being reassigned and **connected** individually into service again.

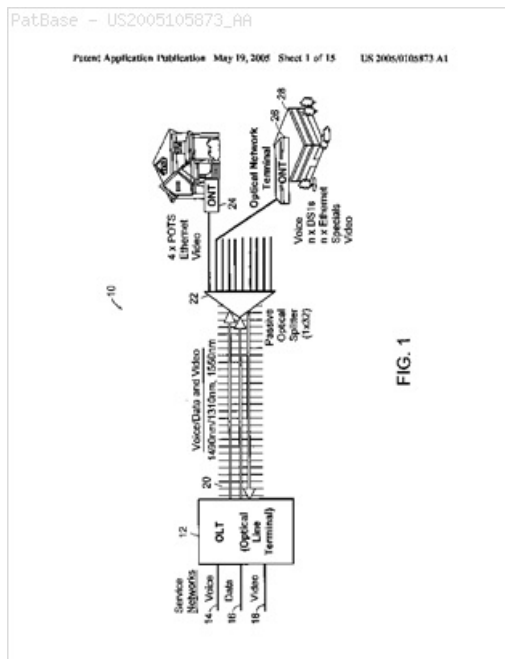
Classifications:

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International (IPC 1-7): G02B6/00 G02B6/44 H04Q1/14 **CPC:** G02B6/3849 G02B6/3897 H04J14/02 H04Q1/021 H04Q1/114 H04Q2201/10 H04Q2201/804 G02B6/4471 H04B10/25 G02B6/445 G02B6/4452 H04Q1/14

European: G02B6/38D16 G02B6/38D6H2 G02B6/40C G02B6/44C8A4 H04Q1/14 S02B6/44C8A4 T04J14/02 T04Q201/10 T04Q201/80F

US: 1/1 385/134 385/134S 385/135 385/135P 385/135S 385/136 385/137 385/139 385/139S 385/24 385/24P 398/70 398/71

**Family:**

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Priority:

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TOM NOONAN MICHA

Agent(s): DAVIES COLLISON CAVE; DAVIES COLLISON CAVE PTY LTD; ROBIC; BOHNENBERGER JOHANNES ET AL; BOHNENBERGER JOHANNES; THOMAS O HOOVER ESQ BOWDITCH AND DEWEY LLP; BOWDITCH AND DEWEY LLP; HARRITY AND SNYDER LLP; HARRITY AND SNYDER LLP SUITE 300; MERCHANT AND GOULD PC ATTENTION OF DAVID G SCHMALTZ; MERCHANT AND GOULD PC; ATTENTION OF DAVID G SCHMALTZ MERCHANT AND GOULD PC

Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LC LI LK LR LS LT LU LV LY MA MC MD MG MK ML MN MR MW MX MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RU SC SD SE SG SI SK SL SM SN SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

Legal status (INPADOC)

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Patent	Date recorded	Event group	Code	Event	Details
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Abstract: Source: US2006274471A Embodiments of a protector block which integrates interconnection and surge protection for equipment in a telecommunications infrastructure are presented herein.

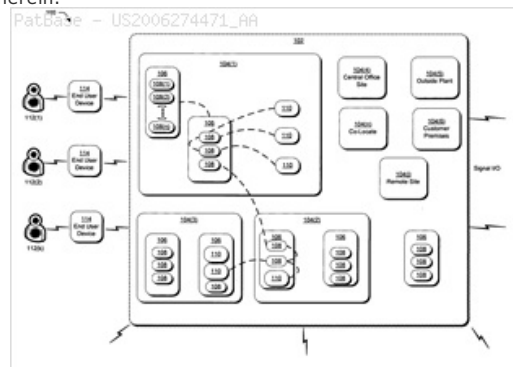
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H05K7/18 (Advanced/Invention);
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CPC: H04L49/40 H04L49/45 H01R13/514 H01R13/518 H05K1/0254
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European: G06F13/40E4 H01R13/514 H01R13/518 H04L49/40
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US: 29/829 361/111 361/679.4 361/679.400P 361/685 361/724
361/724S 361/732 361/737 361/796 361/801 370/463 370/463P
710/301 710/301P 710/302



Publication number	Publication date	Application number	Application date
CA2548705 AA	20061203	CA20062548705	20060529
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Assignee(s): JAYNE ROB ; COMERICA BANK

Inventor(s): (std): ALLEN BRIAN ; GARRETT RICHARD ; GARRETT RICK ; JAYNE ROB ; THOM TERRY ; THOM TERRY T

Inventor(s): ROB JAYNE ; RICK GARRETT

Agent(s): SMART AND BIGGAR; LEE AND HAYES PLLC; MCGINNITY DANIEL T ET AL

Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LC LK LR LS LT LU LV LY MA MC MD MG MK ML MN MR MW MX MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RU SC SD SE SG SI SK SL SM SN SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

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Title: CONTROLLING THE TRANSMISSION AMPLITUDE PROFILE OF A COHERENT LIGHT **BEAM** IN A HOLOGRAPHIC MEMORY SYSTEM

Abstract: Source: US2006274393A Aspects of the present invention are generally directed to shaping coherent light **beams** used in a holographic system. Embodiments of the present invention are directed to **beam**-shaping devices positioned in the path of coherent light **beams** used in a holographic system to shape a coherent light **beam** into an essentially diffraction noise free coherent light **beam** of predetermined dimensions that has a continuous light **beam** profile of distinct intensity zones.

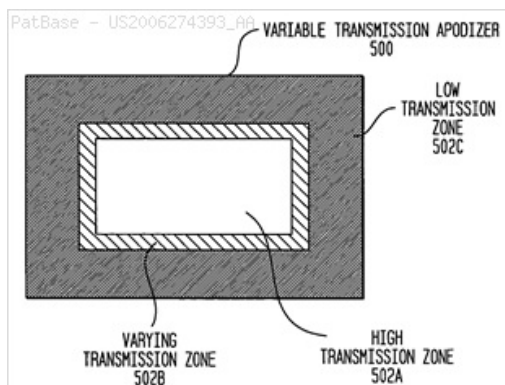
Classifications:

International (IPC 8-9): G01B11/00 G01B9/02 G01D5/34 G02B5/32 G02B6/36 G02B7/02 G03C5/00 G03H1/00 G03H1/04 G03H1/08 G03H1/12 G03H1/22 G03H1/26 G11B7/00 (Advanced/Invention); G01B11/00 G01D5/26 G02B5/32 G02B6/36 G03C5/00 G03H1/04 G03H1/08 G03H1/26 G11B7/00 (Core/Invention)

CPC: G02B7/003 G02B7/021 G11B7/1384 G11B7/22 G02B27/0927 G02B27/0988 G03H1/04 G03H1/0465 G11B7/1398 G02B7/004 G02B7/005 G11B7/128 G03H1/22 G03H2222/56 G02B17/023 G03H1/02 G03H2001/0212 G03H2222/22 G03H2223/26 G11B7/1359 G11B7/1362 G02B27/283 G11B7/1263 G11B7/1356 G11B7/1365 G11B7/1369 G03H1/18 G03H1/182 G03H2223/14 G03H2223/19 G03H2260/35 G11B7/0045 G11B7/0055 G01J9/0246 G11B7/126 H01S5/06246 H01S5/0687 G11B7/0946 G11B7/1275 G03H2001/184 G03H2222/20 G03H2260/12 Y10T74/20335 G11B7/0065 G11B7/007 G11B7/09

European: G01J9/02L G02B17/02D G02B27/09C G02B27/09E4 G02B27/09H G02B27/09S4 G02B27/28B G02B7/00C G02B7/00C1 G02B7/00C2 G02B7/02A G03H1/02 G03H1/04 G03H1/04L G03H1/18 G11B7/0045 G11B7/0055 G11B7/0065 G11B7/007 G11B7/09 G11B7/09P G11B7/126 G11B7/1263 G11B7/1275 G11B7/128 G11B7/1356 G11B7/1359 G11B7/1362 G11B7/1365 G11B7/1369 G11B7/1384 G11B7/1398 G11B7/22 S03H1/02C2 S03H1/18A S03H1/18B S03H1/18B2P S03H1/22 S03H2222/20 S03H2222/56 S03H223/14 S03H223/19 S03H223/26 S03H260/12 S03H260/35 S11B7/0055 S11B7/0065 T01S5/062P8 T01S5/0687

US: 250/231.1 250/231.13 341/13 356/399 356/520 356/614 359/1 359/11 359/15 359/15P 359/19 359/22 359/22P 359/30 359/32 359/35 359/436 359/811 359/9 369/103 369/103P 369/44.32 369/53.19 385/88 430/1 430/1P 430/269 430/269P 74/490.06 G9B/007.099 G9B/007.114 G9B/007.117 G9B/007.119 G9B/7.01 G9B/7.010S G9B/7.019 G9B/7.019S G9B/7.027 G9B/7.027S G9B/7.029 G9B/7.062 G9B/7.094 G9B/7.097 G9B/7.097S G9B/7.099 G9B/7.099S G9B/7.114 G9B/7.114S G9B/7.115 G9B/7.115S G9B/7.116 G9B/7.116S G9B/7.117 G9B/7.117S G9B/7.119 G9B/7.119S G9B/7.133 G9B/7.133S G9B/7.138 G9B/7.138S



Family:

Publication number	Publication date	Application number	Application date
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Assignee(s): (std): INPHASE TECH INC ; RILEY BRIAN S ; WADE JAMES J ; WEGNER AARON

Assignee(s): INPHASE TECHNOLOGIES INC ; ACADIA WOODS PARTNERS LLC ; AKONIA HOLOGRAPHICS LLC

Inventor(s): (std): CURTIS KEVIN R ; FABINY LARRY ; FOTHERINGHAM EDELINE ; HOSKINS ALAN ; HUNTER SUSAN ; KMETA VLADIMIR ; KRNETA VLADIMIR ; ANDERSON KEN E ; AYRES MARK R ; REDMOND IAN R ; RILEY BRIAN ; RILEY BRIAN S ; COLE MICHAEL C ; SHUMAN CURTIS A ; WADE JAMES J ; WEGNER AARON ; SHUMAN CURTIS ALAN ; MALANG KEITH W ; SISSOM BRADLEY J

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Agent(s): JAGTIANI AND GUTTAG; AJAY A; VEDDER PRICE PC; MORRISON AND FOERSTER LLP

Legal status (INPADOC)

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Patent	Date recorded	Event group	Code	Event	Details
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Title: NETWORKED COMMUNICATIONS AND EARLY WARNING SYSTEM

Abstract: Source: US2008205324A A networked communications system comprising one or more integrated satellite based devices each for broadband digital signaling, one or more customized segment addressable cable assembly for terrestrial signaling in operable communication with the integrated satellite based device and a command console, the command console further comprising interconnectivity with public data or voice network signals through digital bridging; and a remote relay station in operable communication with the command console and the customized segment addressable cable assembly. Each detector includes a UWB receiver, processor and voice synthesis circuit, further embedded with a media access control address scheme for identity of each detector.

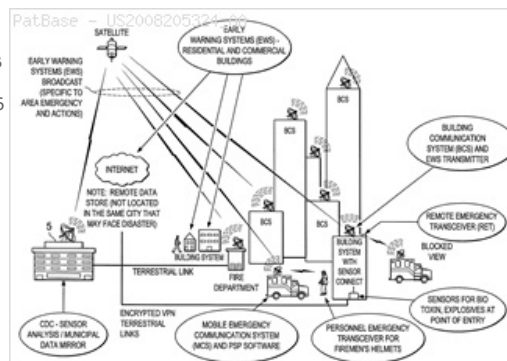
Classifications:

International (IPC 8-9): C08K3/34 F16F15/121 F16M13/00 F16M13/02 G02B G02B6/00 G02B6/44 G08B13/00 G08B13/196 H01B H01B11/00 H01B11/22 H01B7/00 H01B7/17 H01B7/29 H01B9/00 H01Q1/00 H01Q1/02 H01Q1/12 H02G H02G3/03 H02G3/08 H02G5/06 H04B1/00 H04B1/38 H04B1/69 H04B10/00 H04B10/11 H04B10/2575 H04B10/29 H04B10/40 H04B7/15 H04B7/155 H04B7/185 H04H20/02 H04H20/59 H04H20/61 H04H20/74 H04L H04L12/24 H04L29/14 H04W28/04 H05K7/10 H05K7/20 (Advanced/Invention); G02B6/44 H01B11/00 H01B11/22 H04H20/59 H04H40/90 H04L12/24 H04L29/14 (Advanced/Non-invention); G02B6/00 G02B6/44 G08B13/00 H01B11/00 H01B7/17 H01B9/00 H01Q1/12 H02G3/02 H02G3/08 H02G5/00 H04B1/00 H04B1/38 H04B1/69 H04B10/00 H04B7/15 H04B7/155 H04B7/185 H04H1/00 H04L29/14 (Core/Invention); G02B H01B H02G H04L (Subclass/Invention)

CPC: F16M13/022 H01Q1/005 F16F15/121 H04W28/04 G02B6/441 H05K7/1015 H05K7/2039 H01B7/17 H04B1/38 H04H20/02 H04H20/59 H04H20/61 H04H40/90 H04B7/185 H04B10/2575 H04B10/11 H04B10/40 H04B10/29 G08B13/19636 G08B13/1966 H01Q1/125 G02B6/4432 G02B6/443 G02B6/4436

European: G02B6/44C7A G02B6/44C7F H04H20/02 H04H20/61 S02B6/44C5 S02B6/44C7A1 T04H20/59 T04H40/90

US: 1/1 174/105B 174/70R 248/157 248/276.1 248/288.11 248/295.11 248/295.110S 248/636 248/636S 267/136 267/136S 267/140.2 307/147 307/147S 340/541 340/541S 340/566 340/566P 343/704 343/704P 343/878 348/152 361/679.01 361/704 361/752 370/316 370/316P 385/100 385/100S 398/115 398/115P 455/12.1 455/522 455/522S 455/68 455/68P 455/73 455/73S

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Priority:

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Inventor(s): CALVIN H WOOSNAN

Agent(s): SHELSTON IP; SHELSTON IP PTY LTD; JACKSON RICHARD ERIC; CARPMAELS AND RANSFORD LLP; DREW AND NAPIER LLC; GARDERE WYNNE SEWELL LLP, INTELLECTUAL PROPERTY SECTION; GARDERE WYNNE SEWELL LLP INTELLECTUAL PROPERTY SECTION; PATTON BOGGS LLP; SQUIRE PATTON BOGGS US LLP; CAMERON IP; BEECH SHANNON L

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PE PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

Legal status (INPADOC)

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Patent Date recorded Event group Code Event Details

Title: SLIDABLE **FIBER OPTIC CONNECTION** MODULE WITH CABLE SLACK MANAGEMENT

Abstract: Source: US2014241691A A telecommunications device includes a rack defining right, left, front, rear, top, and bottom sides, the rack defining mounting locations in a stacked arrangement from the bottom to the top, the mounting locations for receiving modules defining **connection** locations. A cable storage bay is located at one of the right and left sides of the rack and defines front and rear cable storage areas. Both the front and rear cable storage areas include cable management structures for managing and guiding cables toward and away from the **connection** locations. A trough is defined at the top of the rack, the trough configured for extending cables to other racks in a front to rear direction, the trough also defining a cable drop-off communicating with the cable storage bay for extending cables to either of the front or rear cable storage areas for further **connection** to the **connection** locations.

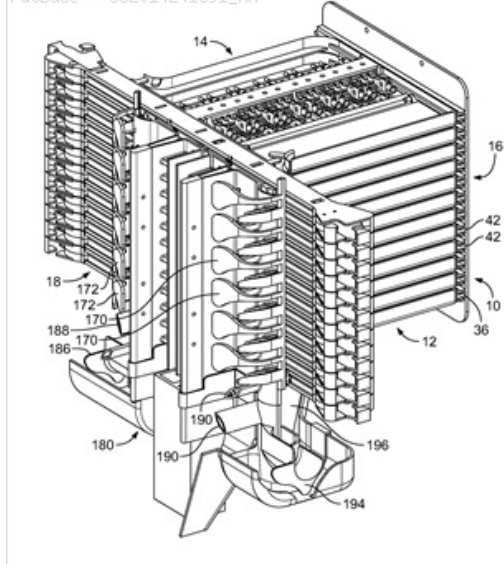
Classifications:

International (IPC 8-9): G02B6/38 G02B6/44 G02B6/46 H01R4/58 H04Q1/06 (Advanced/Invention)

CPC: G02B6/4452 G02B6/4459 H04Q1/023 H04Q1/06 G02B6/4457 G02B6/4478 G02B6/3897 H01R4/58 G02B6/4455

US: 1/1 385/135

PatBase - US2014241691_AA



Family:

Publication number	Publication date	Application number	Application date
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US2014241691 AA	20140828	US20140187470	20140224
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US9389384 BB	20160712	US20140187470	20140224
WO14133943 A1	20140904	WO2014US17937	20140224

Priority:

US20130770165P 20130227 US20140187470 20140224 WO2014US17937 20140224
US20160206834 20160711

Probable Assignee: COMMSCOPE TECHNOLOGIES LLC

Assignee(s): (std): COMMSCOPE TECHNOLOGIES LLC ; HAATAJA TIMOTHY ; SZKLARZ ROB ; SOLHEID JAMES J ; KMIT PAUL ; ADC TELECOMMUNICATIONS INC

Assignee(s): COMMSCOPE EMEA LTD ; TYCO ELECTRONICS SERVICES GMBH ; JPMORGAN CHASE BANK NA COLLATERAL AGENT AS

Inventor(s): (std): HAATAJA TIMOTHY ; KMIT PAUL ; SOLHEID JAMES J ; SZKLARZ ROB

Agent(s): KEANE DAVID COLMAN; MERCHANT AND GOULD PC; BRUESS STEVEN C

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BN BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IR IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PA PE PG PH PL PT QA RO RS RU RW SA SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

Legal status (INPADOC)

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Patent Date recorded Event group Code Event Details

Title: POWER CONVERSION MODULE FOR USE WITH OPTICAL ENERGY TRANSFER AND CONVERSION SYSTEM

Abstract: Source: US2015034144A A power conversion system for converting optical energy received from a **fiber optic** line to electrical energy, the system comprises a housing, a heat sink within the housing, a high power **connector** coupled to the line and having an end positioned within the housing, **beam** forming optics within the interior space positioned proximal to the **connector**, and a partially spherical end array and annular arrays of photovoltaic chips.

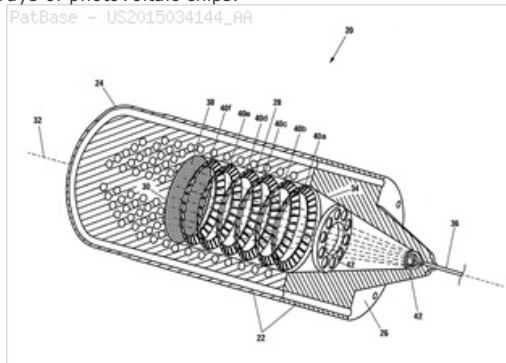
Classifications:

International (IPC 8-9): B63B35/12 B64D33/00 E21B41/00 E21B47/12 F25C5/08 G02B19/00 G02B6/00 G02B6/34 G02B6/36 G02B6/42 G02B6/44 G02B7/00 G02B7/182 H01L31/052 H01L35/00 H01L35/30 H02G3/00 H02J17/00 (Advanced/Invention); E01H5/10 (Advanced/Non-invention)

CPC: F25C5/08 E01H5/10 H01L31/0543 H02S40/22 H02S40/42 H02S40/44 G02B6/4268 G02B19/0052 G02B7/1827 G02B7/005 B64D33/00 G02B6/3604 B63B35/12 G02B6/4296 G02B6/4458 G02B6/4415 G02B6/4436 H02J17/00 Y02E10/52 Y02E10/60 E21B41/0085 E21B47/123 G02B6/36 H01L35/00 H01L35/30 H02J50/30 H02J7/025 H02J50/90

US: 1/1 136/246 307/9.1 385/26

PatBase - US2015034144_AA



Family:

Publication number	Publication date	Application number	Application date
US2015034144 AA	20150205	US20140292495	20140530
US2015253513 AA	20150910	US20150723161	20150527
US2015256033 AA	20150910	US20150723138	20150527
US2015256034 AA	20150910	US20150723206	20150527
US2015357868 AA	20151210	US20150810121	20150727
US9090315 BA	20150728	US20110303449	20111123
US9383520 BB	20160705	US20150723206	20150527

Priority:

US20100416676P 20101123	US20110303449 20111123	US20140292495 20140530
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Assignee(s): (std): HOGAN BARTHOLOMEW P ; PIEDRA SOMBRA CORP INC ; STONE WILLIAM C

Assignee(s): PIEDRA SOMBRA INC CORP ; STONE AEROSPACE INC

Inventor(s): (std): STONE WILLIAM C ; HOGAN BARTHOLOMEW P

Agent(s): GUNN LEE AND CAVE PC; MIGUEL

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