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Concept 3 Concept 4 Concept 2

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Mandatory CONCEPT 1 : Concept 1

EN - (BUS or Universal Serial Bus and (debug or EUD or diagnos*) and electronic* and ITAG

DE -

FR -

CONCEPT 2 : Concept 2

EN - Hub and interface and peripheral and (send* or converts* or transmit*) and (data or packet*)

DE -

FR -

CONCEPT 3 : Concept 3

EN - oscillator* and (suppl* or source*) and (SOC or IC or SIP or super_speed

DE -

FR -

CONCEPT 4 : Concept 4

EN - (SWD or Serial wire debug) and COM

DE -

FR -

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Databases	SS	Status	Results	Query	View...	Comment
EPODOC	1		4628	/IC G06F11/267		<--- Starting Set in EPODOC
SPRINT	> 1		8021	*? (STARTING SET)		TRANSFER SS 1 from EPODOC
2	Lim SS 1		34	(USB OR UNIVERSAL SERIAL BUS) AND (DEBUG* OR EUD OR DIAGNOS*) AND ELEC...		Concept 1: Concept 1
3	Lim SS 1		56	HUB AND INTERFACE* AND PERIPHERAL AND (SEND* OR CONVERT* OR TRANSMIT*) ...		Concept 2: Concept 2
4	Lim SS 1		5	OSCILLATOR* AND (SUPPL* OR SOURCE*) AND (SOC OR IC OR SIP) AND SUPRA...		Concept 3: Concept 3
5	Lim SS 1		6	(SWD OR SERIAL WIRE DEBUG) AND COM		Concept 4: Concept 4
6	Lim SS 1		3	(3 Facets - 1 concept mandatory) 2 AND (3 AND 4) OR (3 AND 5) OR (4 AND 5)		3 Facets - 1 concept mandatory
7	Lim SS 1		5	(4 Facets - 1 concept mandatory) 2 AND (3 AND 4 AND 5)		4 Facets - 1 concept mandatory
8	Lim SS 1		34	(1 Facet - 1 concept mandatory) (3)		1 Facet - 1 concept mandatory
9	Lim SS 1		12	(2 Facets - 1 concept mandatory) 2 AND (3) OR (4) OR (5)		2 Facets - 1 concept mandatory
10	Lim SS 1		3	(3 Facets - 1 concept mandatory) 2 AND (3 AND 4) OR (3 AND 5) OR (4 AND 5)		3 Facets - 1 concept mandatory
11	Lim SS 1		2	(4 Facets - 1 concept mandatory) 2 AND (3 AND 4 AND 5)		4 Facets - 1 concept mandatory

12 documents selected.

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07	US2004174570	Q
08	US2009204831	Q
09	US8547135	Q
10	US2012243160	Q
11	US2014225630	Q
12	WO2007029253	Q
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14	US2008052576	Q
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