

Search history

Search smart and tv (Results 47675)
1:

Search smarttv (Results 631)
2:

Search 1 and 2 (Results 527)
3:

Search 3 and videogame (Results 4)
4:

Search pn=US20120291081 (Results 1)
5:

Search [SP]: 1. a television set, comprising a. a television screen, b. at least one peripheral electronic device connected to said television set, and
6: c. a processor programmed to cause images to be displayed on said television screen, and said processor further being programmed to connect said at least one peripheral electronic device such that an image is displayed on said television screen so that a viewer can select said at least one peripheral electronic device immediately after turning on said television set. 2. the television set as in claim 1, wherein said at least one peripheral electronic device is an internally mounted internet video streaming receiver box connected to said processor and providing information from the internet to said processor. 3. the television set as in claim 1, wherein said at least one peripheral electronic device is a internet video streaming receiver box connected to said processor and providing information from the internet to said processor. 4. the television set as in claim 1 wherein said at least one peripheral electronic device is a roku(r) internet video streaming receiver box. 5. the television set as in claim 1, wherein said television set is lcd television set. 6. the television set as in claim 1, wherein said television set is a led television set. 7. the television set as in claim 1, wherein said television set is a plasma television set. 8. the television set as in claim 1, wherein said television set is a smart television set. (Results 0)

Search tv and game (Results 46788)
7:

Search 7 and 1 (Results 19755)
8:

Search 8 and 2 (Results 260)
9:

Search 9 and wireless (Results 235)
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Search 10 and chatpad (Results 0)
11:

Search 10 and control (Results 228)
12:

Search 12 and GPU (Results 20)
13:

Search 13 and ram (Results 19)
14:

Search 14 display (Results 0)
15:

Search 14 and controller (Results 18)
16:

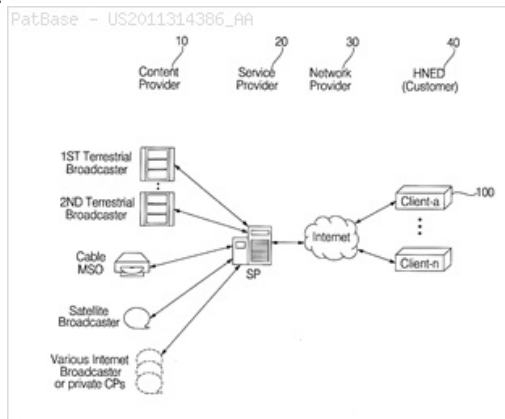
Search 16 and touchpad (Results 10)
17:

Title: IMAGE DISPLAY APPARATUS AND METHOD FOR OPERATING THE SAME

Abstract: A method for operating an image display apparatus is discussed. The method according to an embodiment includes storing a Web site list including a plurality of Web sites and login information for the Web sites, connecting to servers of at least two of the stored Web sites using the stored login information, and displaying a list of the connected Web sites or Web pages of the connected Web sites on at least a part of a display.

Classifications:

International (IPC 8-9): G06F3/00 G06F3/0487 G06Q50/00
G06Q50/30 H04L29/08 H04N21/2362 H04N21/258 H04N21/2665
H04N21/2668 H04N21/431 H04N21/472 H04N21/475 H04N21/4782
H04N21/4788 H04N21/482 H04N21/61 H04N5/44 H04N7/14
H04N7/173 (Advanced/Invention);
H04N21/2362 H04N21/258 (Advanced/Non-invention)
CPC: H04L67/02 H04N21/2362 H04N21/25875 H04N21/4788
H04N21/6125 H04N21/6175 H04N5/4401 H04N5/4403 H04N2005/441
H04N7/141 G06F3/0487 H04N21/2665 H04N21/2668 H04N21/4312
H04N21/482 H04N7/173 G06Q50/01 H04N21/258 H04N21/472
European: G06F3/0487 H04L29/08N1 H04N21/2665 H04N21/2668
H04N21/431L H04N21/4788 H04N21/482 H04N21/61D3 H04N7/14A
T04N21/2362 T04N21/258U1
US: 1/1 715/741 715/741P 725/44



Family:

Publication number	Publication date	Application number	Application date
CN103053174 A	20130417	CN201080068492	20101217
CN103053174 B	20170728	CN201080068492	20101217
DE202010018023 U1	20140116	DE201020018023U	20101217
EP2583470 A1	20130424	EP20100853307	20101217
EP2583470 A4	20141105	EP20100853307	20101217
KR101699736 B1	20170125	KR20100057640	20100617
KR20110137613 A	20111223	KR20100057640	20100617
US2011314386 AA	20111222	US20100972357	20101217
US2016255297 AA	20160901	US20160154421	20160513
US9363470 BB	20160607	US20100972357	20101217
WO11159006 A1	20111222	WO2010KR09072	20101217

Priority:

KR20100057640 20100617 US20100355962P 20100617 US20100972357 20101217
WO2010KR09072 20101217 US20160154421 20160513

Probable Assignee: LG ELECTRONICS INC

Assignee(s): (std): AHN JEONG HEE ; AHN JEONGHEE ; JEONG YOUNG HO ; JEONG YOUNGHO ; LG ELECTRONICS INC

Inventor(s): (std): AHN JEONG HE ; AHN JEONG HEE ; AHN JEONGHEE ; JEONG YOUNG HO ; JEONG YOUNGHO

Agent(s): FRENKEL MATTHIAS ALEXANDER; BIRCH STEWART KOLASCH AND BIRCH LLP; PARK BYUNG CHANG

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU
CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL
IN IS IT JP KE KG KM KN KP KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW
MX MY MZ NA NE NG NI NL NO NZ OM PE PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN ST SV
SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

Title: IMAGE DISPLAY APPARATUS AND METHOD FOR OPERATING THE SAME

Abstract: A multifunctional display device and a method of **controlling** the same are disclosed herein. The method may include displaying a first card object in a first area of the display, displaying a second card object in a second area of the display, and partially displaying a third card object in a third area of the display to indicate that additional card objects are available for display. The first card object may be configured to display a video image and the second card object may have at least one link for connecting to a server. Based on a request to change the displayed card objects, the third card object may be displayed in the second area of the display and a fourth card object may be partially displayed in the third area of the display.

Classifications:**International (IPC 8-9):** G06F13/00 G06F3/00 G06F3/0346

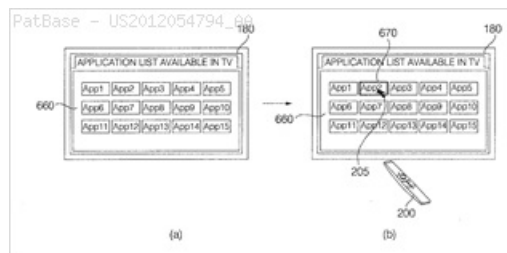
G06F3/048 G06F3/0482 G06F3/0485 G09F3/00 H04N21/40

H04N21/422 H04N21/43 H04N21/431 H04N21/462 H04N21/482

H04N21/8545 H04N5/44 H04N5/445 (Advanced/Invention)

CPC: H04N21/4622 G06F3/0346 G06F3/0482 G06F3/0485

H04N21/42204 H04N21/4312 H04N21/4823 H04N21/8545 H04N5/445

European: H04N21/431L H04N21/8545**US:** 725/37 725/37S 725/38 725/38P 725/43 725/44 725/48**Family:**

Publication number	Publication date	Application number	Application date
CN103081501 A	20130501	CN201180042427	20110311
CN103081501 B	20161012	CN201180042427	20110311
DE202011110780 U1	20160616	DE201120110780U	20110311
EP2612493 A1	20130710	EP20110822011	20110311
EP2612493 A4	20150304	EP20110822011	20110311
KR101709470 B1	20170223	KR20100086151	20100902
KR20120023890 A	20120314	KR20100086151	20100902
US2012054794 AA	20120301	US20110044340	20110309
US2014130098 AA	20140508	US20130037749	20130926
US8572653 BB	20131029	US20110044340	20110309
US8931003 BB	20150106	US20130037749	20130926
WO12030036 A1	20120308	WO2011KR01697	20110311

Priority:

US20100379369P 20100901 KR20100086151 20100902 US20110044340 20110309
 EP20110822011 20110311 WO2011KR01697 20110311 US20130037749 20130926

Probable Assignee: LG ELECTRONICS INC**Assignee(s):** (std): KIM JAE UK ; KIM JAEUK ; KIM JONG SEOK ; KIM JONGSEOK ; LG ELECTRONICS INC**Inventor(s):** (std): KIM JAE UK ; KIM JAEUK ; KIM JONG SEOK ; KIM JONGSEOK**Agent(s):** BEYER ANDREAS; KED AND ASSOCIATES LLP; PARK BYUNG CHANG

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU
 CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL
 IN IS IT JP KE KG KM KN KP KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW
 MX MY MZ NA NE NG NI NL NO NZ OM PE PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN ST SV
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Title: COHERENT PRESENTATION OF MULTIPLE REALITY AND INTERACTION MODELS

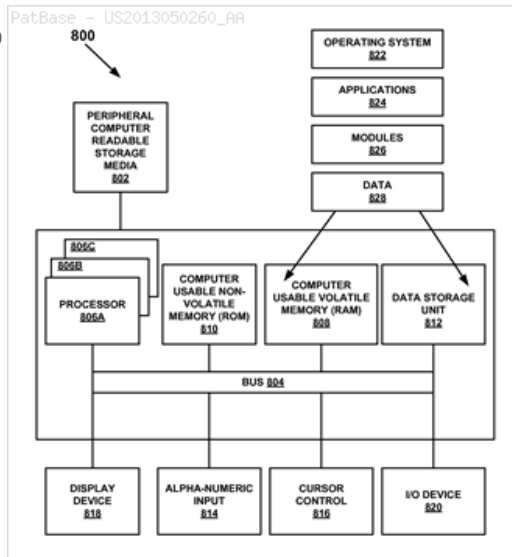
Abstract: A method for navigating concurrently and from point-to-point through multiple reality models is described. The method includes: generating, at a processor, a first navigatable virtual view of a first location of interest, wherein the first location of interest is one of a first virtual location and a first non-virtual location; and concurrently with the generating the first navigatable virtual view of the first location of interest, generating, at the processor, a second navigatable virtual view corresponding to a current physical position of an object, such that real-time sight at the current physical position is enabled within the second navigatable virtual view.

Classifications:

International (IPC 8-9): G06F3/01 G06F3/0484 G06F9/44 G06Q40/00 G06T11/60 G06T13/40 G06T15/00 G06T19/00 G09G5/00 G09G5/377 H04L12/24 H04N5/262 (Advanced/Invention)

CPC: G06Q40/00 G06F3/0484 H04L41/00 G06T11/60 G06T2219/024 G06T19/00 G06T13/40 G06F3/04815 A63F2300/204 A63F2300/6676 A63F2300/69 A63F2300/8076 G06Q30/02 G06T19/006 G06Q50/01 G09G3/003 G09G5/14 G09G2354/00 G06Q10/02 G06F3/14 G09G2340/12 G09G2340/14 H04L65/4084 H04L65/4092 H04L65/605 G06F3/005 G09G5/377 A63F13/65 G06F3/011 G06F8/315 H04N13/366 H04N13/30

US: 1/1 345/156 345/419 345/633 705/35 709/223 715/765 717/116



Family:

Publication number	Publication date	Application number	Application date
US2013050260 AA	20130228	US20120594477	20120824
US2013219357 AA	20130822	US20130841581	20130315
US2013222371 AA	20130829	US20130844351	20130315
US2013226758 AA	20130829	US20130842936	20130315
US2013229433 AA	20130905	US20130843219	20130315
US2013232430 AA	20130905	US20130843210	20130315
US2013234933 AA	20130912	US20130842280	20130315
US2013235034 AA	20130912	US20130842903	20130315
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US2014063061 AA	20140306	US20130844415	20130315
US8963916 BB	20150224	US20130841077	20130315
US9274595 BB	20160301	US20120594477	20120824
WO13032955 A1	20130307	WO2012US52401	20120824

Priority:

US20110575789P	20110826	US20110575790P	20110826	US20110575791P	20110826
US20120594477	20120824	US20130841523	20130315	US20130841077	20130315
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US20130844481	20130315	US20130844415	20130315		

Probable Assignee: REINCLOUD CORP

Assignee(s): (std): REINCLOUD CORP ; REITAN DAN

Inventor(s): (std): REITAN DAN

Agent(s): TRAN AND ASSOCIATES; BAO; GALLENSON MAVIS S ET AL

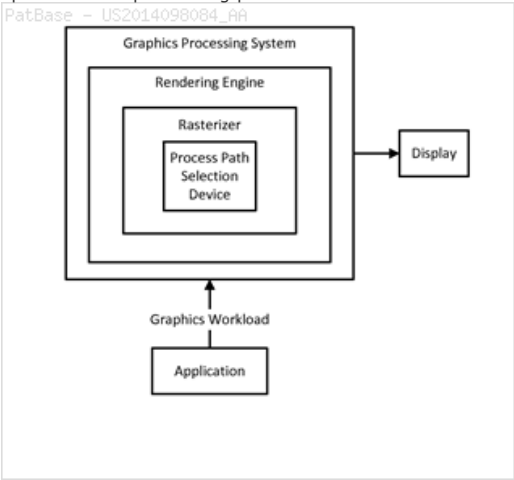
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Title: REDUCED BITCOUNT POLYGON RASTERIZATION

Abstract: Techniques are disclosed for carrying out rasterization of a given graphics workload, wherein portions of the workload associated with relatively high bit count operations are processed via a first process path, and portions of the workload associated with relatively lower bit count operations are processed via a second, relatively faster process path, in accordance with an embodiment. In a more general sense, maximal bit count associated with a given primitive can be identified and compared to a threshold to determine which one of multiple available processing paths can be used.

Classifications:

International (IPC 8-9): G06T11/40 G06T15/00 G06T15/10
G06T17/20 (Advanced/Invention)
CPC: G06T17/20 G06T2200/04 G06T11/40 G06T15/10 G06T2210/12
US: 1/1 345/419



Family:

Publication number	Publication date	Application number	Application date
CN104603844 A	20150506	CN201380046557	20130923
CN104603844 B	20170517	CN201380046557	20130923
DE112013004920 T5	20150618	DE201311004920T	20130923
KR101700174 B1	20170126	KR20157006001	20130923
KR20150041101 A	20150415	KR20157006001	20130923
US2014098084 AA	20140410	US20120647071	20121008
US9218679 BB	20151222	US20120647071	20121008
WO14058604 A1	20140417	WO2013US61166	20130923

Priority:

US20120647071 20121008 WO2013US61166 20130923

Probable Assignee: INTEL CORP

Assignee(s): (std): BENTY NIR ; INTEL CORP ; SUBAG JACOB

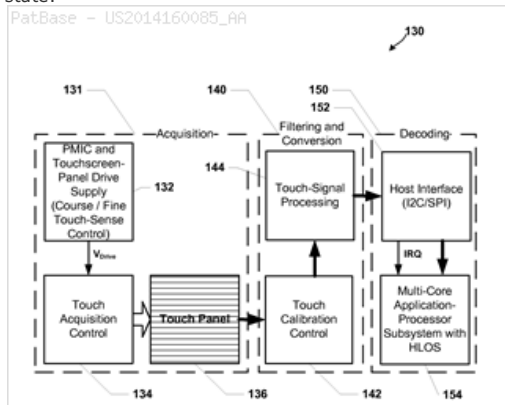
Inventor(s): (std): SUBAG JACOB ; BENTY NIR

Agent(s): FINCH AND MALONEY PLLC; MALONEY NEIL F

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BN BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PA PE PG PH PL PT QA RO RS RU RW SA SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

Title: ADAPTIVE ANALOG-FRONT-END TO OPTIMIZE TOUCH PROCESSING

Abstract: In various aspects a host processor and a touchscreen **controller** may concurrently process a signal sent by a touch transducer when a touch is detected. A touch signal sent directly from a touch transducer may be formatted into a calibrated touch signal that the host processor can process. The calibrated touch signal may allow the host processor to receive and process touch signals from transducers and touchscreens from various manufacturers. The touchscreen **controller** and the host processor may each independently assess the touch-processing state. The touch-processing state may be set to one of a plurality of "states," and the plurality of states may have rules for transitioning between the states. In various aspects, a host processor and a touchscreen **controller** may dynamically set, among other things, the scanning rate and resolution of a touchscreen panel based on the touch-processing state.

Classifications:**International (IPC 8-9):** G06F1/32 G06F3/041 (Advanced/Invention)**CPC:** G06F3/0416 G06F1/3262 G06F3/0418**US:** 345/178**Family:**

Publication number	Publication date	Application number	Application date
TW201432526 A	20140816	TW20130139979	20131104
US2014160085 AA	20140612	US20120707932	20121207
WO14088720 A1	20140612	WO2013US66520	20131024

Priority:

US20120707932 20121207

Probable Assignee: QUALCOMM INC**Assignee(s):** (std): QUALCOMM INC**Inventor(s):** (std): MISHRA LALAN ; RABII KHOSRO M**Agent(s):** COLE NICHOLAS ALBERT

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BN BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IR IS IT JP KE KG KM KN KP KR KZ LA LC LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PA PE PG PH PL PT QA RO RS RU RW SA SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

Title: SYSTEM AND METHOD FOR A COMPREHENSIVE INTEGRATED EDUCATION SYSTEM

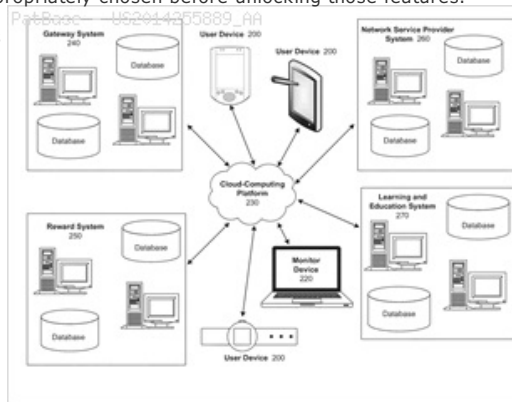
Abstract: A gateway system for providing a comprehensive integrated education system. It is implemented through a cloud-computing platform, linking together learning and assessment systems, which provide learning, education, and evaluation services for a user, and reward systems, which provide rewards for a user for successfully completing learning and education processes, which are connected to the cloud-computing platform. In some embodiments, a user is a student, and a desired reward is access to certain features on the student's electronic computing device. The gateway system ensures that the student correctly answers certain test questions which have been appropriately chosen before unlocking those features.

Classifications:

International (IPC 8-9): G06Q50/20 G09B19/00 G09B5/00 G09B5/08 G09B5/12 G09B7/02 G09B7/08 H04L29/06 H04W12/08 H04W4/02 H04W4/14 H04W48/02 (Advanced/Invention)

CPC: G09B5/00 G06Q30/0207 G06Q30/0241 G06F21/62 G06F2221/2137 G06F2221/2149 G06Q10/101 G06Q50/01 G09B7/04 G06F2221/2113 G06Q50/20 G09B7/02 G09B7/08 G09B19/00 H04W4/023 H04W4/14 H04W12/08 G09B5/125 G09B5/08 H04W4/029

US: 434/236 434/350 434/362 726/18



Family:

Publication number	Publication date	Application number	Application date
US2014255889 AA	20140911	US20140203418	20140310
US2014272894 AA	20140918	US20140203405	20140310
US2015007307 AA	20150101	US20140488839	20140917
US2017076623 AA	20170316	US20160361422	20161126
US2017178526 AA	20170622	US20170436793	20170218
WO14164567 A1	20141009	WO2014US22849	20140310

Priority:

US20130775623P	20130310	US20130777178P	20130312	US20130778988P	20130313
US20130782006P	20130314	US20130879138P	20130918	US20140203405	20140310
US20140203418	20140310	US20140207261	20140312	US20140213928	20140314
US20140488839	20140917	US20160361422	20161126	US20170436793	20170218

Probable Assignee: EDULOCK INC

Assignee(s): (std): EBRAVIUM INC ; EDULOCK INC

Inventor(s): (std): GRIMES CODY M ; GRIMES LINDA S ; GRIMES PATRICK M

Agent(s): COLEMAN BRIAN R ET AL

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BN BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IR IS IT JP KE KG KM KN KP KR KZ LA LC LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PA PE PG PH PL PT QA RO RS RU RW SA SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

Title: SYSTEM AND METHOD FOR INSTRUCTION BASED ACCESS TO ELECTRONIC COMPUTING DEVICES

Abstract: A network-based security system and method for blocking and/or limiting access to a user's electronic computing device (e.g. tablet) until the user answers one or more test questions correctly. The user has the option of selecting the Content (e.g. topic); the Format (e.g. True/False); and the Quantity (e.g. amount of access earned) for each test. The Quantity may be: Open Access with unlimited time constraints, Timed Access with set periods of time to use the device, and Cumulative Access to earn credit points that may be saved for future device access. The entire device may be blocked, or particular functions (e.g. Internet) or software (e.g. **games**) on the device. The system analyzes the user's test results and trends in test performance and electronically transmits a report to a third party to enable the party to monitor the progress and/or deficiencies of the user.

Classifications:

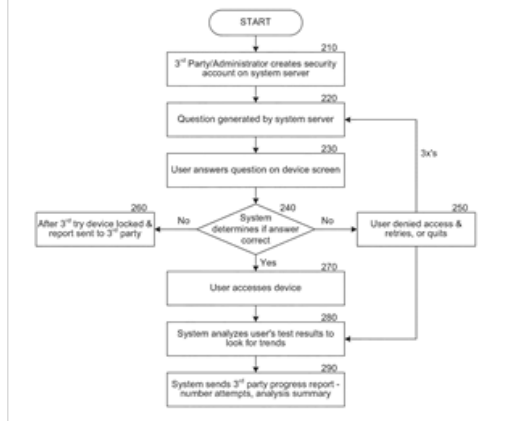
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CPC: G06F2221/2137 G06F2221/2149 G06Q30/0207 G09B7/04

G09B7/08 G06F21/31 G06Q30/0231

US: 705/14.31 726/28

PatBase - US2014278895_AA

**Family:**

Publication number	Publication date	Application number	Application date
US2014278895 AA	20140918	US20140207261	20140312

Priority:

US20130777178P 20130312 US20140207261 20140312

Probable Assignee: EDULOCK INC

Assignee(s): (std): EDULOCK INC

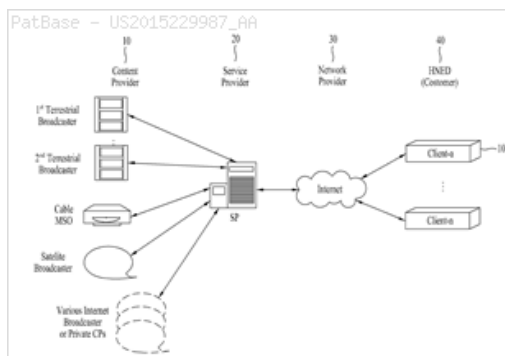
Inventor(s): (std): GRIMES CODY M ; GRIMES LINDA S ; GRIMES PATRICK M

Title: DIGITAL DEVICE AND METHOD FOR **CONTROLLING** THE SAME

Abstract: A method of **controlling** a digital television, the method includes displaying content or application being executed on a display screen of the digital television, receiving a first signal requesting a menu to be displayed, and displaying the requested menu, overlaid on the display screen, wherein the menu includes a first part and a second part, wherein the first part displays at least one menu item corresponding to previously displayed content or previously executed application, and wherein the second part displays at least one menu item corresponding to content or an application executable in the digital television.

Classifications:

International (IPC 8-9): G06F3/048 H04N21/23 H04N21/236 H04N21/25 H04N21/2665 H04N21/27 H04N21/422 H04N21/431 H04N21/442 H04N21/45 H04N21/458 H04N21/466 H04N21/472 H04N21/4722 H04N21/4782 H04N21/81 H04N21/8549 H04N21/858 (Advanced/Invention)
CPC: H04N21/4316 H04N21/44222 H04N21/4532 H04N21/4586 H04N21/4667 H04N21/4668 H04N21/8133 H04N21/858 H04N21/8173 H04N21/4312 H04N21/27 H04N21/2665 H04N21/4782 H04N21/251 G06Q30/0269 H04N21/47202 H04N21/42208 H04N21/4722 G06F3/0482 H04N7/0884 H04N21/4438 H04N21/47208 H04N21/482
US: 725/34 725/59



Family:

Publication number	Publication date	Application number	Application date
CN105814898 A	20160727	CN201480067977	20141215
DE202014011066 U1	20171012	DE201420011066U	20141215
EP3089456 A1	20161102	EP20140873742	20141215
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KR101567832 B1	20151110	KR20150012807	20150127
KR20150075010 A	20150702	KR20140112973	20140828
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WO15099343 A1	20150702	WO2014KR12326	20141215

Priority:

US20130920473P	20131224	KR20140112973	20140828	KR20140112974	20140828
EP20140873742	20141215	US20140437681	20141215	WO2014KR12326	20141215
KR20150012807	20150127	US20150694873	20150423		

Probable Assignee: LG ELECTRONICS INC

Assignee(s): (std): LG ELECTRONICS INC

Inventor(s): (std): ALAN THOMAS SHEN ; BEHNKE BRIAN ; BRIAN BEHNKE ; DAMIR LIRON ; DANIELLE ZIMMERMAN ; DAVID KEMPE ; DAVIS MOLLY ; HERBERT HAN PU WANG ; ITAI VONSHAK ; KEMPE DAVID ; KIM DAE GU ; KIM DAEGU ; LIRON DAMIR ; MOLLY DAVIS ; SHEN ALAN THOMAS ; VONSHAK ITAI ; WANG HERBERT HAN PU ; ZIMMERMAN DANIELLE

Agent(s): KATERLE AXEL; BIRCH STEWART KOLASCH AND BIRCH LLP; KIM YONG IN ET AL

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BN BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IR IS IT JP KE KG KM KN KP KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PA PE PG PH PL PT QA RO RS RU RW SA SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

Title: SAMPLING, FAULT MANAGEMENT, AND/OR CONTEXT SWITCHING VIA A COMPUTE PIPELINE

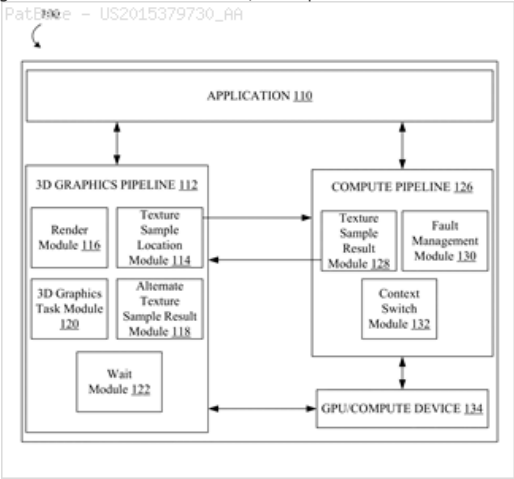
Abstract: Apparatuses, systems, and methods may sample a texture, manage a page fault, and/or switch a context associated with the page fault. A three-dimensional (3D) graphics pipeline may provide texture sample location data corresponding to a texture, wherein sampling of the texture is to be executed external to the 3D graphics pipeline. A compute pipeline may execute sampling of the texture utilizing the texture sample location data and provide texture sample result data corresponding to the texture, wherein the 3D graphics pipeline may composite a frame utilizing the texture sample result data. The compute pipeline may manage a page fault, wherein the page fault and/or management of the page fault may be hidden from a graphics application. In addition, the compute pipeline may switch a compute context associated with the page fault to allow a graphics task not associated with the page fault to be executed and/or to prevent a stall.

Classifications:

International (IPC 8-9): G06T1/20 G06T1/60 G06T15/00 G06T15/04 G06T7/40 (Advanced/Invention)

CPC: G06T15/04 G06T1/60 G06T1/20 G06T15/005

US: 1/1 345/619



Family:

Publication number	Publication date	Application number	Application date
CN105321142 A	20160210	CN201510276147	20150526
DE102015006750 A1	20151231	DE201510006750	20150526
GB201508928 A0	20150701	GB20150008928	20150526
GB2528558 A1	20160127	GB20150008928	20150526
GB2528558 B2	20180509	GB20150008928	20150526
TW201604831 A	20160201	TW20150116809	20150526
TWI576784 B	20170401	TW20150116809	20150526
US2015379730 AA	20151231	US20140318562	20140627
US9449363 BB	20160920	US20140318562	20140627

Priority:

US20140318562 20140627

Probable Assignee: INTEL CORP

Assignee(s): (std): FLIFLET BRANDON L ; INTEL CORP ; RAO JAYANTH N ; TSAKOK JOHN A

Inventor(s): (std): BRANDON L FLIFLET ; FLIFLET BRANDON L ; JAYANTH N RAO ; JOHN A TSAKOK ; RAO JAYANTH N ; TSAKOK JOHN A

Agent(s): JORDAN IP LAW LLC

Title: GRAPHICS PROCESSING UNIT AND DEVICE INCLUDING THE SAME

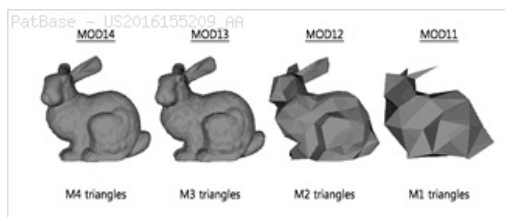
Abstract: A graphics processing unit (**GPU**) for determining whether to perform tessellation on a first model according to a **control** of a central processing unit (CPU) is provided. The **GPU** reads the first model from a memory, which stores prepared models having different complexities; calculates a complexity of the first model; compares the calculated complexity with a reference complexity; and determines whether to perform a tessellation operation on the first model according to a comparison result.

Classifications:

International (IPC 8-9): G06T1/20 G06T1/60 G06T15/00 G06T17/20 G06T19/20 (Advanced/Invention)

CPC: G06T1/20 G06T17/20 G06T1/60 G06T2200/28 G06T19/20 Y02D10/171

US: 345/423



Family:

Publication number	Publication date	Application number	Application date
CN105654418 A	20160608	CN201510844088	20151126
DE102015117768 A1	20160602	DE201510117768	20151019
KR20160063648 A	20160607	KR20140167153	20141127
US2016155209 AA	20160602	US20150823476	20150811

Priority:

KR20140167153 20141127

Probable Assignee: SAMSUNG ELECTRONICS CO LTD

Assignee(s): (std): KIM SEOK HOON ; SAMSUNG ELECTRONICS CO LTD ; YU CHANG HYO

Assignee(s): SAMSUNG ELECTRONICS CO

Inventor(s): (std): KIM SEOK HOON ; YU CHANG HYO