

PatBase - Search results 13 August 2020

Search query (19): [SP]: A hysteresis power supply circuit, comprising: an input circuit that is connected to a power supply input terminal, rectifies a power supply input and provides isolation protection for the input terminal; a switch combination circuit that is connected to an input circuit and is turned on or off to control an output power of a power supply output terminal; a sampling and control circuit that samples an output voltage, compares the output voltage with a reference voltage, controls the switch combination circuit to be turned on or off according to the comparison result; a feedback circuit that is connected between the switch combination circuit and the sampling and control circuit, wherein the switch combination circuit outputs a positive feedback signal to the sampling and control circuit through the feedback circuit, and the positive feedback signal causes a hysteresis effect on a comparison action of a reference power supply chip; an output circuit that is connected to the switch combination circuit, filters an output of the switch combination circuit and provides isolation protection for an output terminal, wherein the output of the output circuit serves as an output of the hysteresis power supply circuit.

Results: 11

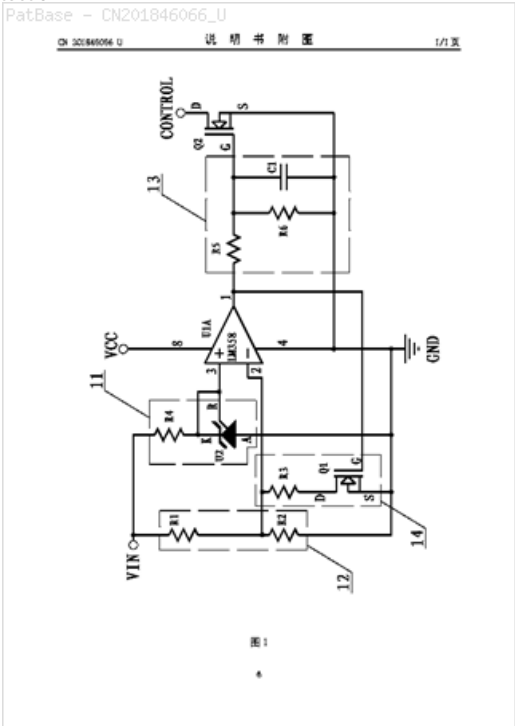
1) Family number: 48988134 (CN201846066U) © PatBase

Title: Undervoltage protective circuit with delaying function

Abstract: The utility model relates to the technical field of undervoltage protective circuits, in particular to an undervoltage protective circuit with a delaying function. The undervoltage protective circuit comprises a comparison circuit, an output unit circuit, a reference unit circuit, an input voltage detection unit circuit, a switching tube Q2 and a delaying unit circuit, wherein the input voltage detection unit circuit comprises a resistor R1 and a resistor R2; one end of the resistor R1 is connected with the input voltage end of a switch power supply; the other end of the resistor R1 is connected with one end of the resistor R2 and the detection voltage input end of the comparison circuit; the other end of the resistor R2 is grounded; the delaying unit circuit comprises a resistor R3 and a filed-effect tube Q1; one end of the resistor R3 is connected with one end of the resistor R2; the other end of the resistor R3 is connected with a drain electrode of the filed-effect tube Q1; and the grid electrode of the filed-effect tube Q1 is connected with the output end of the comparison circuit. The undervoltage protective circuit with the delaying function can avoid the frequent switch on or off of the switch power supply when the input voltage of the switch power supply fluctuates near the set voltage for the undervoltage protection.

Classifications:

International (IPC 8-9): H02H3/24 H02H7/10 (Advanced/Invention)



Family:

Publication number	Publication date	Application number	Application date
CN201846066 U	20110525	CN201020615559U	20101119

Priority:

CN201020615559U 20101119

Probable Assignee: DONGGUAN AUYUAN ELECTRONICS TECH CO LTD

Assignee(s): (std): DONGGUAN AUYUAN ELECTRONIC TECHNOLOGY CO LTD ; DONGGUAN AUYUAN ELECTRONICS TECH CO LTD

Assignee(s): DONGGUAN AUYUAN ELECTRONICS TECH CO

Inventor(s): (std): LAIYUAN LI ; XINGDE ZHANG ; SHUANGXIU CAO

Inventor(s): LI LAIYUAN ; ZHANG XINGDE ; CAO SHUANGXIU

2) Family number: 58311214 (CN204118721U) © PatBase

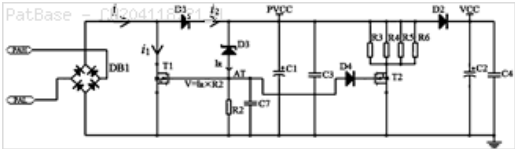
Title: ELECTRONIC RELEASE DEVICE POWER ENERGY DISCHARGING SIMULATION LOAD CIRCUIT AND DISCHARGING MANAGEMENT CIRCUIT

Abstract: The utility model discloses an electronic release device power energy discharging simulation load circuit and discharging management circuit, belonging to the technical field of a low voltage distribution system. The discharging simulation load circuit includes a simulation load discharging resistor, a discharging power transistor T2, and an isolating

diode D4, one end of the simulation load discharging resistor is connected with the drain electrode of the discharging power resistor T2, the grid electrode of the discharging power transistor T2 is connected with the cathode of the isolating diode D4, and the source electrode of the discharging power transistor T2 is grounded. The discharging simulation load circuit and the discharging management circuit can meet the requirement of an electronic release device on lower current trip point, and can reduce the secondary current output of a current transformer under the condition that the current transformer supplies enough energy, thereby preventing the current transformer from heating and protecting the electronic release device working normally.

Classifications:

International (IPC 8-9): H02H9/02 (Advanced/Invention)



Family:

Publication number	Publication date	Application number	Application date
CN104300520 A	20150121	CN201410620481	20141106
CN104300520 B	20170616	CN201410620481	20141106
CN204118721 U	20150121	CN201420660851U	20141106

Priority:

CN201410620481 20141106 CN201420660851U 20141106

Probable Assignee: DAQO GROUP CO LTD

Assignee(s): (std): DAQO GROUP CO LTD ; NANJING DAQUAN ELECTRIC RESEARCH INST CO LTD

Assignee(s): DAQO GROUP CO

Inventor(s): (std): TANG FEITING ; LIU BIN

3) Family number: 48790240 (US2011095732A)

PatBase

Title: POWER FACTOR CORRECTION CIRCUIT AND DRIVING METHOD THEREOF

Abstract: The present invention relates to a power factor correction circuit and a driving method thereof. The power factor correction circuit receives an input voltage and maintains an output voltage at a constant level by controlling switching operation of a power switch connected to an inductor that supplies the output voltage. In this case, the power factor correction circuit controls switching operation of the power switch by differentiating a control structure for an output voltage respectively according to a stabilization period during which the output voltage is constantly maintained and a start-up period during which the output voltage is increased before being stabilized. In addition, the power factor correction circuit controls the switching operation of the power switch according to the control structure of the start-up period during a predetermined correction delay period from a time that the stabilization period starts.

Classifications:

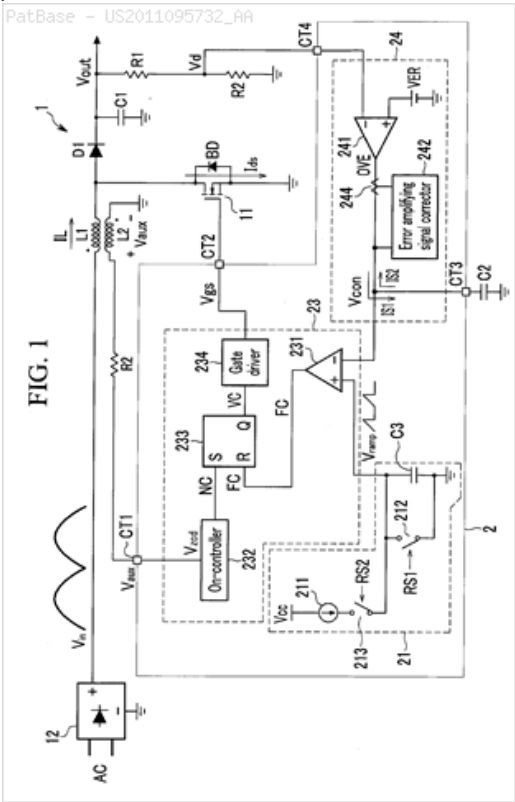
International (IPC 8-9): G05F1/70 G05F5/00 H02M1/42

H02M3/155 (Advanced/Invention)

CPC: G05F1/70

European: G05F1/70

US: 323/207 323/207P 363/49 363/89 363/90 363/97



Family:

Publication number	Publication date	Application number	Application date
KR101677728 B1	20161121	KR20090101679	20091026
KR20110045219 A	20110504	KR20090101679	20091026
US2011095732 AA	20110428	US20100912181	20101026
US8513926 BB	20130820	US20100912181	20101026

Priority:

KR20090101679 20091026

Probable Assignee: SEMICONDUCTOR COMPONENTS IND LLC

Assignee(s): (std): FAIRCHILD KR SEMICONDUCTOR LTD ; FAIRCHILD SEMICONDUCTOR INT INC ; KIM BYOUNG HEON ; MOON SANG CHEOL ; MOON SANGCHEOL ; PARK YOUNG BAE

Assignee(s): SEMICONDUCTOR COMPONENTS IND LLC ; DEUTSCHE BANK NEW YORK BRANCH COLLATERAL AGENT AG AS ; FAIRCHILD KOREA SEMICONDUCTOR LTD

Inventor(s): (std): KIM BYOUNG HEON ; MOON SANG CHEOL ; MOON SANGCHEOL ; PARK YOUNG BAE

Agent(s): OKAMOTO AND BENEDICTO LLP

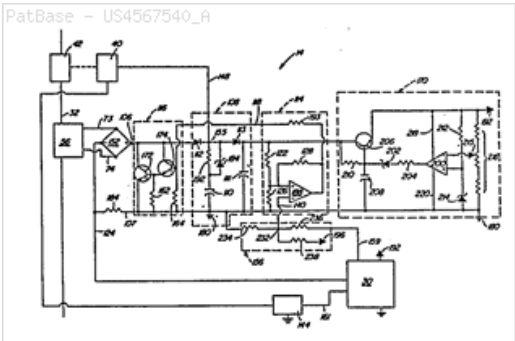
4) Family number: 2467254 (US4567540A)

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Title: POWER SUPPLY FOR A CIRCUIT INTERRUPTER

Abstract: A power supply is provided for energizing a trip signal generator and for selectively operating a device in response to the detection by the trip signal generator of overcurrent in a alternating current line. The power supply receives input of full-wave rectified current that is directly proportional to the alternating current in the line. A regulator circuit is also provided to power the trip signal generator and other circuitry. When current initially flows in the line the regulator rapidly produces an output voltage approximately equal to the input voltage up to a first voltage level. When the input voltage exceeds that level, the output voltage of the regulator increases at a slower rate than the input voltage to permit regulation of the output voltage. When energized, the output voltage is regulated at a predetermined constant level. The power supply also includes an energy storage circuit for supplying the input voltage to the regulator and to a circuit interrupter when desired. A control portion in conjunction with a switch portion alternately shunts current away from or permits current to flow to the energy storage portion and regulator portion to maintain the voltage across the energy storage portion within a predetermined range. The predetermined voltage range of the energy storage circuit is shifted higher upon receipt of a signal from the trip signal generator to increase the amount of energy stored in the energy storage circuit for interrupting the circuit in the line.

Classifications:
International (ipc 8-9): H02H1/06 H02H3/08 (Advanced/Invention); H02H1/00 (Core/Invention)
International (ipc 1-7): H02H1/06 H02H3/08
CPC: H02H1/066
European: H02H1/06B2
US: 361/93 361/93.6 361/98





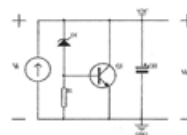


图 1

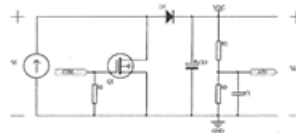


图 2

7

Family:

Publication number	Publication date	Application number	Application date
CN103051185 A	20130417	CN201210596912	20121231
CN103051185 B	20150617	CN201210596912	20121231
CN203193508 U	20130911	CN201220755422U	20121231

Priority:

CN201210596912 20121231 CN201220755422U 20121231

Probable Assignee: ZHEJIANG DELING SCIENCE AND TECHNOLOGY CO LTD**Assignee(s):** (std): ZHEJIANG DELING SCIENCE AND TECHNOLOGY CO LTD ; ZHEJIANG DELING TECHNOLOGY CO LTD**Assignee(s):** ZHEJIANG DELING SCIENCE AND TECHNOLOGY CO**Inventor(s):** (std): YE PENG ; CHEN CHONG ; HUANG FENGYONG ; ZHANG TENGXIANG**7) Family number: 18388736 (US5124875A)**

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Title: OVERCURRENT PROTECTION APPARATUS

Abstract: A current of an alternating current electric line is detected by a current transformer, the output of the current transformer is rectified by a rectifier, a series connection of a switching means and a resistor is coupled across the output terminals of the rectifier, a control circuit composed of a capacitor, a comparator and a reference voltage generator is coupled in parallel to the switching means through a diode, when the DC output voltage exceeds a predetermined voltage based on the reference voltage of the reference voltage generator, the comparator outputs a signal for closing the switching means, and the voltage which is applied to the control circuit is limited to a predetermined value.

Classifications:**International (IPC 8-9):** H02H1/06 H02H3/08 H02H3/087

H02H3/093 (Advanced/Invention);

H02H3/093 (Core/Invention)

International (IPC 1-7): H02H1/06 H02H3/08 H02H3/087 H02H3/093

H02H3/26

CPC: H02H1/066**European:** H02H1/06B2**US:** 361/42 361/45 361/93 361/93.5 361/94

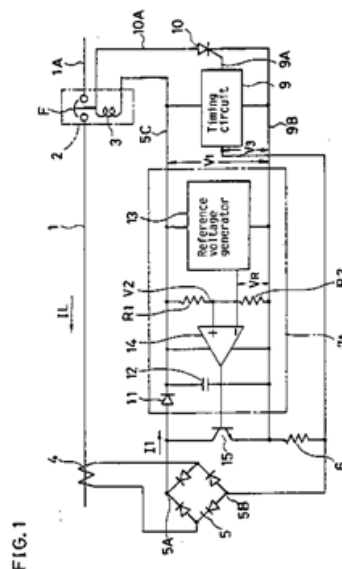


FIG. 1

Family:

Publication number	Publication date	Application number	Application date
DE69028171 D1	19960926	DE19906028171	19900920
DE69028171 D1	19960926	DE19906028171T	19900920
DE69028171 T2	19970123	DE19906028171T	19900920
EP0421204 A2	19910410	EP19900118133	19900920
EP0421204 A3	19920408	EP19900118133	19900920
EP0421204 B1	19960821	EP19900118133	19900920
EP0687050 A2	19951213	EP19950113775	19900920
EP0687050 A3	19960522	EP19950113775	19900920
JP2133569 C3	19971128	JP19890257876	19891004
JP3124215 A2	19910527	JP19890257876	19891004
JP7010145 B4	19950201	JP19890257876	19891004
KR19910008905 A	19910531	KR19900011162	19900723
KR930006942 Y1	19931009	KR19930007559U	19930507
US5124875 A	19920623	US19900568863	19900817
ZA9006490 A	19910626	ZA19900006490	19900816

Priority:

JP19890257876 19891004 KR19900011162 19900723 EP19900118133 19900920
 KR19930007559U 19930507

Probable Assignee: MITSUBISHI ELECTRIC CORP

Assignee(s): (std): MITSUBISHI ELECTRIC CORP

Assignee(s): MITSUBISHI DENKI K K ; MITSUBISHI DENKI K K TOKIO TOKYO JP ; MITSUBISHI DENKI KABUSHIKI KAISHA ; MITSUBISHI DENKI KK

Inventor(s): (std): ISHII KATSUHIRO ; KOUJI HIROTSUNE ; KAZUHIRO ISHII ; ISHII KAZUHIRO ; ISHI KAZUHIRO C O FUKUYAMA SEI ; ISHI KAZUHIRO ; HIROTSUNE KOUJI ; HIROTSUNE KOJI ; HIROTSUNE KOUJI C O FUKUYAMA S

Inventor(s): HIROTSUNE KOUJI 1 8 MIDORIMACHI FUKUYAMA SHI... ; HIROTSUNE KOUJI 1 8 MIDORIMACHI FUKUYAMA SHI 720 JP ; ISHI KAZUHIRO 1 8 MIDORIMACHI FUKUYAMA SHI 720 JP ; HIROTSUNE KOUJI 1 8 MIDORIMACHI FUKUYAMA SHI 720 J ; HIROTSUNE KOUJIUSHO ; HIROTSUNE KOUJI C O FUKUYAMA SEISAKUSHO ; ISHI KAZUHIRO C O FUKUYAMA SEISAKUSHO

Designated states: CH DE FR GB IT LI

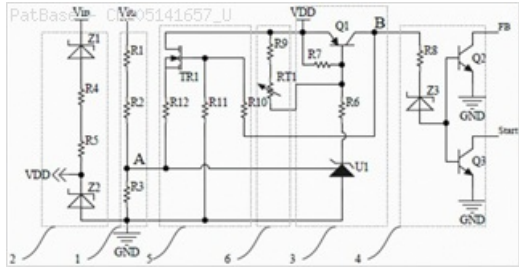
8) Family number: 62033736 (CN205141657U)

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Title: INPUT OVERVOLTAGE CROWBAR

Abstract: The utility model provides an input overvoltage crowbar, includes sampling circuit and switch circuit, still including telegram in reply way and the control circuit of stagnating, stagnant telegram in reply way is connected between sampling circuit and control circuit, control circuit connects between sampling circuit and switch circuit, when busbar voltage is higher than overvoltage protection voltage point, switch circuit is given to the control circuit output high level for the control source gets into input overvoltage protection state, telegram in reply way formation positive feedback circuit stagnates to raise sampling circuit's output voltage, the control circuit's that further rises output voltage, when busbar voltage is less than overvoltage protection voltage point, by the poor DELTA V of stagnant telegram in reply way stagnant back pressure of formation, make control circuit reach at busbar voltage's decline pressure differential could turn -off during the poor DELTA V of the back pressure that stagnates, switch circuit just can withdraw from input overvoltage protection state for let the power resume work. Compared with the prior art, the utility model discloses in can being applied to AC -DC, DC -DC switching power supply, it is high to have the protection precision, and the stagnant telegram in reply that the realization can set up is pressed, and temperature characteristic is good, loss advantage such as little under certain high pressure range.

Classifications:



Family:

Publication number	Publication date	Application number	Application date
CN205141657 U	20160406	CN201520882472U	20151105

Priority:

CN201520882472U 20151105

Probable Assignee: GUANGZHOU MORNSUN SCI AND TECH

Assignee(s): (std): GUANGZHOU MORNSUN SCIENCE AND TECH LTD ; MORNSUN GUANGZHOU SCIENCE AND TECHNOLOGY CO LTD ; GUANGZHOU MORNSUN SCI AND TECH

Assignee(s): GUANGZHOU MORNSUN SCIENCE AND TECHNOLOGY LTD

Inventor(s): (std): LI GUOQIANG

9) Family number: 46694776 (CN101808447B)

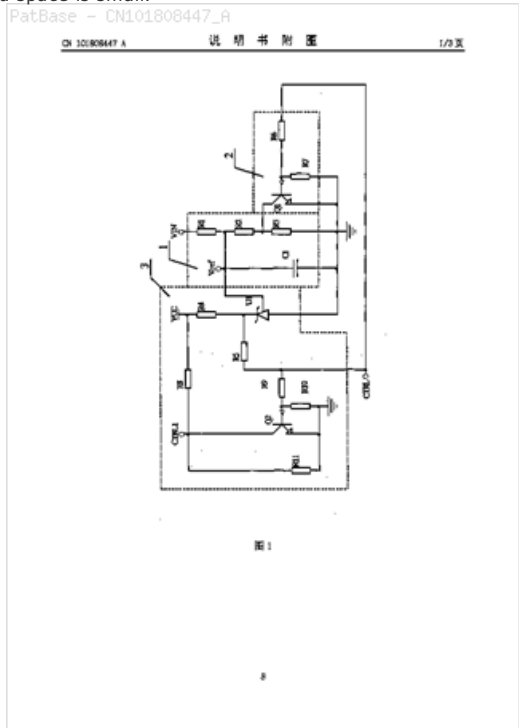
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Title: INPUT OVERVOLTAGE DELAY PROTECTION CIRCUIT, LED CONTROL CIRCUIT AND LED LAMP

Abstract: The invention relates to an input overvoltage delay protection circuit, an LED control circuit and an LED lamp. The input overvoltage delay protection circuit comprises a voltage sampling unit, a protection signal generating unit, and a delay generating unit. The voltage sampling unit is used for sampling DC voltage to be detected to acquire sampling voltage; the protection signal generating unit comprises a three-end adjustable shunt reference source and is used for comparing the sampling voltage with voltage at the reference end of the three-end adjustable shunt reference source to generate a protection control signal; and the delay generating unit is used for adding positive feedback to the comparison side of the sampling voltage and the reference end of the three-end adjustable shunt reference source according to the change of the DC voltage to be detected, so that the sampling voltage changes along with the change of the DC voltage to be detected. Due to the adoption of the three-end adjustable shunt reference source, the input overvoltage delay protection circuit is formed. Therefore, the overvoltage protection control is realized, the precision of the protection voltage is better controlled, the delay voltage range is better set, the cost is low, and the occupied space is small.

Classifications:

International (IPC 8-9): F21S2/00 F21V23/00 H02H3/20
H05B37/02 (Advanced/Invention);
F21Y101/02 (Advanced/Non-invention);
F21S2/00 F21V23/00 H02H3/20 H05B37/02 (Core/Invention)



Family:

Publication number	Publication date	Application number	Application date
CN101808447 A	20100818	CN201010105354	20100129
CN101808447 B	20130327	CN201010105354	20100129

Priority:

CN201010105354 20100129

Probable Assignee: SHENZHEN OCEAN S KING LIGHTING SCIENCE AND TECHNOLOGY CO LTD

Assignee(s): (std): OCEAN S KING LIGHTING SCIENCE AND TECHNOLOGY CO LT ; OCEAN S KING LIGHTING SCIENCE AND TECHNOLOGY CO LTD ; OCEANS KING LIGHTING SCIENCE ; OCEANS KING LIGHTING SCIENCE AND TECHNOLOGY CO LTD ; SHENZHEN OCEAN S KING LIGHTING ENGINEERING CO LTD ; SHENZHEN OCEAN S KING LIGHTING SCIENCE AND TECHNOLOGY CO LTD

Assignee(s): SHENZHEN OCEAN S KING LIGHTING SCIENCE AND TECHNOLOGY CO ; OCEAN S KING LIGHTING

10) Family number: 63760786 (WO18006769A1)

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Title: HYSTERESIS POWER SUPPLY CIRCUIT

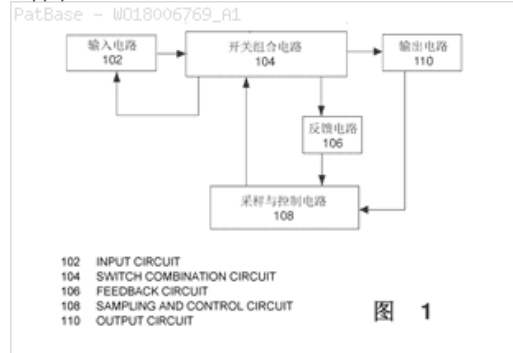
Abstract: A hysteresis power supply circuit, comprising: an input circuit (102), a switch combination circuit (104), a sampling and control circuit (108), a feedback circuit (106) and an output circuit (110). The input circuit (102) is connected to a power supply input terminal, rectifies a power supply input and provides isolation protection for the input terminal. The switch combination circuit (104) is connected to the input circuit (102), and is turned on or turned off for controlling an output power of a power supply output terminal. The sampling and control circuit (108) samples an output voltage, compares the output voltage with a reference voltage, and according to a comparison result, controls the switch combination circuit (104) to be turned on or turned off. The feedback circuit (106) is connected between the switch combination circuit (104) and the sampling and control circuit (108), and the switch combination circuit (104) outputs a positive feedback signal to the sampling and control circuit (108) via the feedback circuit (106), wherein the positive feedback signal causes a comparison action of a reference power supply chip to generate a hysteresis effect. The output circuit (110) is connected to the switch combination circuit (104), filters an output of the switch combination circuit (104) and provides isolation protection for the output terminal, wherein the output thereof serves as an output of a hysteresis power supply circuit.

Classifications:

International (IPC 8-9): H02H1/06 H02M3/145
H02M3/158 (Advanced/Invention)

International (IPC 1-7): H02M3/158

CPC: H02M3/158 H02M3/145 H02H1/066

**Family:**

Publication number	Publication date	Application number	Application date
BR112019000283 A2	20190416	BR20191100283	20170703
CL2019000021 A1	20190517	CL20190000021	20190104
CN106026656 A	20161012	CN201610536928	20160708
CN106026656 B	20190125	CN201610536928	20160708
CN205864260 U	20170104	CN201620718741U	20160708
CO20190000048 A2	20190329	CO20190000048	20190104
EP3484035 A1	20190515	EP20170823581	20170703
EP3484035 A4	20200226	EP20170823581	20170703
MX2019000229 A1	20190906	MX20190000229	20170703
PE20190461 A1	20190403	PE20180003342	20170703
WO18006769 A1	20180111	WO2017CN091418	20170703

Priority:

CN201610536928 20160708 CN201620718741U 20160708 WO2017CN091418 20170703

Probable Assignee: SEARI ELECTRICAL APPARATUS TECHNOLOGY CO LTD

Assignee(s): (std): SEARI ELECTRIC TECH CO LTD ; SEARI ELECTRICAL APPARATUS TECH CO LTD ; SEARI ELECTRICAL APPARATUS TECHNOLOGY CO LTD ; SHANGHAI NOARK ELECTRIC CO LTD ; ZHEJIANG CHINT ELECTRIC APPLIANCE CO LTD ; ZHEJIANG CHINT ELECTRICS CO LTD ; SEARI ELECTRIC TECHNOLOGY CO LTD

Assignee(s): ZHEJIANG CHINT ELECTRICS CO ; ZHEJIANG CHINT ELECTRIC APPLIANCE CO ; SHANGHAI NOARK ELECTRIC CO ; SEARI ELECTRICAL APPARATUS TECHNOLOGY CO ; SEARI ELECTRIC TECHNOLOGY CO

Inventor(s): (std): YINGJUN YIN ; YIN YINGJUN ; YI XIANJUN ; XIANJUN YI ; LIJUN CHEN ; CHEN LIJUN

Agent(s): ESTUDIO CLARKE MODET AND CO CHILE LIMITADA; OSHA LIANG; SHANGHAI PATENT AND TRADEMARK LAW OFFICE LLC

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BN BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DJ DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IR IS IT JO JP KE KG KH KM KN KP KR KW KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PA PE PG PH PL PT QA RO RS RU RW SA SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

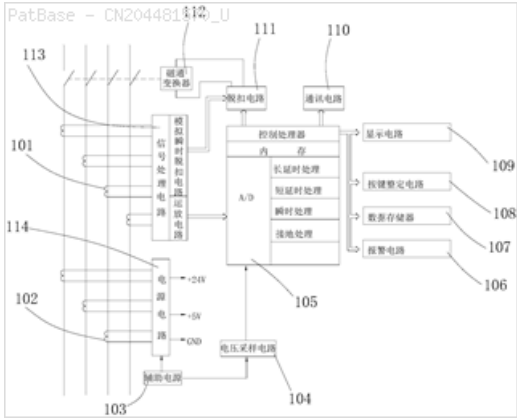
11) Family number: 59768797 (CN204481570U)

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Title: INTELLIGENT CONTROLLER FOR CIRCUIT BREAKER

Abstract: The utility model discloses an intelligent controller for a circuit breaker, and the controller comprises a control processor, a signal processing circuit, and a power circuit. The input end of the signal processing circuit is connected with a hollow sensor, and is used for obtaining a current signal of a main circuit. The output end of the signal processing circuit is connected with an A/D conversion interface of the control processor, and can enable a processed current signal to be supplied to the control processor for judgment and processing through analog-digital conversion. The power circuit is connected with a quickly saturated mutual inductor and an auxiliary power supply, and can be used for supplying working power to the controller. Moreover, a voltage sampling circuit which is used for sampling a voltage value of the auxiliary power supply and transmitting the voltage value to the control processor for judgment and processing is connected between the auxiliary power supply and the A/D conversion interface of the control processor. The controller is high in working stability, is accurate in judgment alarm, and is simple in connection structure.

Classifications:
International (IPC 8-9): H02J13/00 (Advanced/Invention)



Family:

Publication number	Publication date	Application number	Application date
CN204481570 U	20150715	CN201520073323U	20150202

Priority:

CN201520073323U 20150202

Probable Assignee: ZHEJIANG CHINA ELEC APPLIANCE

Assignee(s): (std): ZHEJIANG CHINA ELEC APPLIANCE ; ZHEJIANG CHINT ELECTRICS CO LTD

Assignee(s): ZHEJIANG CHINT ELECTRIC APPLIANCE CO

Inventor(s): (std): ZHANG LONG ; XIAO LEI ; CHEN JIANYU ; MA SHIGANG