

1) Family number: 58776090 (US2015220080A)

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Title: MANAGING BIG DATA IN PROCESS **CONTROL** SYSTEMS

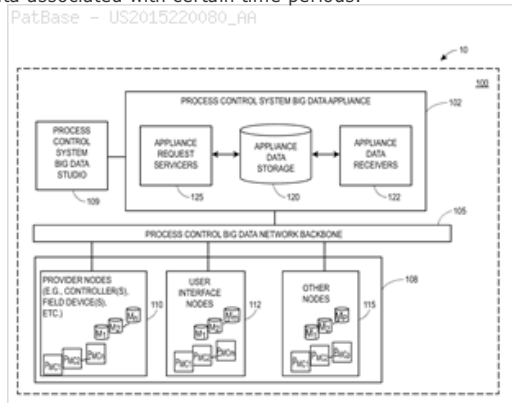
Abstract: A big data network or system for a process **control** system or plant includes a data storage device configured to receive process **control** data from **control** system devices and store the process **control** data. The big data network or system identifies various parameters or attributes from the process **control** data, and creates and uses rowkeys to store the parameters according to various combinations, such as combinations using timestamps. The big data network or system may also store certain aggregate data analyses associated with time periods specified by the timestamps. Accordingly, the big data network or system efficiently stores real-time data having measurements within a database schema, and users or administrators can leverage the aggregate data to analyze certain data associated with certain time periods.

Classifications:

International (IPC 8-9): G05B13/02 G05B15/02 G05B17/02 G05B19/04 G05B19/042 G05B19/048 G05B19/418 G05B23/02 G06F12/00 G06F15/16 G06F15/163 G06F16/21 G06F17/30 G06F21/31 G06F21/44 G06F21/64 G06N5/02 G06N5/04 G06N99/00 G06Q50/04 G07C3/00 G07C3/14 H04L12/24 H04L12/26 H04L12/28 H04L29/06 H04L29/08 H04L9/08 H04L9/32 (Advanced/Invention)

CPC: G05B2219/32339 G05B2219/31167 G05B2219/31088 G05B2219/32128 G05B2219/31455 G05B17/02 G05B2219/31357 G05B2219/1171 G05B2219/14097 G05B2219/14018 G05B19/048 G05B23/02 G05B23/0224 G06F16/2452 G06F16/252 G06N99/005 G05B19/41835 H04L9/0869 H04L9/3226 G06F8/34 G05B19/4184 G05B2219/32191 G05B19/41875 G05B2219/31356 G05B23/0275 G05B19/0421 G05B2219/33333 G05B19/41855 G05B2219/31124 H04L41/0803 H04L43/045 H04L67/10 G05B15/02 G05B19/042 G05B23/0208 G05B2219/15052 G05B19/4183 G05B2219/2609 G05B13/0265 G06F16/00 G05B2219/25232 G05B19/41865 G06F16/22 G05B19/418 G05B23/0264 G05B2219/31462 G05B2219/33273 G05B23/0218 G05B23/0267 G05B23/0286 G05B2219/31342 G05B2219/24155 H04L63/10 H04L63/107 G05B2219/24167 H04L9/0872 H04L9/32 G05B19/4185 G05B2219/31094 G05B2219/31211 G05B2219/31324 G05B2219/36039 G05B19/41845 G06N5/022 G06N20/00 Y02P90/02 G05B19/4186 Y02P90/80 G06F16/256 G06F16/24528 G06F16/24534 G06F16/2471 G06F16/2477 Y10S707/99934 G05B23/024 Y02P90/86 Y02P90/18

US: 1/1 700/11 700/17 700/19 700/20 700/21 700/29 700/47 700/51 700/66 700/79 706/12 707/760 713/168



Family:

Publication number	Publication date	Application number	Application date
■ CN104914807 A	20150916	CN201510113223	20150316
■ CN104914807 B	20190329	CN201510113223	20150316
■ CN105005274 A	20151028	CN201510049715	20150130
■ CN105005274 B	20190104	CN201510049715	20150130
■ CN105373091 A	20160302	CN201510490839	20150811
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■ CN105487499 A	20160413	CN201510640227	20150930
■ CN105487501 A	20160413	CN201510641015	20150930
■ CN106796419 A	20170531	CN201580054441	20151005
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■ CN109901533 A	20190618	CN201910079631	20150811
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■ CN110376983 A	20191025	CN201910634966	20151005
■ DE102015101370 A1	20150806	DE201510101370	20150130
■ DE102015113054 A1	20160225	DE201510113054	20150807
■ DE102015116825 A1	20160407	DE201510116825	20151005
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■ DE102016119066 A1	20170413	DE201610119066	20161007
■ DE102016119084 A1	20170413	DE201610119084	20161007
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■ DE102016119100 A1	20170413	DE201610119100	20161007
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■ DE102016119178 A1	20170413	DE201610119178	20161010
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■ DE112015004578 T5	20170824	DE201511004578T	20151005
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■ GB201513617 A0	20150916	GB20150013617	20150731
■ GB201517035 A0	20151111	GB20150017035	20150925
■ GB201517038 A0	20151111	GB20150017038	20150925
■ GB201617019 A0	20161123	GB20160017019	20161007
■ GB201617020 A0	20161123	GB20160017020	20161007
■ GB201617021 A0	20161123	GB20160017021	20161007
■ GB201617022 A0	20161123	GB20160017022	20161007
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Priority:

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JP20190184275	20191007	JP20200002186	20200109	US20200871988	20200511

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Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BN BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IR IS IT JP KE KG KM KN KP KR KZ LA LC LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PA PE PG PH PL PT QA RO RS RU RW SA SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

2) Family number: 45962885 (US2010180169A)

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Title: SYSTEMS AND METHODS OF IMPLEMENTING REMOTE BOUNDARY SCAN FEATURES

Abstract: A system and method for remotely performing boundary scans on a circuit board, device and/or system across a network. A first computing component, connected to the network, includes a computer readable media including computer executable instructions. The instructions cause the computing component to maintain or access a library of test scan procedures for a plurality of subject circuit boards. At least one of the test scan procedure is downloaded to a second computing component proximate the circuit board, device and/or system. The second computing component and the test scan procedure are monitored and **controlled** remotely via the network.

Classifications:

International (IPC 8-9): G01R31/28 G01R31/302 G01R31/303 G01R31/3177 G01R31/3185 G01R31/319 G06F11/22 G06F11/25 G06F17/50 G06F21/10 G06F9/44 G06Q30/04

H04W4/38 (Advanced/Invention);

G06F21/70 H04W4/38 (Advanced/Non-invention);

G01R31/28 G06F11/25 (Core/Invention)

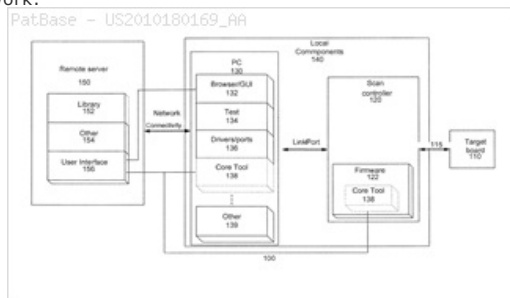
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G06F21/10

European: G01R31/3185S1 G01R31/3185S13 G01R31/3185S4 G01R31/3185S5 G06F11/22R S01R31/302W

US: 714/727 714/727P 714/728 714/E11.155 714/E11.155S 715/771 715/771S 716/136 717/727 717/728 726/28



Family:

Publication number	Publication date	Application number	Application date
CA2749799 AA	20100722	CA20092749799	20091218
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CA3000710 AA	20100722	CA20093000710	20091218
CA3052820 AA	20100722	CA20093052820	20091218
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EP2387723 A2	20111123	EP20090838570	20091218
EP2387723 A4	20130327	EP20090838570	20091218
EP2387723 B1	20140917	EP20090838570	20091218
US2010180169 AA	20100715	US20090641627	20091218
US2013091587 AA	20130411	US20120605888	20120906
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WO10083004 A2	20100722	WO2009US68666	20091218
WO10083004 A3	20101014	WO2009US68666	20091218

Priority:

US20090144980P 20090115	US20090170339P 20090417	CA20092749799 20091218
CA20093000710 20091218	EP20090838570 20091218	WO2009US68666 20091218
US20090641627 20091218	US20120605888 20120906	CA20182749799 20180426

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Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BN BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PE PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

3) Family number: 35860937 (US2007202873A)

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Title: OPTIMAL HOME AGENT ALLOCATION

Abstract: Particular embodiments provide an optimal allocation of a bearer manager or home agent. In one embodiment, a message is received from a mobile node requesting access to a visiting network that is different from a home network for the mobile node. An authentication request is sent to the home network requesting authentication for access. The authentication request indicates that a home agent has not been assigned. The home AAA server then sends a response that indicates the visiting AAA server can assign a home agent for the mobile node. The visiting AAA server then assigns a home agent that is optimally determined. The visiting home agent is different from a home agent that is found in the mobile node's home network. When a registration request is received, an IP gateway may send the registration request to the visiting home agent, which may not be sent back to the home network.

Classifications:

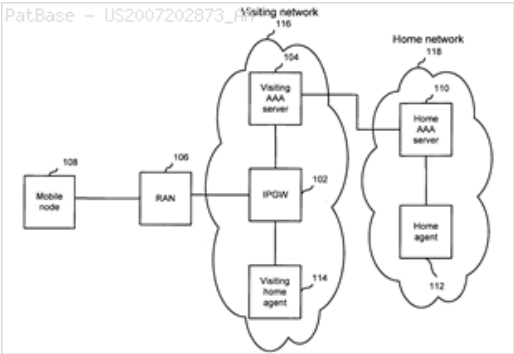
International (ipc 8-9): G01R31/08 G01R5/12 G04R31/08 G06F G06F15/16 G06F15/17 G06F15/173 G06F15/177 G06F17/00 G06F17/30 G06F7/04 G06F9/00 G06Q20/10 H04I3/24 H04K1/00 H04L12/14 H04L12/24 H04L12/28 H04L12/54 H04L12/56 H04L12/66 H04L12/723 H04L12/801 H04L12/911 H04L12/913 H04L12/927 H04L29/00 H04L29/06 H04L29/08 H04L29/12 H04L9/00 H04L9/32 H04M1/00 H04M1/66 H04M11/00 H04M7/00 H04Q7/00 H04Q7/20 H04Q7/24 H04W12/06 H04W12/08 H04W24/00 H04W28/16 H04W28/18 H04W36/00 H04W36/06 H04W36/12 H04W36/14 H04W36/34 H04W4/00 H04W48/14 H04W60/00 H04W72/04 H04W8/04 H04W8/06 H04W8/26 H04W80/04 H04W92/02 (Advanced/Invention); G06F15/16 G06Q20/10 H04L12/28 H04L12/723 H04L12/801 H04L12/911 H04L12/913 H04L12/927 H04L29/12 H04M1/00 H04W28/18 H04W36/00 H04W36/14 H04W4/00 H04W4/02 H04W40/00 H04W48/14 H04W60/00 H04W72/00 H04W8/12 H04W8/26 H04W80/04 H04W80/10 H04W88/16 H04W92/02 (Advanced/Non-invention); G06F7/02 H04K1/00 H04L9/32 H04M1/00 H04M1/66 H04W12/00 H04W24/00 H04W28/16 H04W4/00 H04W8/02 H04W92/00 (Core/Invention); H04W4/02 H04W80/00 H04W88/00 (Core/Non-invention)

International (ipc 1-7): G06F H04K1/00 H04M11/00 H04Q7/20

CPC: H04W8/065 H04W72/04 H04W28/16 H04W88/16 H04L9/0841 H04L9/321 H04L63/062 H04L63/0807 H04W12/04031 H04W8/04 H04W8/12 H04L12/14 H04L12/1403 H04L41/0893 H04L41/5029 H04L41/5061 H04L45/50 H04L47/10 H04L47/14 H04L47/15 H04L47/70 H04L47/724 H04L47/805 H04L47/824 H04L61/2015 H04L63/102 H04L63/162 H04L65/1006 H04L65/1016 H04L69/14 H04L69/24 H04W8/26 H04W28/18 H04W36/0033 H04W36/12 H04W36/14 H04W48/14 H04W60/00 H04W80/04 H04W80/10 H04W92/02 H04L63/08 H04L67/141 H04W12/0609 H04L63/0892 G06Q20/102 H04W12/08 H04W12/06

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Family:

Publication number	Publication date	Application number	Application date
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US20150722925	20150527				

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Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MX MY MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN SV SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

4) Family number: 41619810 (US2008069011A)

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Title: DISTRIBUTABLE, SCALABLE, PLUGGABLE CONFERENCING ARCHITECTURE

Abstract: Architecture for a scalable, pluggable multi-party, and distributed multimedia conferencing. A centralized policy and **control** conferencing component allows the seamless plug-in of different distributed media components (e.g., data, audio/video, messaging) to accommodate client participation in a conference session. The centralized conference **control** component includes the following: a conference notification service for accepting subscriptions to the conference state and notifying subscribers about changes to that state; a conference policy and roster **control** service for storing and manipulating conference policy and rosters; a security service for user authorization/authentication based on user identity information; a scheduling service for conference scheduling; an allocation service for allocating the most available media component(s) for a conference session; and, an MCU management service for conference policy and roster management of the distributed media components.

Classifications:

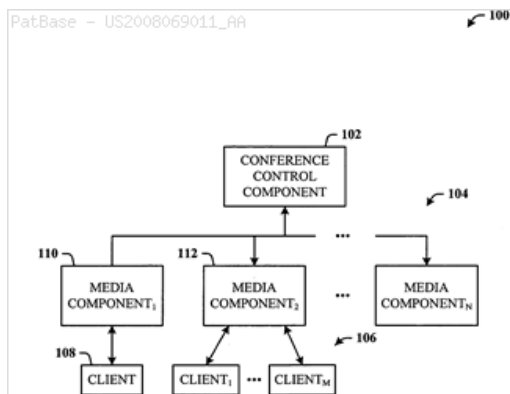
International (IPC 8-9): G06F13/00 H04L12/16 H04L12/18
H04L29/06 H04L29/08 H04L29/12 H04M3/56 (Advanced/Invention);
H04M3/56 (Advanced/Non-invention);
G06F13/00 H04L12/16 H04L12/18 H04L29/06
H04M3/56 (Core/Invention)

International (IPC 1-7): H04L12/18

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H04L61/1511 H04L63/08 H04L63/10 H04L65/4038 H04L67/1002
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European: H04L12/18D H04L29/06M4C2 H04L29/08N9A
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US: 370/260 370/260P 370/261 370/270 370/352 370/390 370/496
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Family:

Publication number	Publication date	Application number	Application date
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Agent(s): DAVIES COLLISON CAVE PTY LTD; DAVIES COLLISON CAVE; SMART AND BIGGAR; GRUENECKER PATENT U RECHTSANWALTE PARTG MBB; ABE KAZUO; TANI YOSHIKAZU; MICROSOFT CORP; JIM; ANDREW; MICKY

Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN SV SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

5) Family number: 52544783 (US2012316696A)

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Title: MULTI-LEVEL TOPOLOGYTOPOGRAPHY FOR ELECTRICAL DISTRIBUTION GRID **CONTROL**

Abstract: Aspects of a multi-level electrical distribution **control** system associated with an electrical distribution grid are disclosed. A multi-level **control** system provides a non-flat topography for electrical distribution grid **control**. A hierarchical structure can be employed in the topography of the **control** system. Further, tree and mesh network structure, among others, can be employed in the topography of the **control** system. These rich topographies can provide for closed loop **control** at each level of the **control** system, improved reliability of the **control** system through redundant topographical network structures, and desirable data handling features that can reduce data congestion and computing costs by distributing data handling, processing, and **control** across the levels of the **control** system.

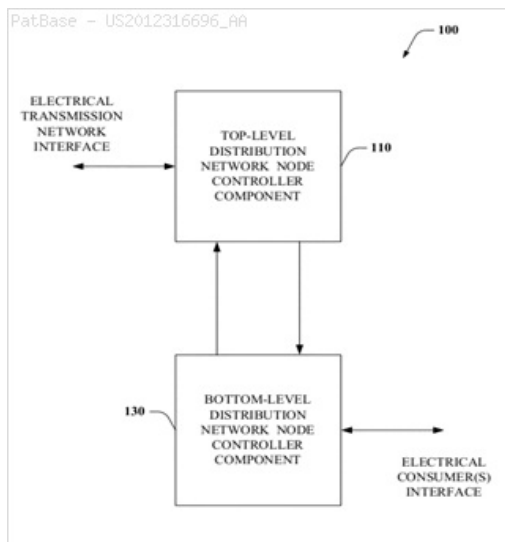
Classifications:

International (IPC 8-9): G06F1/26 H02J13/00 (Advanced/Invention)

CPC: H02J13/00028 H02J13/00034 H02J13/00017 Y04S40/124

European: H02J13/00F4D H02J13/00F4F

US: 700/297



Family:

Publication number	Publication date	Application number	Application date
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Priority:

US20110155594 20110608 EP20120170004 20120530

Probable Assignee: ALSTOM SA

Assignee(s): (std): ALSTOM GRID ; ALSTOM TECHNOLOGY LTD ; VENKATA SUBRAHMANYAM SARASWATI ; BOARDMAN ETHAN CLAIR ; ALSTOM SA

Inventor(s): (std): BOARDMAN ETHAN CLAIR ; VENKATA SUBRAHMANYAM SARASWATI

Inventor(s): VENKATA SUBRAHMANYAM SARASWATI ORO VALLEY ; BOARDMAN ETHAN CLAIR SEATTLE

Agent(s): GRUENECKER PATENT U RECHTSANWALTE PARTG MBB; GRUENECKER KINKELDEY STOCKMAIR AND SCHWANHAEUSSER

Designated states: AL AT BA BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC ME MK MT NL NO PL PT RO RS SE SI SK SM TR

6) Family number: 28237944 (US2002109681A)

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Title: LOW BANDWIDTH TRANSMISSION OF 3D GRAPHICAL DATA

Abstract: A graphical data-compressor for compressing received arbitrary graphical data for subsequent transmission; where the graphical data-compressor comprises an input for receiving the received arbitrary graphical data, the input being linked to an analyzer for analysis of the received arbitrary graphical data into constituent geometrical parts, the analyzer being linked to an analytic scene describer, for description of said constituent geometrical parts as an analytic description of the received arbitrary graphical data, the analytic scene describer being linked to a transmitter, the transmitter being for transmission of the analytic description.

Classifications:

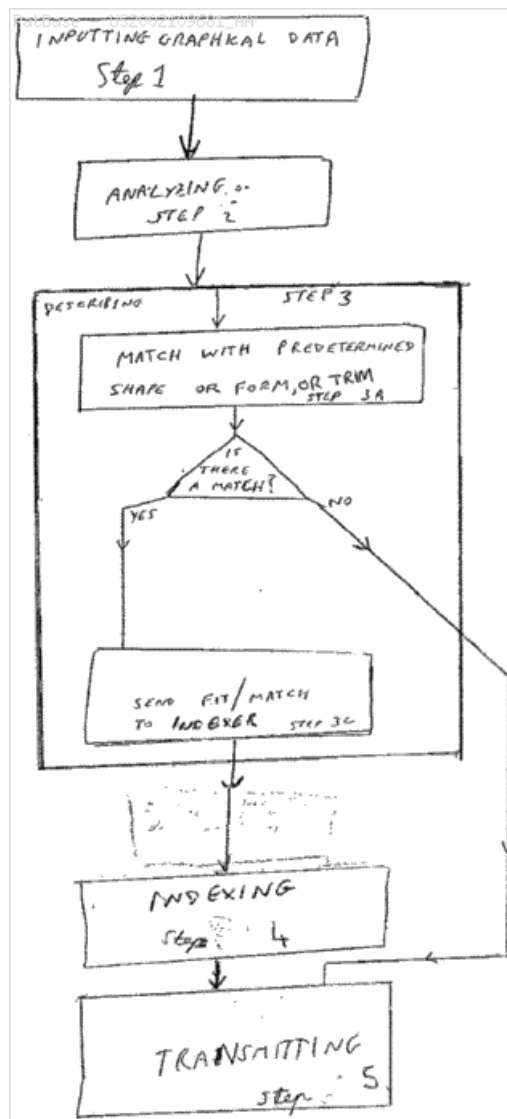
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G06F11/00 G06F11/25 G06F17/50 G06F9/305 G06F9/308 G06F9/312
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G06F17/10

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Probable Assignee: OF NORTH CAROLINA AT CHARLOTTE UNIV

Assignee(s): (std): GEOMCORE LTD ; KOCH III KENNETH ELMON ; KOCH KENNETH ELMON ; UNIV NORTH CAROLINA ; TECHNION RES AND DEV FOUNDATION ; BOOLEAN CORE DEVICES LLC

Assignee(s): TECHNION RESEARCH AND DEVELOPMENT FOUNDATION LTD ; NORTH CAROLINA AT CHARLOTTE UNIV ; OF NORTH CAROLINA AT CHARLOTTE UNIV ; UNIVERSITY OF NORTH CAROLINA AT CHARLOTTE

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Inventor(s): KENNETH ELMON KOCH ; GERSHON ELBER

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Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PE PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

Title: ONLINE PROCUREMENT OF BIOLOGICALLY RELATED PRODUCTS/SERVICES USING INTERACTIVE CONTEXT SEARCHING OF BIOLOGICAL INFORMATION

Abstract: Systems and methods for procuring biologically related products available on a vendor Website are described which involve user-server interfacing with a Web based browser to retrieve database files representing available target products via processing biological context searches on named annotated text string databases.

Classifications:

International (ipc 8-9): A61K38/17 C07H21/04 C12M1/34 C12N9/12 C12Q1/68 C40B40/10 G01N33/48 G01N33/50 G01N33/53 G06F17/30 G06F19/00 G06F19/28 G06F3/048 G06K9/00 G06Q10/00 G06Q30/00 G06Q30/06 G06Q50/00 G06Q50/22 G06Q99/00 (Advanced/Invention); C12Q1/68 G06F19/00 G06F19/20 G06F19/28 (Advanced/Non-invention);

C07H21/00 C12Q1/68 G01N33/48 G01N33/50 G01N33/53 G06F17/30 G06F19/00 G06Q10/00 (Core/Invention); G06F19/00 (Core/Non-invention)

International (ipc 1-7): C12Q1/68 G01N33/48 G01N33/50 G06F19/00

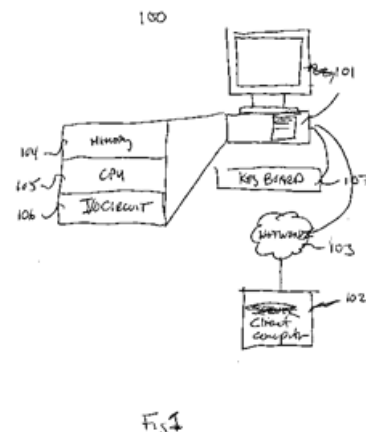
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European: C12N9/12C21 C12Q1/68M G01N33/50D2E14 G01N33/68A G01N33/68A10 G01N33/68A8 G01N33/68V2 G06F19/28 G06Q10/087 G06Q10/08E G06Q10/103 G06Q30/06 G06Q30/0601 G06Q50/22 G06Q99/00 K61K38/00 M12Q1/68B10A S01N333/912B1 S06F19/20 S06F19/28

US: 1/1 382/133 382/133S 435/287.2 435/6 435/6.1 435/6.100S 435/6.18 435/6.180P 435/7.1 435/7.100P 506/18 506/18S 536/24.3 536/24.300S 702/19 702/19P 702/20 702/20S 705/1 705/1.1 705/2 705/26 705/26.1 705/26.100P 705/26.61 705/26.63 705/26.8 705/26.800P 705/26S 705/27 705/27P 705/2S 705/301 705/301P 715/760 715/760S

PatBase - US2005240352_AA

Patent Application Publication Oct. 27, 2005 Sheet 1 of 18 US 2005/0240352 A1



Family:

Publication number	Publication date	Application number	Application date
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JP2007535040 T2	20071129	JP20070509690T	20050422
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Priority:

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US20060371354	20060307	US20090557163	20090910	US20100791820	20100601
US20120422202	20120316	US20140265990	20140430	US20150637587	20150304
US20180114374	20180828				

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Inventor(s): KOLESKA ANTHONY

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Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KM KP KR KZ LC LI LK LR LS LT LU LV MA MC MD MG MK ML MN MR MW MX MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RU SC SD SE SG SI SK SL SM SN SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

8) Family number: 31954586 (US2005091227A) © PatBase

Title: MODEL-BASED MANAGEMENT OF COMPUTER SYSTEMS AND DISTRIBUTED APPLICATIONS

Abstract: Model-based application management architecture. A developer can describe an application or service in terms of its constituent components. Desired states can be described in terms of functionality, configuration, security, and performance. The description is employed at application installation to configure management services, which services help to ensure availability of the application through automatic management actions, such as configuration management, problem detection, diagnosis, and recovery.

Classifications:

International (IPC 8-9): G06F11/07 G06F11/30 G06F12/00 G06F13/00 G06F15/16 G06F15/173 G06F15/177 G06F17/00 G06F17/30 G06F19/00 G06F7/00 G06F9/06 G06F9/44 G06F9/445 G06F9/45 G06F9/46 G06F9/48 G06N5/02 G06N5/04 G06Q50/00 (Advanced/Invention); G06F13/00 G06F15/16 G06F9/06 G06F9/44 G06F9/46 G06Q50/00 (Advanced/Non-invention); G06F11/07 G06F11/30 G06F12/00 G06F13/00 G06F15/16 G06F17/00 G06F17/30 G06F19/00 G06F7/00 G06F9/06 G06F9/44 G06F9/445 G06F9/45 G06F9/46 G06N5/00 (Core/Invention); G06F9/46 (Core/Non-invention); G06F G06K (Subclass/Invention)

International (IPC 1-7): G06F G06F0/00 G06F12/00 G06F13/00 G06F15/173 G06F17/00 G06F17/30 G06F17/60 G06F7/00 G06F9/44 G06F9/445 G06F9/45 G06K1/00

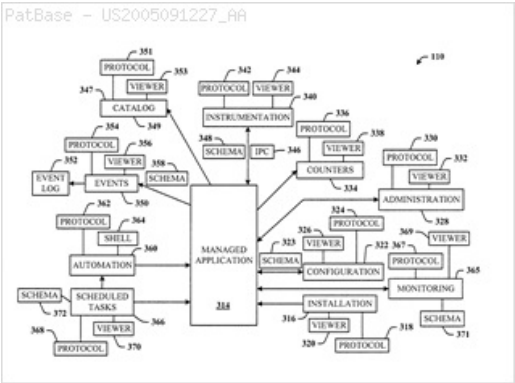
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European: G06F11/07P10 G06F11/07P1A G06F11/07P6 G06F11/34C G06F11/34T G06F11/34T12 G06F8/20 G06F8/36 G06F8/71 G06F9/445C G06F9/54B G06N5/02K2 G06N5/04 H04L12/24D H04L12/24F1 H04L41/14B S06F11/34M S06F201/810 S06F201/880 S06F8/314

US: 1/1 1/1P 707/100 707/3 707/999.003 707/999.003S 707/999.1 707/999.100S 709/220 709/223 709/223P 709/223S 709/224 709/224P 714/E11.023 714/E11.023S 714/E11.026 714/E11.026S 714/E11.192 714/E11.192S 714/E11.2 714/E11.200S 717/101 717/101P 717/117 717/117P 717/119 717/120 717/121 717/127S 717/130 717/130P 717/130S 717/136 717/149 717/153 717/158 717/158S 717/159 717/167 717/171 717/174 717/174S 717/176 719/328

Family:

Publication number	Publication date	Application number	Application date
AU2004279195 AA	20050519	AU20040279195	20040712
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CN1836208 A	20060920	CN200480001254	20040712
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KR20070012178 A	20070125	KR20057006230	20040712
MXPA05007141 A1	20050826	MX2005PA07141	20040712
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US2005114485 AA	20050526	US20030693588	20031024
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US7765540 BB	20100727	US20030692432	20031023
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WO05045559 A3	20060209	WO2004US22378	20040712
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Priority:

US20030692216	20031023	US20030692432	20031023	US20030692660	20031024
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Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KP KR KZ LC LI LK LR LS LT LU LV MA MC MD MG MK ML MN MR MW MX MZ NA NE NI NL NO NZ OM PG PH PL PT RO RU SC SD SE SG SI SK SL SN SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

9) Family number: 46625499 (US2010213759A)

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Title: DIGITAL SWITCH COMMUNICATION

Abstract: In one example, an electric circuit may be provided that is coupled to a line. The electric circuit may include a switch connected in series with a **load**, where the switch and the **load** are in series between two conductors of the line. The line may provide power to the **load**. The electric circuit may also include a resistive element connected in parallel with the switch and in series with the **load**. The switch may be configured to short circuit the resistive element when closed such that the voltage on the line varies between two voltage levels when the switch is opened and closed. The line provides power to the **load** when the switch is opened and when the switch is closed.

Classifications:

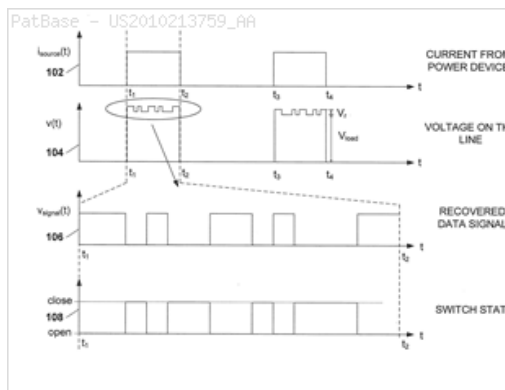
International (IPC 8-9): G05B11/01 G05F1/577 G06F1/26 G08B1/08 G08C19/16 H02J1/00 H02J1/08 H02J1/14 H02J13/00 H02J9/00 H03K7/08 H04B1/48 H04B3/54 H05B37/02 H05B41/36 (Advanced/Invention);

G05B11/01 H02J1/00 H02J9/00 H03K7/00 H04B3/54 (Core/Invention)

CPC: H05B41/3927 H05B47/185 H05B45/10 H05B45/10 H03K7/08 H04B3/54 H04B14/026 H04B2203/5416 G08C19/16 H04B3/542 H04B2203/547 H04B2203/5445 G05F1/577 H04B3/548 H04B2203/5441 H04B2203/545 H02J4/00 H02J2310/14 H02J13/0003 H02J13/0096 Y02B20/40 Y10T307/313 Y10T307/367 Y10T307/406 Y10T307/658 Y10T307/74 Y10T307/937 Y10T307/944 Y04S40/121

European: H02J1/08 H02J1/14 H02J13/00E H03K7/08 H04B3/54A H05B41/392D8 T04B203/54S6 Y02B90/22H Y04S20/18

US: 1/1 307/1 307/112 307/139 307/140 307/154 307/19 307/19P 307/1P 307/26 307/31 307/31S 307/72 315/297 315/297P 320/101 34/12.32 34/12.33 34/12.37 34/3.1 34/538.11 34/538.15 34/568.2 34/870.02 340/12.16 340/12.32 340/12.33 340/12.35 340/12.37 340/3.1 340/310.12 340/310.120P 340/506 340/532 340/538 340/538.11 340/538.15 340/540 340/541 340/568.2 340/870.02 340/870.2 370/212 375/238 375/238S 700/295



Family:

Publication number	Publication date	Application number	Application date
CN102237680 A	20111109	CN201110115491	20110429
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EP2385603 A2	20111109	EP20110003568	20110502
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US9583979 BB	20170228	US20140518267	20141020

Priority:

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US20100330536P 20100503 US20100790038 20100528 US20120568895 20120807
US20130769550 20130218 US20130859992 20130410 US20140518267 20141020

Probable Assignee: COMMScope TECHNOLOGIES LLC

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Designated states: AL AT BA BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC ME MK MT NL NO PL PT RO RS SE SI SK SM TR

10) Family number: 23517252 (US2004037221A)

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Title: METHOD, SYSTEM AND APPARATUS FOR THE TRANSMISSION OF DATA ON ELECTRIC NETWORK

Abstract: There are described a method and a system for the transmission/reception or communication of data and/or information on electric line between two electronic **control** devices, including: an electric user (HA), in particular a household appliance, having a first electronic **control** system and at least a first electric **load**; a monitoring or **control** device (SA), having a second electronic **control** system, said device (SA) being located on said line between an electric power source and said first electric **load**. According to the invention, the transmission/reception or communication of data and/or information on said line is realized by means of an electric power modulation between said user (HA) and said device (SA), and/or vice-versa.

Classifications:

International (IPC 8-9): H02J H02J13/00

H04B3/54 (Advanced/Invention);

H02J13/00 H04B3/54 (Core/Invention);

H02J (Subclass/Invention)

International (IPC 1-7): H02J13/00 H04B3/54 H04J H04L1/00

H04M11/04

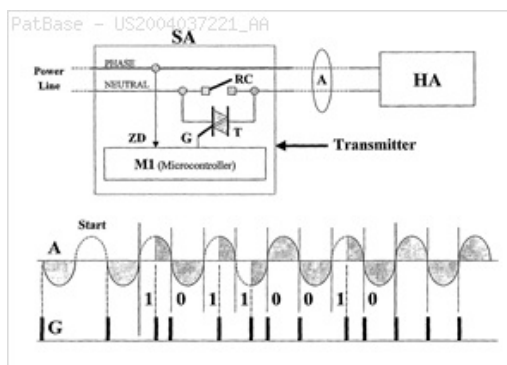
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H02J13/00009 Y02E60/00 Y04S40/121 Y04S20/222 Y04S20/242

European: H02J13/00F4B2F T02J3/14H T04B203/54M4

Y02B90/26B2D Y04S40/12B4

US: 340/12.37 340/310.01 340/310.02 340/310.06 340/310.08 340/310.11 340/310.12 340/310.16 340/310.18 370/203 370/203S 370/229 370/229P



Family:

Publication number	Publication date	Application number	Application date
AT441960 E	20090915	AT20010919687T	20010403
AU200146747 A1	20020322	AU20010046747	20010403
AU200146747 A5	20020322	AU20010046747	20010403
CN100340048 C	20070926	CN20018018395	20010403
CN1475040 A	20040211	CN20018018395	20010403
DE60139787 D1	20091015	DE20016039787	20010403
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EP1316134 A1	20030604	EP20010919687	20010403
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ES2331688 T3	20100113	ES20010919687T	20010403
HK1061311 A1	20040910	HK20040104228	20040611
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HK1061311 A3	20040910	HK20040104228	20040611
HK1061311 B	20080620	HK20040104228	20040611
IT1320621 B1	20031210	IT2000T000840	20000905
ITTO20000840 A0	20000905	IT2000T000840	20000905
ITTO20000840 A1	20020305	IT2000T000840	20000905
JP2004508796 T2	20040318	JP20020525971T	20010403
JP4721617 B2	20110713	JP20020525971T	20010403
RU2003106189 A	20041010	RU20030106189	20010403
RU2265955 C2	20051210	RU20030106189	20010403
US2004037221 AA	20040226	US20030363757	20030804
US6950010 BB	20050927	US20010363757	20010403
WO0221664 A1	20020314	WO20011B00548	20010403

Priority:

IT2000T000840 20000905 WO20011B00548 20010403

Probable Assignee: INDESIT CO SPA

Assignee(s): (std): AISA VALERIO ; INDESIT CO S P A ; INDESIT CO SPA ; WRAP S P A ; WRAP SPA
Assignee(s): INDESIT COMPANY S P A
Inventor(s): (std): AISA VALERIO ; AJSA VALERIO ; AISA V
Inventor(s): VALERIO AISA ; V AISA
Agent(s): DINI ROBERTO; SHANGHAI PATENT AND TRADEMARK OFFICE ROOM 1804 TUNG WAI COMMERCIAL; SHANGHAI PATENT AND TRADEMARK OFFICE; CESARI AND MCKENNA LLP
Designated states: AE AL AM AT AU AZ BA BB BE BF BG BJ BR BY CA CF CG CH CI CM CN CR CU CY CZ DE DK DM EE ES FI FR GA GB GD GE GH GM GN GR GW HR HU ID IE IL IN IS IT JP KE KG KP KR KZ LC LI LK LR LS LT LU LV MA MC MD MG MK ML MN MR MW MX MZ NE NL NO NZ PL PT RO RU SD SE SG SI SK SL SN SZ TD TG TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

11) Family number: 32760022 (US2006076830A) © PatBase

Title: COMMUNICATION AND AC POWER SYSTEM

Abstract: A system for bidirectional data and power transmission is shown and described. In an embodiment, a network power **controller** provides power to a node and communicates bidirectionally with the node. In an embodiment, the network power **controller** is coupled to an AC power source and provides both AC and DC power to the node. In an embodiment, the network power **controller** and node are coupled via a conduit comprising three wires, wherein the first wire carries DC power and communications, the second wire carries AC power, and the third wire is the neutral/ground line. In another embodiment, the network power **controller** and node are coupled via a conduit comprising two wires, wherein the first wire carries AC power and communications and the second wire is the neutral/ground line. In an embodiment, AC power having one phase or more can be **controlled** at the network power **controller** and the node.

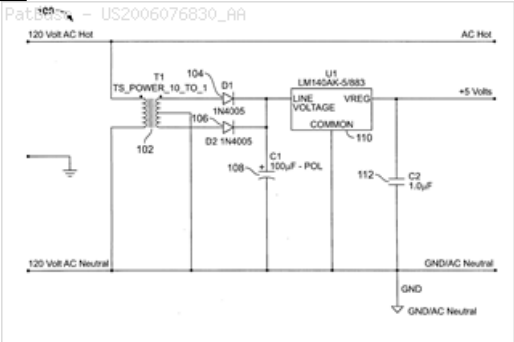
Classifications:

International (IPC 8-9): B60L1/00 G05B11/01 G06F15/173 G08B1/08 H04B3/54 H04L12/66 (Advanced/Invention); B60L1/00 G05B11/01 H04B3/54 (Core/Invention)

CPC: H04B3/54 H04B3/542 H04B2203/5412 H04B2203/5445 H04B2203/547

European: H04B3/54 H04B3/54A H04B3/54D T04B203/54M4 T04B203/54P8 T04B203/54S6

US: 307/1 307/10.1 307/10.100S 307/1P 340/12.32 340/310.11 340/310.110S 340/538 370/352 709/223



Family:

Publication number	Publication date	Application number	Application date
US2006076830 AA	20060413	US20050231507	20050921
US7405652 BB	20080729	US20050231507	20050921
WO06034323 A2	20060330	WO2005US33768	20050921
WO06034323 A3	20071227	WO2005US33768	20050921
WO06034323 C2	20071108	WO2005US33768	20050921

Priority:

US20040611834P 20040921 US20050231507 20050921

Probable Assignee: ABET TECHNOLOGIES LLC

Assignee(s): (std): ABET TECHNOLOGIES LLC ; GREENE DANIEL L ; HAIR JAMES M III

Assignee(s): USRG HOLDCO IX LLC ; HAIR JAMES M

Inventor(s): (std): GREENE DANIEL L ; HAIR JAMES M III ; HAIR III JAMES M

Inventor(s): GREENE DANIEL ; HAIR JAMES MIII ; HAIR JAMES ; HAIR JAMES M

Agent(s): GARDNER CARTON AND DOUGLAS LLP ATTN PATENT DOCKET DEPT; DRINKER BIDDLE AND REATH LLP

Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KM KP KR KZ LC LK LR LS LT LU LV LY MA MC MD MG MK ML MN MR MW MX MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RU SC SD SE SG SI SK SL SM SN SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

12) Family number: 30353273 (US2006108988A) © PatBase

Title: POWER SUPPLY SYSTEM

Abstract: A power supply system for supplying power to a **load** is provided. The system comprises power-line carriers for supplying power to the **load**, and a plurality of power supply units the outputs of which are parallelly connected to power-line carriers. Each of the power supply unit comprises a generator; an inverter for converting a voltage generated by the generator into an AC voltage and outputting the AC voltage; an inverter **control** unit for **controlling** the inverter, and a connection apparatus for connecting the AC voltage outputted from the inverter to the power-line carriers. The inverter **controller** comprises a **load** sharing adjuster for adjusting a shared **load** current supplied from the power supply unit, so that at least one of the power supply units in operation can operated at its rating condition, thereby realizing an efficiency operation.

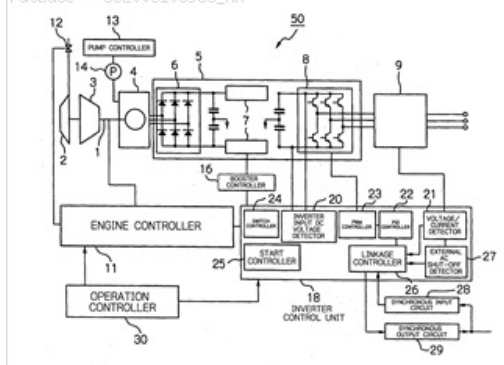
Classifications:

International (IPC 8-9): G05D11/00 G05F1/70 H02J1/00 H02J3/00 H02J3/46 H02M5/45 H02M5/458 H02M7/48 H02M7/493 (Advanced/Invention);

G05D11/00 G05F1/70 H02J1/00 H02J3/00 H02J3/46 H02M5/00 H02M7/48 (Core/Invention)

International (IPC 1-7): H02J3/38

CPC: H02J3/46 H02J9/06 H02M5/458 Y10T307/25 Y10T307/414



Publication number	Publication date	Application number	Application date
AU2003257641 AA	20040311	AU20030257641	20030821
AU2003257641 AH	20040311	AU20030257641	20030821
EP1542331 A1	20050615	EP20030792766	20030821
EP1542331 A4	20100120	EP20030792766	20030821
JP2004019466 A1	20051215	JP20040530592T	20030821
JP4256845 B1	20090422	JP20040530592T	20030821
JP4256845 B2	20090422	JP20040530592T	20030821
US2006108988 AA	20060525	US20050525219	20050912
US7514813 BB	20090407	US20050525219	20050912
WO04019466 A1	20040304	WO2003JP10564	20030821

JP20020241240 20020821 JP20020241243 20020821 WO2003JP10564 20030821

Assignee(s): (std): EBARA CORP ; EBARA DENSAN LTD ; FURUYA TAI ; ISHIHARA SEIICHI ; MCKELVEY TERENCE ; MARUI EISHI ; MIYAMOTO MASAHIRO ; SATO MOTOYASU ; KATAOKA TADASHI ; OZAWA TAKAHIDE ; KINOSHITA NOBORU ; ZHENG SHAOJUN

Agent(s): SHAMOTO ICHIO; TANAKA HIDEO; OHTSUKA SUMIE; NISHIYAMA FUMITOSHI; OBLON SPIVAK
MCCLELLAND MAIER AND NEUSTADT PC

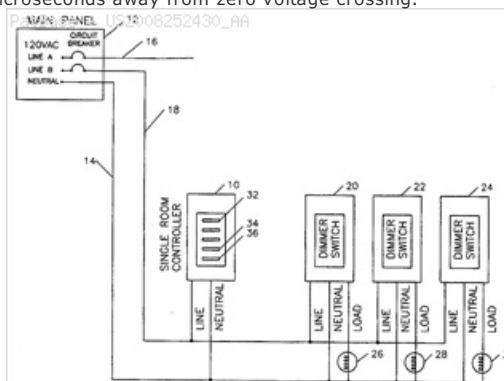
Designated states: AE AG AL AM AT AU AZ BA BB BE BF BG BJ BR BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DZ EC EE ES FI FR GA GB GD GE GH GM GN GQ GR GW HR HU ID IE IL IN IS IT JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MC MD MG MK ML MN MR MW MX MZ NE NI NL NO NZ OM PG PH PL PT RO RU SC SD SE SG SI SK SL SN SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW

Abstract: A transmitting **controller** is connected to a powerline and on command places a reference signal and a series of signal pulses in the powerline at a series of signal timing positions related to zero voltage crossing points so that the signals pulses are substantially in the powerline temporal quiet zone near zero crossing. The signal pulses are produced from a pair of capacitors and switches which are each sequentially charged a first polarity from the powerline and is discharged in the powerline at the opposite polarity so that the powerline voltage at the time of the pulse is additive to the pulse voltage. The receiving **controller** is connected to the powerline and has a filter circuit therein which filters away the powerline AC signal and noise to leave the reference and signal pulses. The signal pulses are compared to the position of starting reference pulses to determine in which signal timing position the pulses have occurred. Digital data is communicated over the powerline in accordance with the position placement of the data pulses related to the reference pulse positions. The timing quiet zone for transmission and signals is preferably about 500 to about 1000 microseconds away from zero voltage crossing.

International (IPC 8-9): G05B11/01 G05B11/26
G05B3/00 (Advanced/Invention);
G05B11/01 (Core/Invention)

European: H02J13/00F4B2B H05B37/02B6P Y02B70/32D2D
Y04S20/24B4

US: 340/12.32 340/12.320P 340/12.33 340/12.35 340/12.37
340/310.11 340/310.12 340/310.14 340/310.16



Publication number	Publication date	Application number	Application date
US2008252430 AA	20081016	US20070780403	20070719
US7265654 BA	20070904	US20050112568	20050422

Priority:

US20040564499P 20040422 US20050112568 20050422 US20070780403 20070719

Probable Assignee: POWERLINE CONTROL SYSTEMS INC

Assignee(s): (std): LESTER MARSHALL E ; POWERLINE CONTROL SYSTEMS INC

Assignee(s): VALLEY ECONOMIC DEVELOPMENT CENTER INC ; CITY OF LOS ANGELES

Inventor(s): (std): LESTER MARSHALL E

Agent(s): LEWIS BRISBOIS BISGAARD AND SMITH LLP

14) Family number: 50014779 (US2011291583A)

© PatBase

Title: DIMMER CIRCUIT APPLICABLE FOR LED DEVICE AND **CONTROL** METHOD THEREOF

Abstract: A dimmer circuit applicable for LED device and **control** method thereof is disclosed in the embodiments of the present invention. The dimmer circuit is applicable for **controlling** at least a LED device. The dimmer circuit includes a rectifier, a bleeder, a phase angle detect circuit, a constant current circuit and a programmable micro **controller**. The phase angle detect circuit couples to the programmable micro **controller**. The constant current circuit couples to the LED device. The programmable micro **controller** generates a PWM signal according to the output signal of the phase angle detect circuit to adjust current of the constant current circuit.

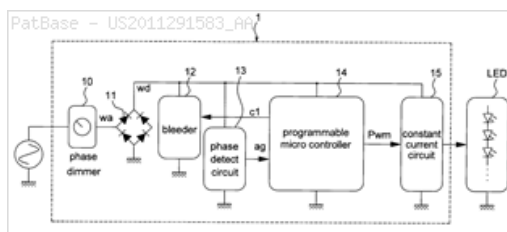
Classifications:

International (IPC 8-9): H05B37/02 H05B41/16 (Advanced/Invention)

CPC: H05B45/10 H05B45/37 H05B47/185

European: H05B33/08D1C4 H05B33/08D3B H05B37/02B6P

US: 315/200R 315/287 315/287P 315/307


Family:

Publication number	Publication date	Application number	Application date
TW201146087 A	20111216	TW20100146213	20101228
TWI434616 B	20140411	TW20100146213	20101228
US2011291583 AA	20111201	US20110095260	20110427
US8508147 BB	20130813	US20110095260	20110427

Priority:

TW20100117507 20100601 TW20100146213 20101228

Probable Assignee: JD TEK JIM DANDY TECHNOLOGY CORP

Assignee(s): (std): JD TEK JIM DANDY TECHNOLOGY CORP ; SHEN FENG MIN ; UNITED POWER RES TECHNOLOGY CORP

Assignee(s): UNITED POWER RESEARCH TECHNOLOGY CORP

Inventor(s): (std): SHEN FENG MIN

Agent(s): MUNCY GEISSLER OLDS AND LOWE PLLC

15) Family number: 47096201 (WO10117340A1)

© PatBase

Title: POWER LINE CARRIER DATA SYSTEMS AND METHODS

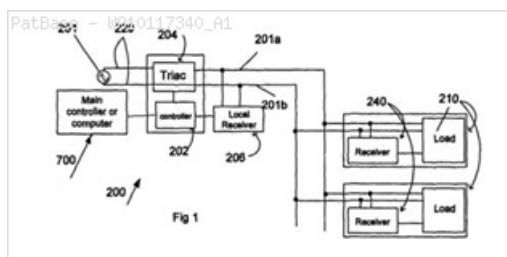
Abstract: Systems and methods for transmitting **control** data over alternating current (ac) power lines. The system may include a transmitter including: a zero crossing detector; a microprocessor; and a triode for alternating current (Triac) or a silicon-controlled rectifier (SCR) triggering device; wherein said transmitter receives the **control** data from a data source; and said microprocessor splits said **control** data into a plurality of elements, provides a fixed time delay for each of said elements and transmits each of said elements to said Triac or SCR triggering device, which is capable of triggering a corresponding Triac or SCR connected to said ac power line when said zero crossing detector detects a zero crossing of a supply voltage of said ac power line.

Classifications:

International (IPC 8-9): H04B3/54 (Advanced/Invention)

CPC: H04B3/54 H04B2203/5412 H04B2203/542 H05B47/185

European: H04B3/54 T04B203/54M4 T04B203/54M8


Family:

Publication number	Publication date	Application number	Application date
SG166007 A1	20101129	SG20090023888	20090407
SG176548 A1	20120130	SG20110073822	20100407
WO10117340 A1	20101014	WO2010SG00138	20100407

Priority:

SG20090023888 20090407 SG20090002497 20090413 WO2010SG00138 20100407
SG20110073822 20100407

Probable Assignee: POWER LINK TECHNOLOGY PTE LTD

Assignee(s): (std): AH HONG KHET ; POWER LINK TECHNOLOGY PTE LTD
Inventor(s): (std): AH HONG KHET ; HONG KHET AH
Agent(s): ELLA CHEONG SPRUSON AND FERGUSON SINGAPORE PTE LTD
Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CL CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PE PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TH TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

16) Family number: 56884079 (US2014191574A)

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Title: VIRTUAL PARALLEL **LOAD** BANK SYSTEM

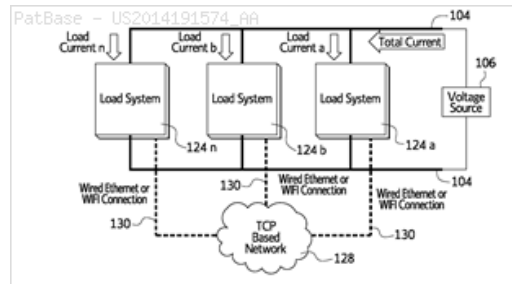
Abstract: A virtual parallel **load** bank system is characterized by a **load** bank connected in parallel with the source terminals of a voltage source to create a specific current to be applied to the terminals. The **load** bank includes a plurality of **loads** that are selectively connected in accordance with the desired current for the voltage source. One **load** device is designated as a master **load** and a communication network is connected with the **load** devices to enable the master **load** device to communicate with the other **load** devices to determine which are available for connection in the virtual **load** bank system.

Classifications:

International (IPC 8-9): H02J4/00 (Advanced/Invention)

CPC: G01R31/40 Y10T307/461

US: 307/38



Family:

Publication number	Publication date	Application number	Application date
US2014191574 AA	20140710	US20130737262	20130109

Priority:

US20130737262 20130109

Probable Assignee: EXPERIUM TECHNOLOGIES LLC

Assignee(s): (std): EXPERIUM TECHNOLOGIES LLC

Inventor(s): (std): PALATINI LANCE

17) Family number: 43956665 (US2011043124A)

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Title: METHODS AND APPARATUS FOR ENCODING INFORMATION ON AN A.C. LINE VOLTAGE

Abstract: An AC line voltage may be encoded with **control** information, such as dimming information derived from an output signal of a conventional dimmer, so as to provide an encoded AC power signal. One or more lighting units, including LED-based lighting units, may be both provided with operating power and **controlled** (e.g., dimmed) based on the encoded power signal. In one implementation, information may be encoded on the AC line voltage by inverting some half cycles of the AC line voltage to generate an encoded AC power signal, with the ratio of positive half-cycles to negative half-cycles representing the encoded information. In other aspects, the encoded information may relate to one or more parameters of the light generated by the LED-based lighting unit(s) (e.g., intensity, color, color temperature, etc.).

Classifications:

International (IPC 8-9): G02F1/01 G05F1/00 H01L51/50 H04B3/54

H05B33/08 H05B37/02 H05B39/08 H05B41/24 (Advanced/Invention);

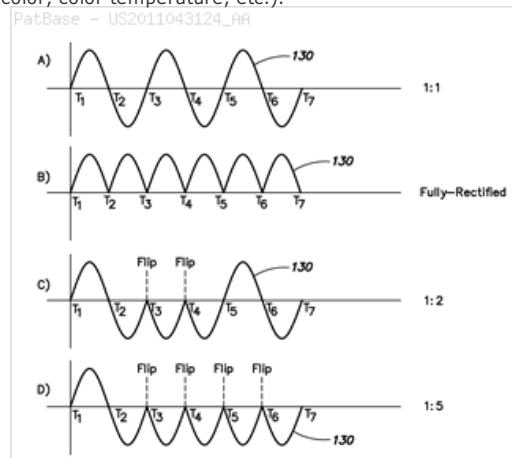
H04B3/54 H05B37/02 H05B39/08 (Advanced/Non-invention);

H05B33/02 H05B39/00 (Core/Invention)

CPC: H05B47/185 H05B45/10 H05B45/20 H05B45/37

European: H05B37/02B6P

US: 315/246 315/250 315/250P 315/265 315/291 315/294 315/294S 315/307 315/308 315/314



Family:

Publication number	Publication date	Application number	Application date
CN102017795 A	20110413	CN200980115579	20090421
CN102017795 B	20140305	CN200980115579	20090421
DE602009030666 D1	20150528	DE200960030666T	20090421
EP2277357 A1	20110126	EP20090738502	20090421
EP2277357 B1	20150415	EP20090738502	20090421

JP2011519468 T2	20110707	JP20110506802T	20090421
JP5777509 B2	20150909	JP20110506802T	20090421
KR101727093 B1	20170414	KR20107026812	20090421
KR20100135329 A	20101224	KR20107026812	20090421
RU2010148801 A	20120610	RU20100148801	20090421
RU2515609 C2	20140520	RU20100148801	20090421
US2011043124 AA	20110224	US20090989091	20090421
US8957595 BB	20150217	US20090989091	20090421
WO09133489 A1	20091105	WO20091B51633	20090421

Priority:

US20080048986 20080430 US20080048986P 20080430 EP20090738502 20090421
WO20091B51633 20090421 US20090989091 20090421

Probable Assignee: SIGNIFY HOLDING BV

Assignee(s): (std): BLACKWELL MICHAEL KEENAN ; JOHNSTON SCOTT ; KONINKL PHILIPS ELECTRONICS NV ; KONINKL PHILIPS NV ; ROYAL DUTCH PHILIPS ELECTRONICS LTD ; JOHNSTON SCOTT D ; KONINKLIJKE PHILIPS N V

Assignee(s): SIGNIFY HOLDING BV ; KONINKLEJKE FILIPS EHLEKTRONIKS NV ; KONINIKLIJKE PHILIPS NV ; KONINKLIJKE PHILIPS ELECTRONICS N V ; PHILIPS ELECTRONICS KONINKLL NV ; PHILIPS ELECTRONICS N V KONINKLL ; PHILIPS KONINKLL NV ; PHILIPS LIGHTING HOLDING BV

Inventor(s): (std): BLACKWELL MICHAEL KEENAN ; JOHNSTON SCOTT ; KEENAN BLACKWELL MICHAEL ; SCOTT JOHNSTON ; JOHNSTON SCOTT D ; DZHONSTON SKOTT ; BLEHKUEHLL MAJKL KINAN

Inventor(s): JOHNSTON SCOTT BRIARCLIFF MANOR ; BLACKWELL MICHAEL KEENAN BRIARCLIFF MANOR

Agent(s): MEISSNER BOLTE AND PARTNER GBR ; BEKKERS JOOST J J ; BEKKERS JOOST JJ ; PHILIPS INTELLECTUAL PROPERTY AND STANDARDS; YULIYA ; DAMEN DANIEL M

Designated states: AE AG AL AM AO AT AU AZ BA BB BE BF BG BH BJ BR BW BY BZ CA CF CG CH CI CM CN CO CR CU CY CZ DE DK DM DO DZ EC EE EG ES FI FR GA GB GD GE GH GM GN GQ GR GT GW HN HR HU ID IE IL IN IS IT JP KE KG KM KN KP KR KZ LA LC LI LK LR LS LT LU LV LY MA MC MD ME MG MK ML MN MR MT MW MX MY MZ NA NE NG NI NL NO NZ OM PG PH PL PT RO RS RU SC SD SE SG SI SK SL SM SN ST SV SY SZ TD TG TJ TM TN TR TT TZ UA UG US UZ VC VN ZA ZM ZW

18) Family number: 30160540 (US2004004468A)

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Title: LDO REGULATOR WITH WIDE OUTPUT **LOAD** RANGE AND FAST INTERNAL LOOP

Abstract: A method and a circuit to achieve a low drop-out voltage regulator with a wide output **load** range has been achieved. A fast loop is introduced in the circuit. The circuit is internally compensated and uses a capacitor to ensure that the internal pole is more dominant than the output pole as in standard Miller compensation. The quiescent current is set being proportional to the output **load** current. No explicit low power drive stage is required. The whole output range is covered by one output drive stage. By that means the total consumption of quiescent or wasted current is reduced. An excellent PSRR is achieved due to **load** dependent bias current.

Classifications:

International (IPC 8-9): G05F1/56 G05F1/563

G05F1/575 (Advanced/Invention);

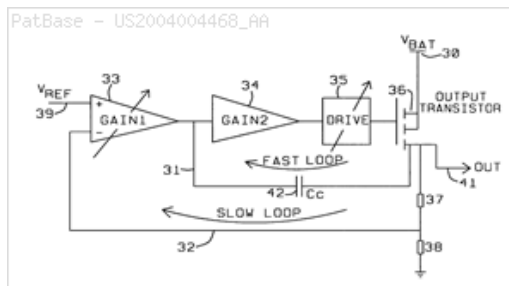
G05F1/10 (Core/Invention)

International (IPC 1-7): G05F1/40 G05F1/563 G05F1/575

CPC: G05F1/575

European: G05F1/575

US: 323/273 323/280



Family:

Publication number	Publication date	Application number	Application date
AT386969 E	20080315	AT20020368074T	20020705
DE60225124 D1	20080403	DE20026025124	20020705
DE60225124 D1	20080403	DE20026025124T	20020705
DE60225124 T2	20090219	DE20026025124T	20020705
DK1378808 T3	20080623	DK20020368074T	20020705
EP1378808 A1	20040107	EP20020368074	20020705
EP1378808 B1	20080220	EP20020368074	20020705
US2004004468 AA	20040108	US20020191849	20020709
US6856124 BB	20050215	US20020191849	20020709

Priority:

EP20020368074 20020705

Probable Assignee: DIALOG SEMICONDUCTOR GMBH

Assignee(s): (std): DIALOG SEMICONDUCTOR GMBH

Inventor(s): (std): DEARN DAVE ; DEARN DAVID ; MALCOLM JOHN ; MALCOLM JOHN STUART ; PANNWITZ AXEL

Inventor(s): JOHN STUART MALCOLM ; AXEL PANNWITZ ; DAVID DEARN

Agent(s): SCHUFFENECKER THIERRY; GEORGE O; STEPHEN B

Designated states: AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

19) Family number: 15901164 (US6297724B)

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Title: LIGHTING **CONTROL** SUBSYSTEM FOR USE IN SYSTEM ARCHITECTURE FOR AUTOMATED BUILDING

Abstract: An electrical power **control** system is adapted for **controlling** power and lighting in a building. Switch **controls** have either relays or triacs, **controlled** by microprocessors, to switch power on electrical branch circuits. An interface device 122 communicates with the switches 124,126, arranged in a multidrop configuration, with variable length data packets over serial branch circuits or data buses 142. In addition to being **controlled** by the interface device 122, the switch **controls** also have local switch actuators. The actuation of the local switch **controls** can be used by the interface device 122 as a **control** inputs to initiate other actions. Adaptive polling of switch **controls** is used to minimize system response time, and both transmit and receive signals are carried between the switch **controls** and the interface device 122 at different times on the same twisted pair wires. The interface device 122 can also communicate with a home automation system **controller**.

Classifications:

International (IPC 8-9): H05B37/02 (Advanced/Invention);
H05B37/02 (Core/Invention)

International (IPC 1-7): G06F15/46

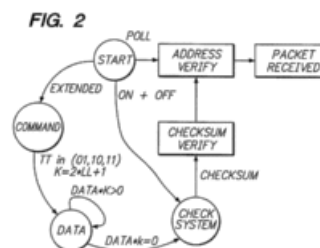
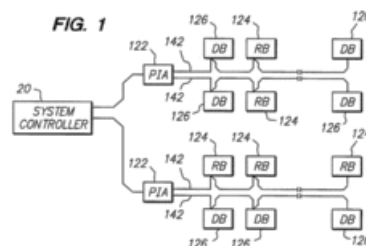
CPC: H05B47/18 Y10T307/461

European: H05B37/02B6D

US: 307/38 340/3.21 340/3.51 340/321 340/351

PatBase - US6297724_BA

U.S. Patent Oct. 2, 2001 Sheet 1 of 8 US 6,297,724 B1



Family:

Publication number	Publication date	Application number	Application date
US6297724 BA	20011002	US19960613828	19960307

Priority:

US19940303967 19940909 US19960613828 19960307

Probable Assignee: COMMScope TECHNOLOGIES LLC

Assignee(s): (std): WHITAKER CORP

Assignee(s): WHITAKER LLC ; COMMScope EMEA LTD ; COMMScope TECHNOLOGIES LLC

Inventor(s): (std): BRYANS DARRYL J ; JUMP LANCE B

20) Family number: 44412018 (JP2010027406A)

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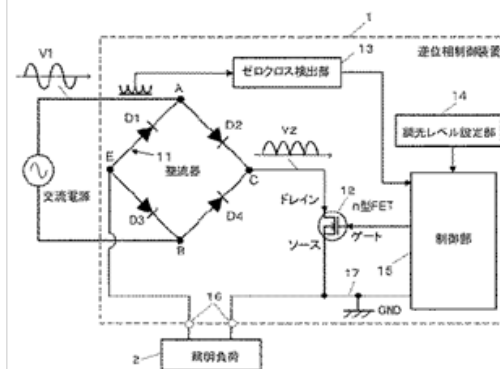
Title: ANTIPHASE **CONTROLLER**, AND ANTIPHASE ILLUMINATION **CONTROL** SYSTEM USING THE SAME

Abstract: PROBLEM TO BE SOLVED: To reduce the number of part items, to compactify a size and to reduce a cost, in an antiphase **controller** for illumination-**controlling** an illumination **load**, by antiphase-**controlling** an input voltage from an alternating current source. SOLUTION: This antiphase **controller** 1 is provided with a rectifier 11 for full-wave rectifying the alternating current voltage V1; an n-type FET 12 interposed between the rectifier 11 and the illumination **load** 2, and for **controlling** the continuity of a current flowing in the illumination **load** 2; a zero-cross detecting part 13 for detecting zero crossing of the full-wave rectified voltage; a dimming **control** level setting part 14 for setting a dimming **control** level of the illumination **load** 2; and a **control** part 15 for turning on and off the FET, at the timing of the zero cross, and for turning on and off the FET 12 at a fixed fringe angle, in response to the illumination **control** level. The illumination **load** 2 is thereby continuity-**controlled** in the antiphase **control** for the alternating current voltage V1 by the one switching element, and consequently, the number of part items is reduced to make the size small and to reduce the cost.

Classifications:

International (IPC 8-9): H05B37/02 (Advanced/Invention);
H05B37/02 (Core/Invention)

CPC: Y02B20/40

**Family:**

Publication number	Publication date	Application number	Application date
JP2010027406 A2	20100204	JP20080187920	20080718

Priority:

JP20080187920 20080718

Probable Assignee: PANASONIC ELEC WORKS CO LTD
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Assignee(s): PANASONIC ELECTRIC WORKS CO LTD
Inventor(s): (std): MURAKAMI YOSHINOBU ; TANIGAWA TETSUYA
Agent(s): ITAYA YASUO

21) Family number: 42102217 (US2008192512A)

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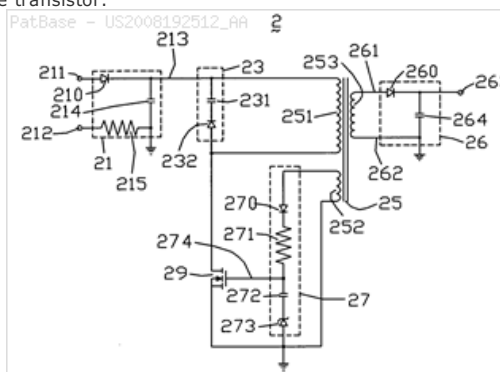
Title: DIRECT CURRENT TO DIRECT CURRENT CONVERTER

Abstract: An exemplary DC to DC converter (2) includes a transistor (29), a first rectifying and filtering circuit (21), a pulse generating circuit (27) and a transformer (25). The first rectifying and filtering circuit transforms an external AC voltage to a first DC voltage. The transformer includes a primary winding (251), an assistant winding (252) connected with the primary winding in a flyback mode, and a secondary winding (253). The first DC voltage is connected to ground via the primary winding, the transistor in series. The pulse generating circuit includes a **controlling** terminal, a diode, a resistor, and a capacitor. One terminal of the assistant winding is connected to ground, and the other terminal of the assistant winding is connected to an anode of the diode. A cathode of the diode is connected to ground via the resistor and the capacitor in series. The **controlling** terminal is connected to gate electrode of the transistor.

Classifications:**International (IPC 8-9):** H02M3/335 H02M3/338

H02M7/00 (Advanced/Invention);

H02M3/24 (Core/Invention)

CPC: H02M3/3381**European:** H02M3/338A**US:** 363/18 363/18P 363/21.12 363/21.16**Family:**

Publication number	Publication date	Application number	Application date
TW200835128 A	20080816	TW20070104977	20070212
TWI332309 B	20101021	TW20070104977	20070212
US2008192512 AA	20080814	US20080069921	20080212
US7848119 BB	20101207	US20080069921	20080212

Priority:

TW20070104977 20070212

Probable Assignee: INNOLUX CORP**Assignee(s):** (std): INNOLUX DISPLAY CORP ; INNOCOM TECH SHENZHEN CO LTD ; CHIMEI INNOLUX CORP

Assignee(s): INNOLUX CORP ; INNOCOM TECHNOLOGY SHENZHEN CO LTD ; INNOCOM TECHNOLOGY SHENZHEN CO ; INNOCOM TECHNOLOGY CO LTD ; INNOCOM TECHNOLOGY (SHENZHEN) CO LTD

Inventor(s): (std): ZHOU TONG ; ZHENG JIE JIAN ; LU JIAN HUI

Inventor(s): ZHENG, JIE-JIAN ; LU, JIAN-HUI

Agent(s): WEI TE CHUNG FOXCONN INT INC ; WEI TE

22) Family number: 48093133 (US2011082599A)

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Title: OPTIMIZING UTILITY USAGE BY SMART MONITORING

Abstract: A system for optimizing utility usage is described. The system comprises a monitoring device adapted to be connected to a utility point, a utility consuming device and a central processing unit (CPU). The monitoring device is configured to regulate utility usage information of the utility consuming device and is further configured to assign a unique identifier to the monitoring device. The monitoring device comprises a measurement circuit for measuring utility usage information, wherein the measurement circuit is coupled to the utility point. The monitoring device is configured to communicate with the CPU, wherein the monitoring device is configured to transmit utility usage information of the utility consuming device to the CPU on the unique identifier and wherein the monitoring device is configured to receive utility usage information of the utility consuming device and wherein the CPU is configured to process the utility usage information based on the unique identifier.

Classifications:

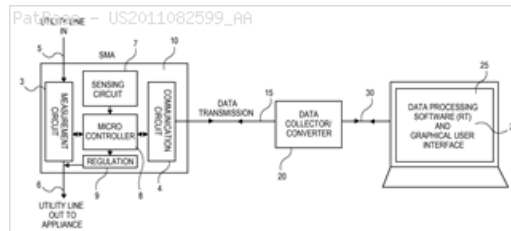
International (ipc 8-9): G01D4/00 G01R21/00 G06F1/28 G06F17/00 G06F19/00 (Advanced/Invention)

International (ipc 1-7): G01D4/00 G06F17/00

CPC: H02J13/0003 Y02B90/20 Y04S20/00

European: H02J13/00E Y02B90/22H Y04S20/18

US: 700/295 700/295P 700/90 700/90S 702/45 702/45S 702/62 702/62S



Family:

Publication number	Publication date	Application number	Application date
IN02777MU2010 A	20110107	IN2010MU02777	20101006
US2011082599 AA	20110407	US20100899488	20101006

Priority:

US20090249237P 20091006 US20100899488 20101006

Probable Assignee: BRISKO WILLIAM

Assignee(s): (std): BRISKO WILLIAM ; SHINDE MAKARAND ; SHINDE NITU

Assignee(s): MAKARAND SHINDE

Inventor(s): (std): BRISKO WILLIAM ; SHINDE MAKARAND ; SHINDE NITU

Inventor(s): WILLIAM BRISKO ; NITU M SHINDE ; MAKARAND SHINDE