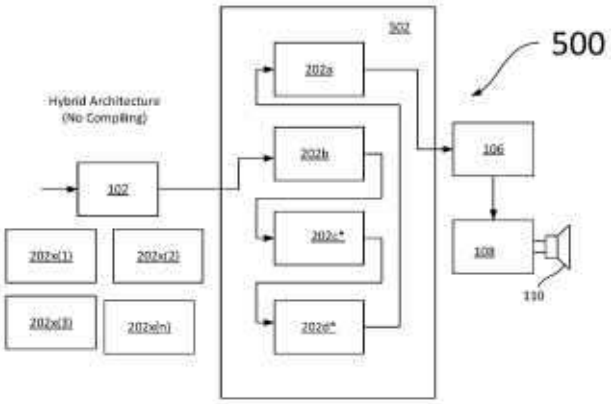
Welcome to EPOQUE Rebuild under the Production Environment

[EPODOC: SS 50] signal and processor and (hybrid 1w architecture)

Results in EPODOC **10**

[SS 51] ..li

1/10 © EPODOC / EPO



PN - US2020065054 A1 20200227

PR - US201916671688 20191101; US201715614006 20170605; US201662345326P 20160603

AP - US201916671688 20191101

DT - **

CCI - G06F3/162 ; G06F3/165

CI - G06F3/162 (EP, US)
- G06F3/165 (EP, US)

CA - H04R2420/01 (US)
- H04S2400/03 (US)

PA - CRESTRON ELECTRONICS INC [US]

TI - Audio Digital **Signal Processor** Utilizing a **Hybrid** Network **Architecture**

AB - A system and method executed by audio processing software on one or more electronic devices in a computer system to process digital audio signals. The system comprises a digitizer for digitizing a received audio **signal**; and **processor** for performing a plurality of audio processing functions on the digitized audio signals, each of the audio processing functions having at least one programmable

parameter, and wherein each of the audio processing functions are categorized and grouped as audio objects, and organized into a channel strip, the channel strip processing digitized audio signals for a particular received audio **signal**, and wherein, the audio objects are fixed in order, so that the digitized received audio signals are processed by a predefined number of N audio objects, and wherein the N audio objects occur in a fixed sequence, and further wherein, the N audio objects comprise a first subset of non-exchangeable audio objects and a second subset of exchangeable audio objects, such that any one or more of the second subset of audio objects can be exchanged by a replacement audio object, and further wherein when the audio processing functions are programmed, they can be saved without compiling the audio processing software.

2/10 © EPODOC / EPO

PN - **US2017352357** A1 20171207

PNFP - US10474419 B2 20191112

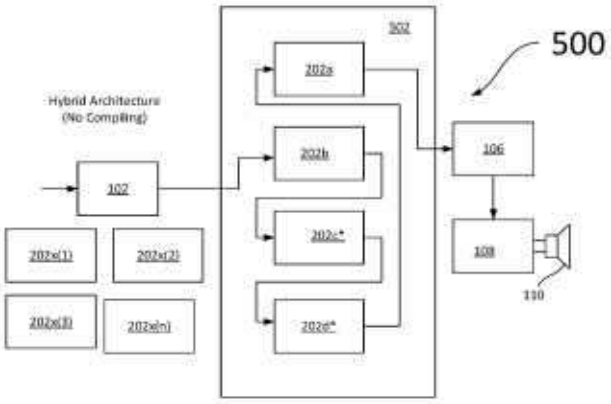
PR - US201715614006 20170605; US201662345326P 20160603

AP - **US201715614006** 20170605

DT - *; Rep

CT - *|| R141 (A1)*
US2009248183 A1 *20091001 [ ] (MIWA AKIHIRO [JP])*;
US2006015200 A1 *20060119 [ ] (TAKEMURA SATOSHI [JP], et al)*

- *|| STSE (A1)*
US2006015200 A1 *20060119 [*X*] (TAKEMURA SATOSHI [JP], et al) [X] 1*
** Claim 1 | para 27 | Fig. 4 | para 31 | para 61 | para 32 | para 59 | Fig. 4b | Fig. 4a | Figs. 2a | Fig. 6b | para 64 | para 101 | Fig. 8 | para 28*



*| para 37 | **;*
US2009248183 A1 *20091001 [A] (MIWA AKIHIRO [JP])*

- *|| STSE (B2)*
US2006015200 A1 *20060119 [ ] (TAKEMURA SATOSHI [JP], et al)*;
US2009248183 A1 *20091001 [ ] (MIWA AKIHIRO [JP])*

CCI
- **G06F3/162** ; **G06F3/165**

CI
- G06F3/162 (EP, US)
- G06F3/165 (EP, US)

CA
- H04R2420/01 (US)
- H04S2400/03 (US)

PA
- *(A1 B2)*
CRESTRON ELECTRONICS INC [US]

TI
- *(A1)*
AUDIO DIGITAL **SIGNAL PROCESSOR** UTILIZING A **HYBRID** NETWORK **ARCHITECTURE**
- *(B2)*
Audio digital **signal processor** utilizing a **hybrid** network **architecture**

AB
- *(A1 B2)*
A system and method executed by audio processing software on one or more electronic devices in a computer system to process digital audio signals. The system comprises a digitizer for digitizing a received audio **signal**; and **processor** for performing a plurality of audio processing functions on the digitized audio signals, each of the audio processing functions having at least one programmable parameter, and wherein each of the audio processing functions are categorized and grouped as audio objects, and organized into a channel strip, the channel strip processing digitized audio signals for a particular received audio **signal**, and wherein, the audio objects are fixed in order, so that the digitized received audio signals are processed by a predefined number of N audio objects, and wherein the N audio

objects occur in a fixed sequence, and further wherein, the N audio objects comprise a first subset of non-exchangeable audio objects and a second subset of exchangeable audio objects, such that any one or more of the second subset of audio objects can be exchanged by a replacement audio object, and further wherein when the audio processing functions are programmed, they can be saved without compiling the audio processing software.

3/10 © EPODOC / EPO

PN - **CN106525754** A 20170322

PR - CN20161153357 20161124

AP - **CN20161153357** 20161124

DT - I

CI - G01N21/01 (CN)
- G01N21/3554 (CN)

PA - ANHUI WANTUO AUTOMATION CO LTD

TI - Infrared moisture meter

AB - The invention provides an infrared moisture meter and particularly relates to the field of automatic equipment. The infrared moisture meter comprises an infrared detection head, a motor and a control box, wherein the control box is connected with the motor and the infrared detection head and provided with a data acquisition system, the data acquisition system adopts an FPGA (field programmable gate array) and DSP (digital **signal processor**) **hybrid architecture**, an FPGA and a DSP are connected, and an A/D converter and a DA chip are connected between the FPGA and the DSP in series. The infrared moisture meter is high in detection precision and high in operation speed, has a self-checking function and facilitates equipment overhaul and maintenance.

4/10 © EPODOC / EPO

PN - **CN104159089** A 20141119

PNFP - CN104159089B B 20170818

PR - CN201410447901 20140904

AP - **CN201410447901** 20140904

DT - I

CT - *|| STSE (A)*
CN102158691 A *00000000* [A]
[A] 1-2
** 全文 **;
CN102724478 A *00000000* [A]
[A] 1-2
** 全文 **

CTNP - *|| STSE (A)*

[X] - 朱海, "基于DM8168的视频监控系统的实现", 《中国优秀硕士学位论文全文数据库-信息科技辑》

[X] 1

** 第1.4、2.1-2.2、3.3、4.5节；图2-2、3-6 **

[A] - 郑亮, "基于DM8168多路视频监控系统研制", 《中国优秀硕士学位论文全文数据库-信息科技辑》

[A] 1-2

** 全文 **

[A] - 管庆等, "基于TMS320DM8168的视频监控跟踪系统", 《数据采集与处理》

[A] 1-2

** 全文 **

[A] - 杨振永等, "基于TMS320DM8168的SOC高清视频处理系统的设计与实现", 《液晶与显示》

[A] 1-2

** 全文 **

[A] - 周建平等, "基于DM8168的高清视频智能分析系统设计与实现", 《现代电子技术》

[A] 1-2

** 全文 **

[A] - 贾文全等, "基于TMS320DM8168硬件平台的智能网络视频监控系统", 《计算机与数字工程》

[A] 1-2

** 全文 **

PA - *(A)*

SICHUAN MIANYANG SOUTHWEST AUTOMATION INST

TI - *(A)*

Abnormal event alarm high-resolution video intelligent **processor**

AB - *(A)*

The invention provides an abnormal event alarm high-resolution video intelligent **processor**. An A/D (Analog to Digital) processing unit in the high-resolution video intelligent **processor** is accessed to perform digital processing on a high-resolution video, and outputting video streams to an intelligent video analysis unit DSP (Digital **Signal Processor**) ; and the intelligent video analysis unit DSP is used for outputting the video stream superimposed with analysis data to a H.246 video encoding unit for compressed encoding, and outputting target detection and feature extraction data to a central processing unit ARM (Advanced RISC Machine) for performing abnormal event management and judgment. Distributed **hybrid architecture** is adopted, and abnormal event modeling is performing on the basis of a business rule, so that the functions of acquisition, encoding, intelligent analysis, alarming, compression, network transmission and the like are realized on high-resolution video streams of 1080P and more than 1080P, and an integral solution is provided for intelligent analysis and processing of high-resolution video images.

PN - **CN103053118** A 20130417

PNFP - CN103053118B B 20150401

PR - WO2011US01092 20110617; US20100821260 20100623

AP - **CN20118038402** 20110617

DT - I

CT - *|| STSE (A)*
US2009221232 A1 *00000000 [YX] [Y] 1-20*
** 说明书第0018-0020段，图1 **
[X] 1-20;
CN1937427 A *00000000 [A] [A] 1-20*
** 全文 **;
EP0955751 A2 *00000000 [Y] [Y] 7,15*
** 说明书第0015段第40-45行，图1 **

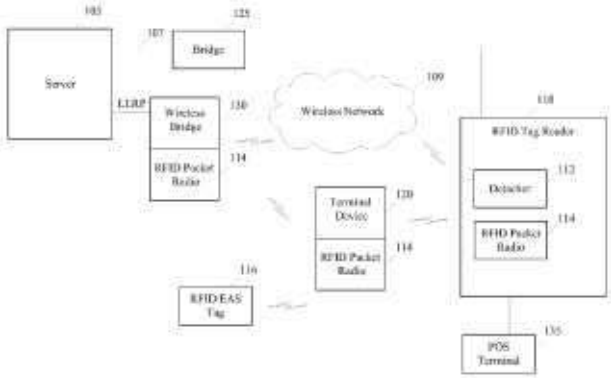
CCI - **G06K7/10297** ; **H04B1/10** ; **H04B1/3805**

CI - G06K7/10297 (EP, KR)
- H04B1/10 (EP, KR)
- H04B1/3805 (EP, KR)
- H04B1/401 (KR)
- H04B5/0062 (KR)
- H04B5/02 (KR)

PA - *(A B)*
SENSORMATIC ELECTRONICS LLC

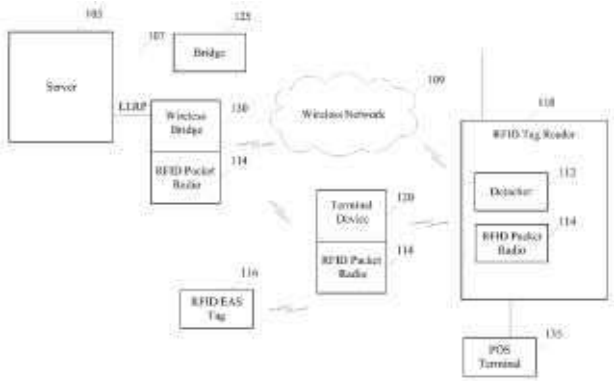
TI - *(A B)*
Hybrid architecture for radio frequency identification and packet radio communication

AB - *(A)*
A radio frequency identification (''RFID'') hybrid packet radio is provided having full duplex operability. The RFID packet radio includes an antenna and a radio that receives and transmits a packet radio **signal** and a RFID **signal**. A **processor** is provided in communication with the radio to perform **signal** detection and demodulation with respect to the RFID **signal**. A transmit path is provided to transport the packet radio **signal** and the RFID **signal** from the radio to the antenna. The RFID packet radio includes a first



receive path for transporting a received packet radio **signal** from the antenna to the radio and a second receive path, different from the first receive path, to transport a received RFID **signal** from the antenna to the **processor**. The first receive path and the second receive path enable simultaneous transmission and reception of RFID signals.

6/10 © EPODOC / EPO



PN - **CA2805752** A1 20111229

PNFP - CA2805752 C 20180828

PR - US20100821260 20100623; WO2011US01092 20110617

AP - **CA20112805752** 20110617

DT - I

CCI - **G06K7/10297** ; **H04B1/10** ; **H04B1/3805**

CI - G06K7/10297 (EP, KR)
- H04B1/10 (EP, KR)
- H04B1/3805 (EP, KR)
- H04B1/401 (KR)
- H04B5/0062 (KR)
- H04B5/02 (KR)

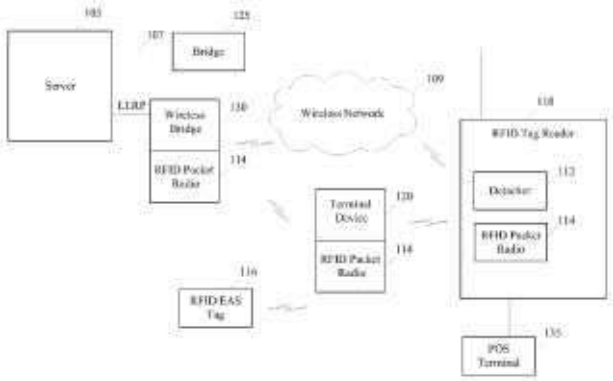
PA - *(A1 C)*
SENSORMATIC ELECTRONICS LLC [US]

TI - *(A1 C)*
HYBRID ARCHITECTURE FOR RADIO FREQUENCY IDENTIFICATION AND PACKET RADIO COMMUNICATION

AB - *(A1 C)*
A radio frequency identification ("RFID") hybrid packet radio is provided having full duplex operability. The RFID packet radio includes an antenna and a radio that receives and transmits a packet radio **signal** and a RFID **signal**. A **processor** is provided in communication with the radio to perform **signal** detection and demodulation with respect to the RFID **signal**. A

transmit path is provided to transport the packet radio **signal** and the RFID **signal** from the radio to the antenna. The RFID packet radio includes a first receive path for transporting a received packet radio **signal** from the antenna to the radio and a second receive path, different from the first receive path, to transport a received RFID **signal** from the antenna to the **processor**. The first receive path and the second receive path enable simultaneous transmission and reception of RFID signals.

7/10 © EPODOC / EPO



PN - **AU2011269828** A1 20130131

PNFP - AU2011269828B B2 20160609

PR - US20100821260 20100623; WO2011US01092 20110617

AP - **AU20110269828** 20110617

DT - I

CT - *|| STSE (B2)*
US2007205865 A1 *20070906 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)*;
US2009221232 A1 *20090903 [ ] (ESTEVEZ LEONARDO W [US], et al)*

CCI - **G06K7/10297** ; **H04B1/10** ; **H04B1/3805**

CI - G06K7/10297 (EP, KR)
- H04B1/10 (EP, KR)
- H04B1/3805 (EP, KR)
- H04B1/401 (KR)
- H04B5/0062 (KR)
- H04B5/02 (KR)

PA - *(A1)*
SENSORMATIC ELECTRONICS LLC
- *(B2)*
SENSORMATIC ELECTRONICS LLC [US]

TI - *(A1 B2)*
Hybrid architecture for radio frequency identification and packet radio communication

AB - *(A1 B2)*
A radio frequency identification ("RFID") hybrid packet radio is provided having full duplex operability. The RFID packet radio includes an antenna and a radio that receives and transmits a packet radio **signal** and a RFID **signal**. A **processor** is provided in communication with the radio to perform **signal** detection and demodulation with respect to the RFID **signal**. A transmit path is provided to transport the packet radio **signal** and the RFID **signal** from the radio to the antenna. The RFID packet radio includes a first receive path for transporting a received packet radio **signal** from the antenna to the radio and a second receive path, different from the first receive path, to transport a received RFID **signal** from the antenna to the **processor**. The first receive path and the second receive path enable simultaneous transmission and reception of RFID signals.

8/10 © EPODOC / EPO

PN - US2011317594 A1 20111229

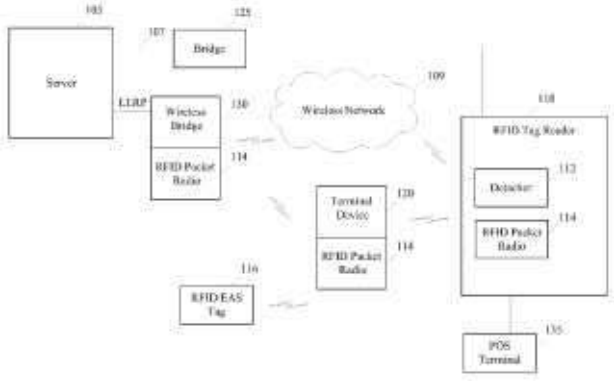
PNFP - US8610543 B2 20131217

PR - US20100821260 20100623

AP - US20100821260 20100623

DT - *; Rep

CT - *|| R141 (A1)*
US4672601 A *19870609 [ ] (ABLAY SEWIM F [US])*;
US2003025631 A1 *20030206 [ ] (KIM JONNATHAN H [US])* ;
US6560443 B1 *20030506 [ ] (VAISANEN ARI [FI], et al)*;
US2004137852 A1 *20040715 [ ] (SHI ZHONGMING [US], et al)*;
US6771612 B1 *20040803 [ ] (PARK SEONG-HYUN [KR])* ;
US2007046435 A1 *20070301 [ ] (FRATTI ROGER A [US], et al)*;



US2007126584 A1 *20070607 [ ] (HYDE JOHN D [US], et al)*;
US2008074282 A1 *20080327 [ ] (KOO SI-GYOUNG [KR], et al)*;
US2008232279 A1 *20080925 [ ] (MARHOLEV BOJKO [US])*;
US7420458 B1 *20080902 [ ] (KUZMA PETER J [US], et al)*;
US7532908 B2 *20090512 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)* ;
US2009156276 A1 *20090618 [ ] (ROFOUGARAN AHMADREZA [US])*;
US2009276338 A1 *20091105 [ ] (MASERMANN UTE [DE], et al)*;
US2009319677 A1 *20091224 [ ] (MASERMANN UTE [DE], et al)*;
US7688208 B2 *20100330 [ ] (SCHULER FRANCESCA [US], et al)*;
US7671720 B1 *20100302 [ ] (MARTIN ROBERT [US], et al)*;
US2010323635 A1 *20101223 [ ] (STEEPER TERRY [GB], et al)*;
US7878395 B2 *20110201 [ ] (ADAMS NEIL [CA], et al)*;
US7929934 B2 *20110419 [ ] (OISHI SADATOSHI [JP], et al)*;
US8019379 B2 *20110913 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)* ;
US2011234399 A1 *20110929 [ ] (YAN QIFENG [FI])*;
US2011309931 A1 *20111222 [ ] (ROSE MARK D [US])*;
US2011299444 A1 *20111208 [ ] (ROFOUGARAN AHMADREZA REZA [US])*;
US2012003938 A1 *20120105 [ ] (KAWAGUCHI DEAN [US], et al)*;
US2012165684 A1 *20120628 [ ] (SHOLDER JASON [US])*;
US8249502 B2 *20120821 [ ] (SAVRY OLIVIER [FR])* ;
US8275382 B2 *20120925 [ ] (LEWIS SCOTT W [US])*;
US2012249292 A1 *20121004 [ ] (WONG SHEK DUNCAN [HK], et al)*;
US2012268250 A1 *20121025 [ ] (KAUFMAN JEFFREY R [US], et al)*;
US2012276858 A1 *20121101 [ ] (WALLEY JOHN [US], et al)*;
US2005085237 A1 *20050421 [ ]*

(YANG BAOGUO [US], et al);
US2007205865 A1 *20070906 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)* ;
US2008280574 A1 *20081113 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)* ;
US2009028074 A1 *20090129 [ ] (KNOX MICHAEL E [US])*;
US2010142508 A1 *20100610 [ ] (WALLEY JOHN [US], et al)*;
US6078251 A *20000620 [ ] (LANDT JEREMY A [US], et al)*;
US7705711 B2 *20100427 [ ] (TAKEUCHI ISAO [JP])*

- *|| STSE (B2)*
US4672601 A *19870609 [ ] (ABLAY SEWIM F [US])*;
US6078251 A *20000620 [ ] (LANDT JEREMY A [US], et al)*;
US6560443 B1 *20030506 [ ] (VAISANEN ARI [FI], et al)*;
US6771612 B1 *20040803 [ ] (PARK SEONG-HYUN [KR])* ;
US7420458 B1 *20080902 [ ] (KUZMA PETER J [US], et al)*;
US7532908 B2 *20090512 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)* ;
US7671720 B1 *20100302 [ ] (MARTIN ROBERT [US], et al)*;
US7688208 B2 *20100330 [ ] (SCHULER FRANCESCA [US], et al)*;
US7705711 B2 *20100427 [ ] (TAKEUCHI ISAO [JP])* ;
US7878395 B2 *20110201 [ ] (ADAMS NEIL [CA], et al)*;
US7929934 B2 *20110419 [ ] (OISHI SADATOSHI [JP], et al)*;
US8019379 B2 *20110913 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)* ;
US8249502 B2 *20120821 [ ] (SAVRY OLIVIER [FR])* ;
US8275382 B2 *20120925 [ ] (LEWIS SCOTT W [US])*;
US2003025631 A1 *20030206 [ ] (KIM JONNATHAN H [US])* ;
US2004137852 A1 *20040715 [ ] (SHI ZHONGMING [US], et al)*;
US2005085237 A1 *20050421 [ ] (YANG BAOGUO [US], et al)*;

US2007046435 A1 *20070301 [ ] (FRATTI ROGER A [US], et al)*;
US2007126584 A1 *20070607 [ ] (HYDE JOHN D [US], et al)*;
US2007205865 A1 *20070906 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)* ;
US2008074282 A1 *20080327 [ ] (KOO SI-GYOUNG [KR], et al)*;
US2008232279 A1 *20080925 [ ] (MARHOLEV BOJKO [US])*;
US2008280574 A1 *20081113 [ ] (ROFOUGARAN AHMADREZA REZA [US], et al)* ;
US2009028074 A1 *20090129 [ ] (KNOX MICHAEL E [US])*;
US2009156276 A1 *20090618 [ ] (ROFOUGARAN AHMADREZA [US])*;
US2009276338 A1 *20091105 [ ] (MASERMANN UTE [DE], et al)*;
US2009319677 A1 *20091224 [ ] (MASERMANN UTE [DE], et al)*;
US2010142508 A1 *20100610 [ ] (WALLEY JOHN [US], et al)*;
US2010323635 A1 *20101223 [ ] (STEEPER TERRY [GB], et al)*;
US2011234399 A1 *20110929 [ ] (YAN QIFENG [FI])*;
US2011299444 A1 *20111208 [ ] (ROFOUGARAN AHMADREZA REZA [US])*;
US2011309931 A1 *20111222 [ ] (ROSE MARK D [US])*;
US2012003938 A1 *20120105 [ ] (KAWAGUCHI DEAN [US], et al)*;
US2012165684 A1 *20120628 [ ] (SHOLDER JASON [US])*;
US2012249292 A1 *20121004 [ ] (WONG SHEK DUNCAN [HK], et al)*;
US2012268250 A1 *20121025 [ ] (KAUFMAN JEFFREY R [US], et al)*;
US2012276858 A1 *20121101 [ ] (WALLEY JOHN [US], et al)*

CCI - **G06K7/10297** ; **H04B1/10** ; **H04B1/3805**

CI
- G06K7/10297 (EP, KR)
- H04B1/10 (EP, KR)
- H04B1/3805 (EP, KR)
- H04B1/401 (KR)
- H04B5/0062 (KR)
- H04B5/02 (KR)

PA
- *(A1)*
ALICOT JORGE F [US]; MOHIUDDIN MOHAMMAD [US]; RELIHAN TIMOTHY J [US] ; SENSORMATIC ELECTRONICS LLC [US]
- *(B2)*
ALICOT JORGE F [US]; MOHIUDDIN MOHAMMAD [US]; RELIHAN TIMOTHY J [US] ; TYCO FIRE & SECURITY GMBH [CH]

TI
- *(A1)*
HYBRID ARCHITECTURE FOR RADIO FREQUENCY IDENTIFICATION AND PACKET RADIO COMMUNICATION
- *(B2)*
Hybrid architecture for radio frequency identification and packet radio communication

AB
- *(A1)*
A radio frequency identification (“RFID”) hybrid packet radio is provided having full duplex operability. The RFID packet radio includes an antenna and a radio that receives and transmits a packet radio **signal** and a RFID **signal**. A **processor** is provided in communication with the radio to perform **signal** detection and demodulation with respect to the RFID **signal**. A transmit path is provided to transport the packet radio **signal** and the RFID **signal** from the radio to the antenna. The RFID packet radio includes a first receive path for transporting a received packet radio **signal** from the antenna to the radio and a second receive path, different from the first receive path, to transport a received RFID **signal** from the antenna to the **processor**. The first receive path and the second receive path enable simultaneous transmission and reception of RFID signals.
- *(B2)*
A radio frequency identification ("RFID") hybrid packet radio is

provided having full duplex operability. The RFID packet radio includes an antenna and a radio that receives and transmits a packet radio **signal** and a RFID **signal**. A **processor** is provided in communication with the radio to perform **signal** detection and demodulation with respect to the RFID **signal**. A transmit path is provided to transport the packet radio **signal** and the RFID **signal** from the radio to the antenna. The RFID packet radio includes a first receive path for transporting a received packet radio **signal** from the antenna to the radio and a second receive path, different from the first receive path, to transport a received RFID **signal** from the antenna to the **processor**. The first receive path and the second receive path enable simultaneous transmission and reception of RFID signals.

9/10 © EPODOC / EPO

PN - **TW200721876** A 20070601

PNFP - TWI295898B B 20080411

PR - TW20050140256 20051116

AP - **TW20050140256** 20051116

DT - I

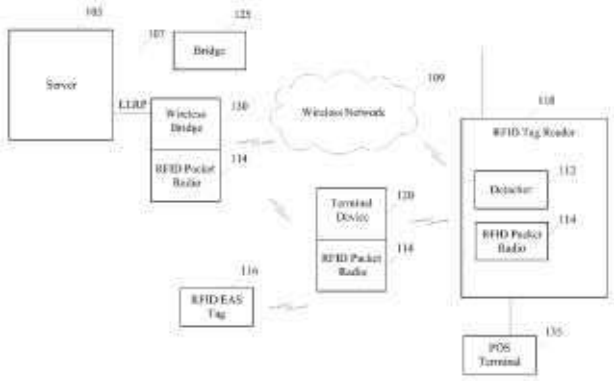
PA - *(A B)*
UNIV NAT CHIAO TUNG [TW]

TI - *(A)*
Sensor-free bass compensation system

AB - *(A)*
A sensor-free bass compensation system includes a bi-layer **hybrid** control **architecture** having a feed-forward control device and a feedback control device as well as a volume velocity estimation device of acoustic source. The volume velocity estimation device of acoustic source employs a dynamic equation and the linear quadratic Gaussian theorem to obtain a sensor-free volume velocity estimation device of acoustic source. The feed-forward control device is mounted on a digital **signal processor**, and a feedback control device is designed with the quantitative feedback technique and is mounted on an analog circuit for enhancing the robust stability while a speaker is operating. Besides, the system of the present invention possesses the capability of automatic modeling and synthetic filter, thereby achieving the reduction of operating frequency of speaker, correct low-frequency loudness in different room and the suppression of

parametric drift arising from long working hours.

10/10 © EPODOC / EPO



PN - **WO2011162800** A1 20111229

PR - US20100821260 20100623

AP - **WO2011US01092** 20110617

DT - **

CT - *|EP| ISR (A1)*
US2009221232 A1 *00000000 [*XI*]*
(ESTEVEZ LEONARDO W [US] ET AL)
[X] 1,3,4,7,9,11,12,15,17-20
** abstract **
** figures 1-4 **
** paragraph [0021] - paragraph [0027] **
** paragraph [0028] **
** paragraph [0018] - paragraph [0020] **
** claims 1-19 **
[I] 2,5,6,8,10,13,14,16;
XP002659867 A*[A]*;
US2007066278 A1 *00000000 [*A*]*
(HONG SEOK JIN [KR])
[A] 1-20
** the whole document **;
EP1545015 A2 *00000000 [*A*]*
(SAMSUNG ELECTRONICS CO LTD [KR])
[A] 1-20
** the whole document **

CTNP - *|EP| ISR (A1)*
[A] - Wikipedia, "Packet radio", (20100516), URL: **http://en.wikipedia.org/w/index.php?title=Packet_radio&oldid=362412027**, (20110923), XP002659867
[A] 1-20
** the whole document **

CCI - **G06K7/10297** ; **H04B1/10** ; **H04B1/3805**

CI - G06K7/10297 (EP, KR)
- H04B1/10 (EP, KR)
- H04B1/3805 (EP, KR)
- H04B1/401 (KR)
- H04B5/0062 (KR)
- H04B5/02 (KR)

PA - SENSORMATIC ELECTRONICS LLC [US]

TI - **HYBRID ARCHITECTURE** FOR RADIO

FREQUENCY IDENTIFICATION AND PACKET RADIO COMMUNICATION

AB - A radio frequency identification ("RFID") hybrid packet radio is provided having full duplex operability. The RFID packet radio includes an antenna and a radio that receives and transmits a packet radio **signal** and a RFID **signal**. A **processor** is provided in communication with the radio to perform **signal** detection and demodulation with respect to the RFID **signal**. A transmit path is provided to transport the packet radio **signal** and the RFID **signal** from the radio to the antenna. The RFID packet radio includes a first receive path for transporting a received packet radio **signal** from the antenna to the radio and a second receive path, different from the first receive path, to transport a received RFID **signal** from the antenna to the **processor**. The first receive path and the second receive path enable simultaneous transmission and reception of RFID signals.

[SS 51]